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Instruction Manual

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Model

1952 B

Universal Counter-Timer

P/N 390054

DECEMBER 1975

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Section 1
Introduction & Specifications

1-1 INTRODUCTION

1-2 The Fluke Model 1952B is a universal, DC to 80 MHz, counter/timer capable of making frequency, frequency ratio, period, time interval, gateable totalize, and self check measurements. Each measurement mode and one of six associated ranges are individually selectable using the front panel FUNCTION and RANGE switches.

1-3 Front panel features include individual trigger level adjustments for the CHANNEL A and B input signals, switch selection of a continuous or single measurement cycle, and a seven digit display.

1-4 A self check mode provides a convenient method of verifying the overall operation of the 1952B. In this mode, the counter is configured such that a frequency measurement is made using the 100 kHz output of the internal frequency standard as an unknown input. A valid readout for each setting of the range switch verifies proper overall operation.

1-5 The trigger level controls allow the operator to manually define the conditions under which the individual CHANNEL A and B inputs will accept an input signal as a trigger. Switches are used to select x1 or x10 attenuation, ac or dc coupling, and + or - slope triggering. A continuously variable trigger level control is used to set the + or - dc threshold voltage which the input signal must exceed before being accepted as an input signal. A PRESET position on the trigger level adjustment sets the threshold to 0 V dc. Two light emitting diode (LED) Status Indicators operate in conjunction with the variable trigger level to simplify the task of establishing proper triggering.

1-6 A measurement can be continuously repeated at an adjustable rate, or a front panel manual RESET command can be selected to initiate each measurement cycle.

1-7 The front panel display on the standard instrument consists of a seven digit readout plus six range annunciators (measurement units indicators). The display is equipped with leading zero suppression for all decades except the least significant decade (LSD). Decimal point location, if any, is fixed according to the selected FUNCTION and RANGE, and is supplemented, when necessary,

by the illumination of one or more of the measurement units indicators.

1-8 A factory installed option (-03) is available which provides a temperature compensated crystal oscillator (TCXO) and an eight digit display for higher measurement accuracy and resolution.

1-9 The model 1952B is designed as a bench-top instrument. However, by installing the Accessory Rack Mounting Kit (part no. M00-200-614) the 1952B can be mounted in a standard 19 inch equipment rack. Input power requirements are 115 or 230 V ac ± 10%, 50 to 400 Hz, 20 Watts.

TABLE 1-1 - AVAILABLE OPTION

OPTION	DESCRIPTION	INSTALLATION	
		FACTORY	FIELD
-03	-03 TEMPERATURE COMPENSATED CRYSTAL OSCILLATOR (TCXO) AND 8 DIGIT DISPLAY	X	-

1-10 SPECIFICATIONS

Operating Modes

FREQUENCY

Range.....	DC to 80 MHz (dc coupled) 5 Hz to 80 MHz (ac coupled)
Gate Time.....	0.1 ms to 10 s in 6 decade steps
Resolution.....	0.1 Hz at 10 s gate time to 10 kHz at 0.1 ms gate
Accuracy.....	± 1 count ± timebase accuracy
Readout.....	kHz or MHz automatically displayed with decimal point

RATIO

Displays.....	f_1/f_2 where f_1 and f_2 are applied at the two input channels, A and B
Range f_1.....	DC to 80 MHz (dc coupled) 5 Hz to 80 MHz (ac coupled)
f_2.....	1 kHz to 10 MHz
Accuracy.....	± 1 count of signal on input A. ± trigger error of signal on input B*

PERIOD

Range.....	DC to 10 MHz (dc coupled) 5 Hz to 10 MHz (ac coupled)
Periods averaged.....	1 period to 10^5 periods
Frequency counted.....	100 kHz
Resolution.....	0.1 ns at 10^5 periods to 10 us at 1 period
Accuracy.....	± 1 count ± timebase accuracy, ± trigger error of signal on input A*
Readout.....	ms or us automatically displayed with decimal point

TIME INTERVAL

Range.....	0.1 us to 10^7 s (with 7 digit display) 0.1 us to 10^8 s (with 8 digit display)
Input.....	Channels A and B; common or separate
Resolution.....	0.1 us to 10 ms in 6 decade steps
Accuracy.....	± 1 count ± timebase accuracy ± trigger error**

* Trigger error is less than ± 0.3% - periods averaged for signals with better than 40 dB S/N ratio and greater than 100 mV rms amplitude.

** Trigger error is less than ± 0.0025 V/Signal Slope (V/us) in us.

TOTALIZING

Range.....	DC to 80 MHz (dc coupled)
Capacity.....	10^7 standard, up to 10^8 with 8 digit display
SELF CHECK.....	A timebase-derived 100 kHz signal is internally connected to the Channel A preamplifier output.

Input Signals

SENSITIVITY

Input A or B.....	Sinewave: 50 mV rms from dc to 50 MHz, decreasing to 75 mV rms at 80 MHz Pulse: 100 mV pulse amplitude with minimum pulse width of 20 ns.

IMPEDANCE

Input A or B.....	1 M ohm in parallel with 30 pF

ATTENUATOR

Input A and B only..... Sensitivity is decreased by a factor of 10 in the X10 position.

TRIGGER SLOPE

Input A and B only..... Front panel slide switch selects positive or negative slope triggering.

TRIGGER LEVEL

Input A and B only..... Front panel control has ± 1 V range when attenuator is in X1 position and ± 10 V in the X10 positions. Preset position is fully counter-clockwise.

OVERLOAD CAPABILITY

Input A or B..... 150 V rms to 1 kHz (decreasing to 5 V rms at 80 MHz)
DC + Peak ac must not exceed 150 V

Output Signal

Marker Output..... A TTL Compatible (5 Volt) output is available for the duration of a time interval measurement at the rear panel BNC connector.

Time Base

INTERNAL

	Standard	Option -03
Frequency.....	10.00 MHz	10.00 MHz
Aging Rate.....	$< \pm 5 \times 10^{-7}$/mo	$< \pm 3 \times 10^{-7}$/mo < 0.7 PARTS IN 10^7 PER WEEK
Temperature Stability		
20°C to 30°C.....	$< \pm 2 \times 10^{-6}$ typical	$\pm 5 \times 10^{-7}$ typical
0°C to 50°C.....	$< \pm 5 \times 10^{-6}$	$< \pm 2 \times 10^{-6}$
Line Voltage		
For ± 10% change.....	$< \pm 1 \times 10^{-7}$	$< \pm 1 \times 10^{-7}$

EXTERNAL

Frequency Required.....	10 MHz
Sensitivity.....	100 mV minimum
Impedance.....	1 M ohm, 15 pF

General

DISPLAY.....	7 digit LED display incorporating 0.27" high, seven segment characters. Up to 8 digits optionally available. Full leading zero suppression.
CYCLE RATE.....	When in the "CONT" mode, the time interval between successive measurements can be varied between 0.2 and 2.0 seconds by means of a CYCLE RATE control. RESET button clears display and activates a new measurement.
RESET.....	In "TRIG" mode the readings may be updated by pushing the RESET button.
POWER REQUIREMENTS.....	50, 60, 400 Hz; 115/230 V ± 10% (100 V operation available) 25 Watts
DIMENSION.....	2.94" high, 12.96" wide and 9.95" deep. (7.5 cm X 32.9 cm X 25.3 cm) See Figure 1-1.
WEIGHT.....	8 pounds (3.63 kg)

Environmental

STORAGE TEMPERATURE.....	-40°C to + 75°C
OPERATING TEMPERATURE.....	0°C to + 50°C

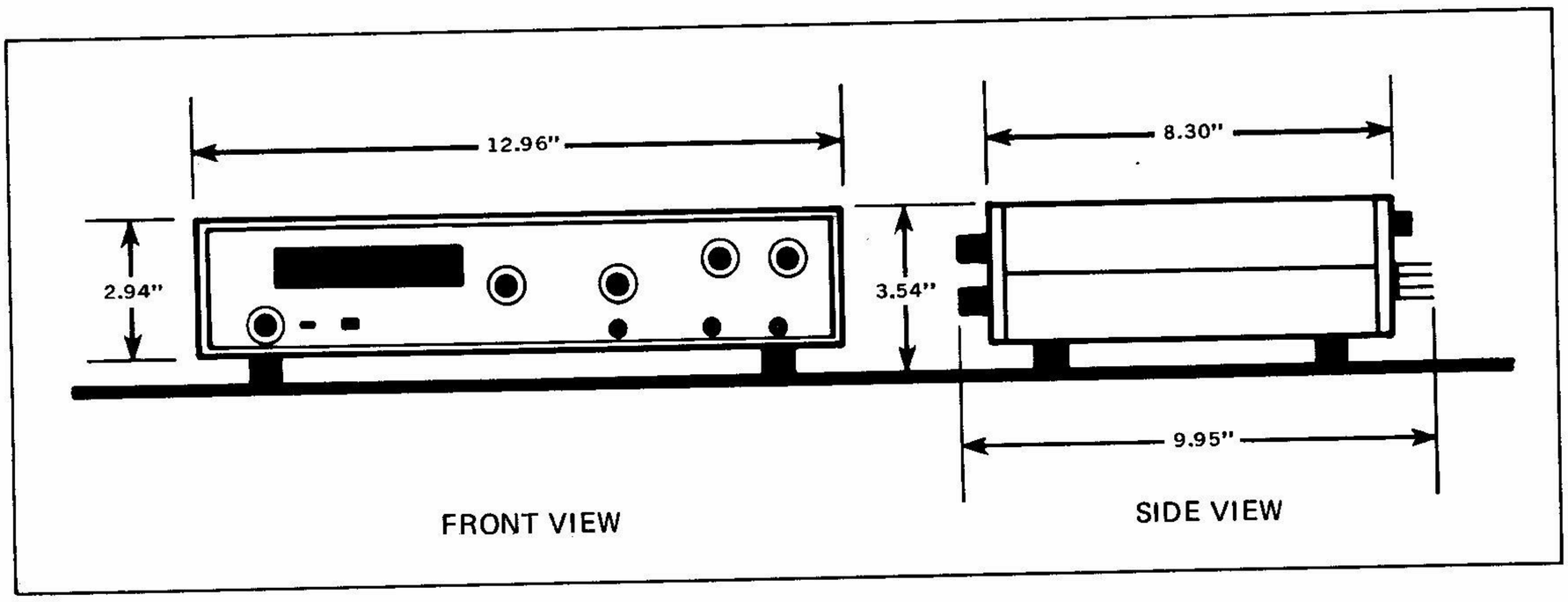


Figure 1-1. OUTLINE DRAWING

Section 2
Operating Instructions

2-1 INTRODUCTION

2-2 This section of the manual contains information regarding installation and operation of the Model 1952B Counter. It is recommended that the contents of this section be read and understood before any attempt is made to operate the counter.

2-3 SHIPPING INFORMATION

2-4 The Model 1952B is packaged and shipped in a shock resistant container. Upon receipt of the equipment, a thorough inspection should be made to reveal any possible shipping damage. Special instructions for inspection and claims are included in the shipping carton.

2-5 If reshipment of the equipment is necessary, the original container should be used. If the original container is not available, a new container can be obtained from the John Fluke Mfg. Co., Ltd. Please reference the equipment model number when requesting a new shipping container.

2-6 INPUT POWER

2-7 The Model 1952B can be operated from a 115 or 230 V ac, 48 to 440 Hz power line (A 100/200 V ac version is available on request). Before connecting the equipment to primary power, check and, if necessary, set the instrument to operate at the local line voltage as follows:

a. Locate the input power selection switch on the rear panel of the 1952B.

b. Set the slide switch so that the desired voltage, 115 or 230, appears in the slide aperture.

c. Install the proper fuse (i.e. 1A slo-blo for 115 V ac and 0.5 A slo-blo for 230 V ac) in the rear panel fuse holder.

2-8 RACK INSTALLATION

2-9 The 1952B is designed for bench-top use or for installation in a standard 19 inch

equipment rack using the optional Accessory Rack Mounting Kit (part number M00-200-614).

2-10 OPERATING FEATURES

2-11 The function and location of all front panel controls, indicators and connectors are shown in Figure 2-1 and all rear panel controls, indicators and connectors are shown in Figure 2-2.

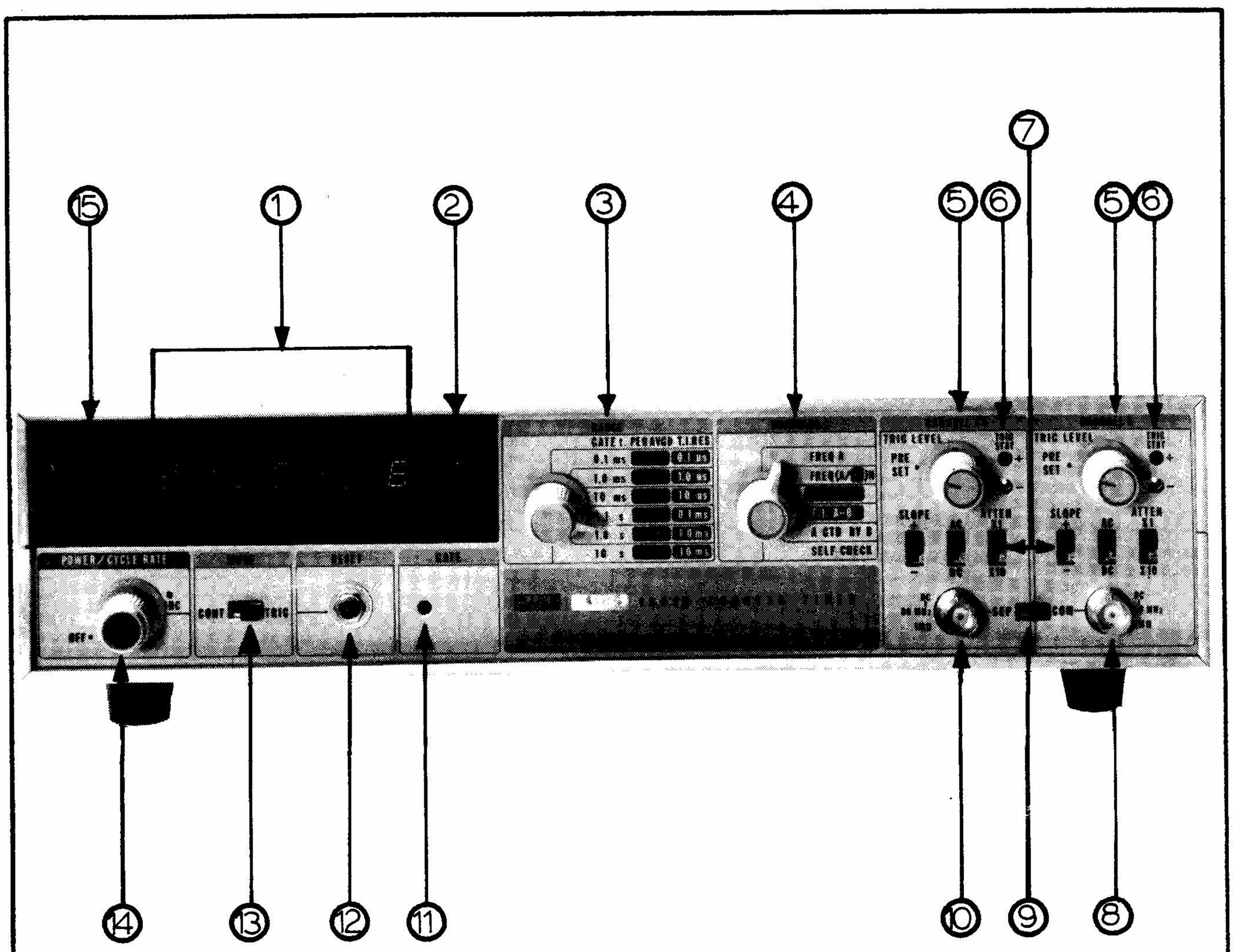


Figure 2-1 . FRONT PANEL CONTROLS , INDICATORS AND CONNECTORS .
(See pages 2-3,2-4 and 2-5 for ref. numbers)

REF. NO.	NAME	FUNCTION
1	Digital Display	Displays a seven digit readout of the counter measurement. The position of the RANGE and FUNCTION switches determine the location of the decimal point on the display. All decades are capable of displaying from 0 through 9.
2	Measurement Units	Displays a frequency or time multiplier necessary to properly define the value of the measurement readout. The multiplier displayed depends on the position of the RANGE and FUNCTION switches.
3	RANGE Switch	Selects one of six ranges for the selected operating function. GATE t..... Gate time ranges for frequency modes. PER.AVGD..... Number of periods averaged in PERIOD A mode. T.I, RES..... Resolution for T.I, A-B mode.
4	FUNCTION Switch	Selects the desired operating mode. FREQ. A..... Measures frequency of the CHANNEL A input signal. FREQ.(A/B)N..... Measures the frequency ratio of the CHANNEL A and B inputs. PERIOD A..... Measures the period of the CHANNEL A input signal. The number of periods averaged (10^0 through 10^5) are selected by the setting of the RANGE switch. T.I. A-B..... Measures the time interval between a start input signal at CHANNEL A and a stop input signal at CHANNEL B. A GTD. BY B..... Measures the number of events which occur on the CHANNEL A input when an enable signal is applied to the CHANNEL B input. The RANGE switch is not effective in this mode. SELF CHECK..... All inputs are disabled and an internally generated 100 kHz signal is counted and displayed.
5	Trigger Level Adjust	Sets the dc trigger level for the CHANNEL A/B input signal. With the control set to the PRESET position (maximum ccw) the trigger level is set to zero volts. Switched out of the detent position, the control can be set to cause triggering at positive or negative threshold voltages.

REF NO.	NAME	FUNCTION
6	Status Indicators + and - (CHANNEL A/B)	Two light emitting diodes (LED's) which illuminate on a mutually exclusive basis to indicate that the input signal is above or below the set trigger level. The illumination pattern is alternately reversed as the input signal passes above and below the trigger level threshold.
7	CHANNEL A and B Input Signal Conditioners	Slide switches which select the trigger Slope, Coupling and Attenuation for the respective CHANNEL A and B input signals. Slope..... (+ or -) Selects the leading or trailing edge slopes of the input signal for use in triggering the counter. Coupling..... Selects either direct DC coupling or capacitive AC coupling for the input signal. Attenuators..... (x1 or x10) Selects x1 or x10 attenuation of the input signal. In the x10 position the input sensitivity is reduced by a factor of 10.
8	CHANNEL B Input Connector	A BNC connector used to connect the CHANNEL B input signal to the counter.
9	SEP - COM Switch	A slide switch which electrically isolates or separates the CHANNEL A and B input signals in the SEP position, and electrically connects the inputs in the COM position.
10	CHANNEL A Input Connector	A BNC connector used to connect the CHANNEL A input signal to the counter.
11	GATE Indicator	An LED which illuminates to indicate that the counter is executing a measurement cycle.
12	RESET Switch	Momentary depression of the button resets the display to zero and initiates a new measurement.
13	CONT-TRIG Switch	Selects the internal (CONT) or external (TRIG) trigger method for initiating a new measurement. CONT..... Measurements are made on a continuous basis, one after another, separated by time delays dependent on the setting of the CYCLE RATE control. TRIG..... Measurements are initiated by a RESET pulse

		generated by momentarily depressing the RESET button.
14	CYCLE RATE	Determines the amount of time between successive measurements when the CONT - TRIG switch is in the CONT position. Also turns off the entire instrument in the CCW extreme (POWER OFF).
15	OVERFLOW Indicator	Illuminates to indicate that the counter has exceeded its total display capacity (overflowed).

2-12 OPERATING NOTES

2-13 The following paragraphs describe various conditions which should be considered before operating the Model 1952B Counter.

2-14 AC Line Connection

2-15 The input power cord is a three-pin polarized connector which permits connection to any of the power line voltages described in paragraph 2-6. Ensure that the offset pin is connected to a high quality earth ground. The 1952B Counter is energized by means of the front panel POWER/CYCLE RATE switch.

2-16 A and B Inputs

2-17 Input channels A and B are functionally similar. With the attenuators in the X1 position, the input impedance of each channel is 1 M ohm in parallel with 30 pF for signals less than 1 volt peak-to-peak, and decreases to 10 k ohm for signals that exceed 1 volt peak-to-peak. With the attenuators in the X10 position, the input resistance is 1 M ohm for all input signal levels.

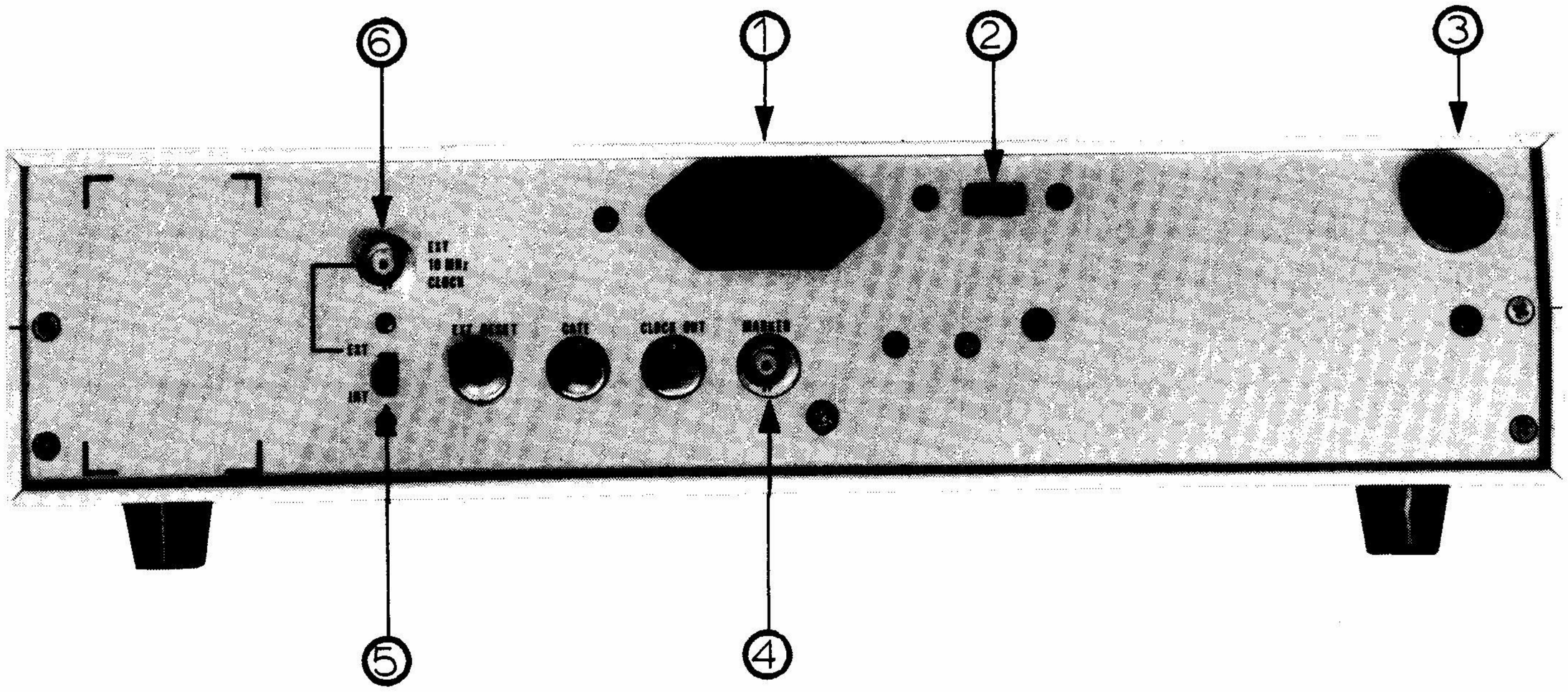
2-18 SEP - COM Switch

2-19 The SEP-COM (separate-common) switch is used to isolate the A and B inputs when the switch is in the SEP position, or to electrically connect the two inputs in the COM position. The switch is normally left in SEP position except for time interval measurements using a single input signal. With the switch in the COM position, CHANNEL A is used to establish the start trigger level conditions and the CHANNEL B is used to establish the stop trigger level conditions.

CAUTION!

If separate signal sources are to be connected to the A and B input, the SEP-COM switch should be set to the SEP position. Otherwise the outputs of the signal sources will be shorted together.

Figure 2-2. REAR PANEL CONTROLS , INDICATORS AND CONNECTORS .



REF NO.	NAME	FUNCTION
1	Power Input Connector	Provides the means of connecting the instrument through the power cord to line power.
2	115/230 Power Select Switch	Provides rear panel selection of 115 or 230 V ac input power.
3	FUSE	Protects the ac input section of the power supply.
4	MARKER Connector	Provides for external connection to the Marker output signal. This signal is used to modulate the Z axis on an oscilloscope while displaying the input signal. The result is an intensified zone which displays the start to stop interval for a time interval measurement.
5	Clock Select Switch	Selects the internal or external clock for use as the counter time base. INT..... Selects the internal 10 MHz clock. EXT..... Selects the external clock input signal connected to the EXT 10 MHz clock connector.
6	EXT 10 MHz Connector	Provides for the connection of an external 10 MHz clock input signal. The minimum input level requirement is 150 mV and the input impedance is 1 M ohm.

2-20 Trigger Levels

2-21 The front panel trigger level controls provided for each CHANNEL A and B input consist of three slide switches, two LED Status Indicators and a manual Trigger Level Adjust. The slide switches condition the input signal before it is used to trigger the counter by selecting the desired trigger slope (+ or -), coupling (AC or DC) and attenuation (x1 or x10). The Trigger Level Adjust is then used to set the dc threshold of an internal trigger circuit and thereby define the trigger level for the channel input signal. The range of the trigger level control is continuously variable from + 1 to - 1 V dc with the attenuator switch in the x1 position. Selecting the x10 attenuator increases the trigger level voltage range by a factor of 10. When the trigger level control is turned CCW to the PRESET detent, the trigger level is set to 0 V dc. The two Status Indicators provide a convenient method of determining the effective operating range of the trigger level control for a given input signal. Assuming a sinewave input is used, both LED's are illuminated to an equal intensity when the trigger level control is adjusted to trigger at 0 V dc. For non-symmetrical input signals triggering occurs when both LED's are illuminated regardless of intensity. Very narrow low frequency pulses may not illuminate the Status Indicators even though proper triggering occurs.

2-22 MARKER Output

2-23 In the Time Interval mode, a pulse is generated at the rear panel MARKER output connector to provide an indication of the actual time interval being measured. The width of the MARKER pulse is variable depending on the time interval of the signal being measured and the trigger level settings for CHANNELS A and B (Start and Stop). When the measured input signal is fed to the vertical input of an oscilloscope, the MARKER pulse can be used to modulate the Z axis of the scope and cause the measured portion of the input signal to be intensified. Using this technique, the thresholds for the A and B (Start and Stop) Trigger Levels can be individually adjusted for precise time interval measurements.

2-24 In the Frequency A mode, the Marker pulse can be used to precisely set the A trigger level. However, the Channel B Trigger Level controls must also be adjusted to reset the Marker generator. This is accomplished by setting the SEP - COM switch to the COM position and adjusting the B Trigger Level so that the intensified zone is terminated shortly after it begins. Adjust the Channel A Trigger Level to the desired position.

2-25 OPERATION

2-26 Turn-On Procedure

2-27 Use of the following procedure is suggested for initial turn-on of the 1952B Counter:

a. Connect the counter to the ac power line. (See paragraph 2-6).

b. Turn the POWER/CYCLE RATE switch clockwise and out of the power OFF detent. The front panel display should illuminate.

2-28 Operating Modes

2-29 The following paragraphs describe the self check, frequency, ratio, period, time interval and totalize functions and the procedures necessary to operate the 1952B in each mode.

2-30 SELF CHECK

2-31 The SELF CHECK function is used to verify the overall operation of Model 1952B. The Frequency of a 100 kHz clock, derived from the 10 MHz oscillator, is measured to produce a predictable readout for each setting of the GATE t switch.

2-32 Use the following procedure to operate the Model 1952B in the SELF CHECK mode:

a. Turn the CYCLE RATE switch clockwise out of the power OFF detent.

b. Set the rear panel Clock Select Switch to the INT (internal) position.

c. Set the FUNCTION switch to the SELF CHECK position.

d. Set the MODE switch to the CONT position.

e. Set the CYCLE RATE control for the desired display period.

f. Refer to Table 2-1 and verify the display readout for each GATE t setting of the RANGE switch.

Table 2-1 DISPLAY READOUT FOR SELF CHECK MODE

GATE t SETTINGS	NUMERIC READOUT
0.1 ms	10
1.0 ms	100
10 ms	1000
0.1 s	10000
1.0 s	100000
10 s	1000000

2-33 FREQUENCY

2-34 Standard frequency measurements from 0 to 80 MHz are made using the FREQUENCY A operating mode. The GATE times available for each frequency measurement range from 0.1 ms to 10 s in power of ten steps and are selected by the setting of the RANGE switch. The RANGE switch is set to provide optimum resolution of the displayed frequency.

to occur at precise voltage levels of the input signal. These levels can be visually set using an oscilloscope to display the input signal while modulating the Z axis scope input with the Marker output signal available at the rear of the 1952B. The measured time interval appears as an intensified zone on the scope trace and can be adjusted using the CHANNEL A and B Trigger Level controls. Refer to MARKER output discussion given earlier in this section under Operating Notes.

2-46 Use the following procedure to operate the Model 1952B in the time interval function:

a. Turn the CYCLE RATE control clockwise out of the POWER OFF detent.

b. Set the CONT - TRIG switch to the CONT position.

c. Set the FUNCTION switch to T.I. A-B.

d. Set the SEP - COM switch to the SEP position if separate start/stop inputs are used, or to COM if the start/stop inputs are derived from a single input signal.

e. If separate input signals are used, connect the start signal to the CHANNEL A input and the stop signal to the CHANNEL B input. If a single input is used connect it to the CHANNEL A input.

f. Adjust the CHANNEL A and B Trigger Levels to establish the desired start/stop trigger points. Use the MARKER output pulse in conjunction with an oscilloscope to establish precise trigger levels.

g. Adjust the CYCLE RATE control for the desired display period.

h. Using the RANGE switch, select the T.I. RES setting which provides the desired resolution.

2-47 TOTALIZING

2-48 In the A GTD.BY B function the Model 1952B can be used as a gateable totalizer. Each triggerable event which occurs on the CHANNEL A input is accumulated or totalized while a gate pulse is present at the CHANNEL B input. The CHANNEL B gate pulse assumes an active high state when the associated slope switch is set to the positive (+) position and an active low state when the slope switch is set to the negative (-) position. In this mode the RANGE switch is not functional.

2-49 Use the following procedure to operate the Model 1952B in the totalize function:

a. Turn the CYCLE RATE control clockwise out of the POWER OFF detent.

b. Set the FUNCTION switch to the A GTD.BY B position.

c. Set the SEP - COM switch to the SEP position.

d. Connect the signal to be counted or totalized to the CHANNEL A Input Connector.

e. Connect the gate pulse to the CHANNEL B Input Connector.

f. Adjust the CHANNEL A and B Trigger Level Adjust to ensure proper triggering. The counter will totalize event inputs on CHANNEL A as long as the CHANNEL B input is at an active level. In this mode the display will reset only when the manual RESET is activated.

NOTE!

If no input signal to channel B is required or available, the gating of the accumulator can be manually controlled by the CHANNEL B SLOPE switch. Turn the CHANNEL B TRIGGER LEVEL control fully cw. Setting the SLOPE switch for CHANNEL B to - will now gate the accumulator, and the 1952B will totalize. Setting it back to + will inhibit the accumulator, and the totalizing will halt. To resume totalizing, set the SLOPE switch to - again; to start over from zero, momentarily press the RESET switch.

Section 3
Theory of Operation

3-1 INTRODUCTION

3-2 This section of the manual contains an overall functional description followed by a detailed block diagram analysis of the Model 1952B Digital Counter-Timer. Simplified block diagrams and timing diagrams are included, as necessary, to supplement the text.

3-3 OVERALL FUNCTIONAL DESCRIPTION

3-4 The Fluke Model 1952B, as shown in the simplified block diagram of Figure 3-1, is a

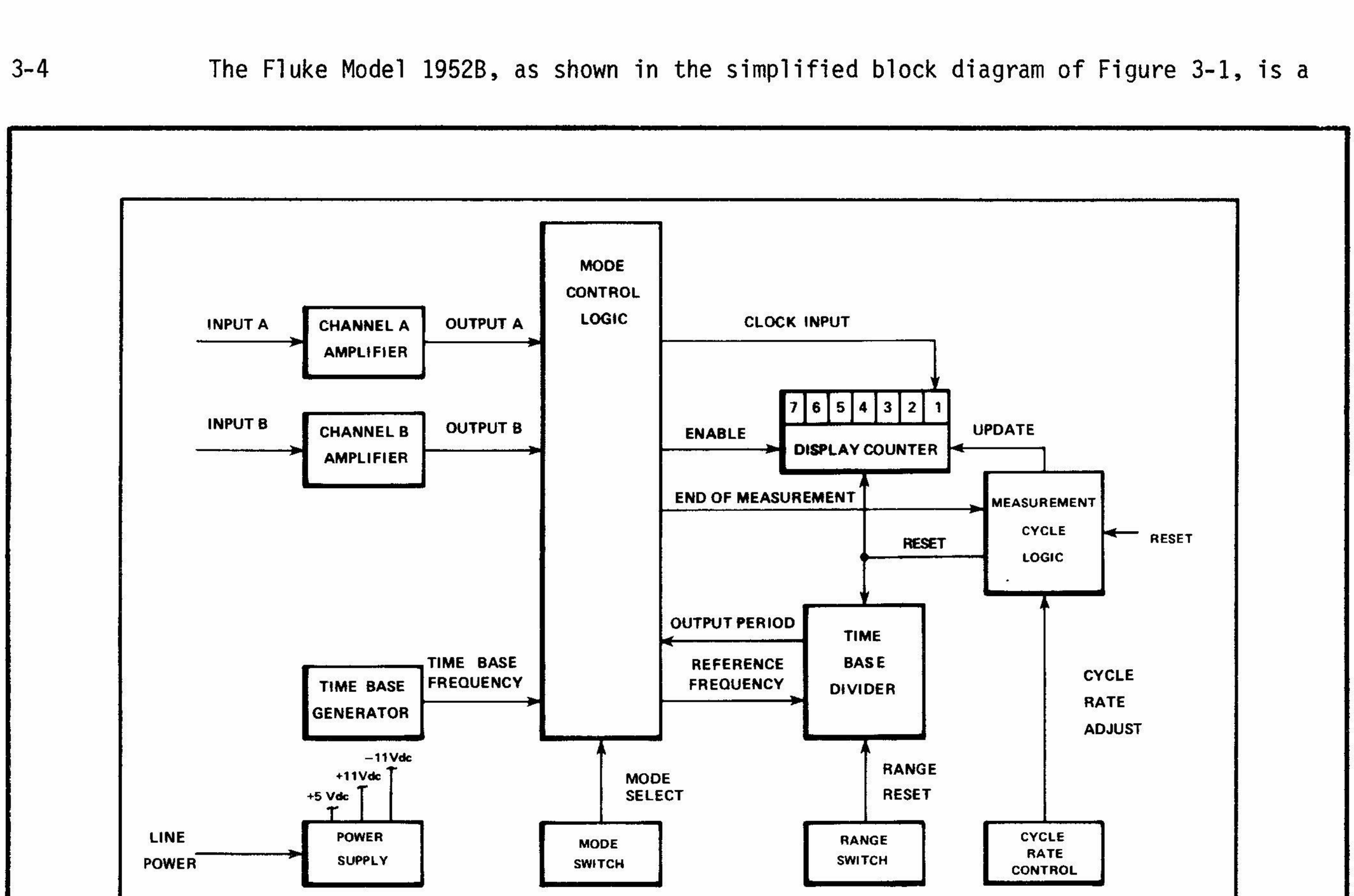


Figure 3-1 . MODEL 1952B , SIMPLIFIED BLOCK DIAGRAM .

universal counter/timer capable of making frequency, frequency ratio, period average, time interval and totalize measurements. Each measurement mode and six associated ranges are individually selectable from the front panel. Separate CHANNEL A and B inputs provide the trigger levels controls necessary to condition input frequencies from 0 to 80 MHz. All measurements are displayed on a seven decade LED display along with range annunciators and decimal point, when applicable. The time between successive measurements can be adjusted on a continuous basis.

3-5 The input signals for all operating modes are conditioned for use by the CHANNEL A and B amplifiers. These amplifiers condition their input signals by providing AC or DC coupling, x1 or x10 attenuation, + or - slope trigger selection, and continuously variable positive or negative threshold voltage adjustments for trigger level control. A series of high gain amplifiers square the leading and trailing edges of the input signal to provide a digital pulse train representative or the input frequency. The pulse train is interfaced to provide the amplifier with output voltage levels which are TTL compatible.

3-6 The Mode Control Logic accepts the outputs of the CHANNEL A and B amplifiers along with the output of a precision Time Base Generator, and depending on the position of the FUNCTION switch, routes them to the Time Base Divider and/or the Display Counter. The Time Base Divider, when used, divides the Reference Frequency by a selectable power of 10 to provide an Output Period equal to 10^0 through 10^5 periods of the Reference Frequency. The selected Output Period controls the time during which the other input frequency is allowed to accumulate or totalize in the Display Counter before being displayed. The Time Base Divider is not used in the A GTD BY B mode. Instead the output of the CHANNEL B amplifier is used to determine the time during which the Display Counter is enabled.

3-7 The Measurement Cycle Logic controls the time between successive measurements, and provides the Display Counter with an Update and Reset command following each measurement cycle. When a measurement is completed, the Display Counter is disabled and an End of Measurement (EOM) command is sent to the Measurement Cycle Logic. The EOM command enables the Cycle Logic causing it to sequentially generate the display Update and Reset pulses. The time between the two pulses is variable depending on the position of the CYCLE RATE control. The display Update pulse enables the display memory to enter the data currently contained in the counter. The new data is immediately transferred to the display LED's. When the cycle rate period is elapsed, the Reset pulse is generated to reset the counter and start a new measurement.

3-8 The Display Counter, when enabled, counts or accumulates each Clock Input pulse received from the Mode Control Logic. The counters are arranged in decade-series fashion and are capable of accumulating nine counts in each of the seven available decades. When the counters are disabled at the end of a measurement, the accumulated data is transferred to a display memory by the action of the Update pulse from the Measurement Logic. The new contents of the updated memory immediately appears on the LED display. As a new measurement is initiated, the Reset pulse resets the counters to zero but does not affect the memory or display. Included on the display are decimal point and measurement units information derived from the FUNCTION and RANGE switch settings.

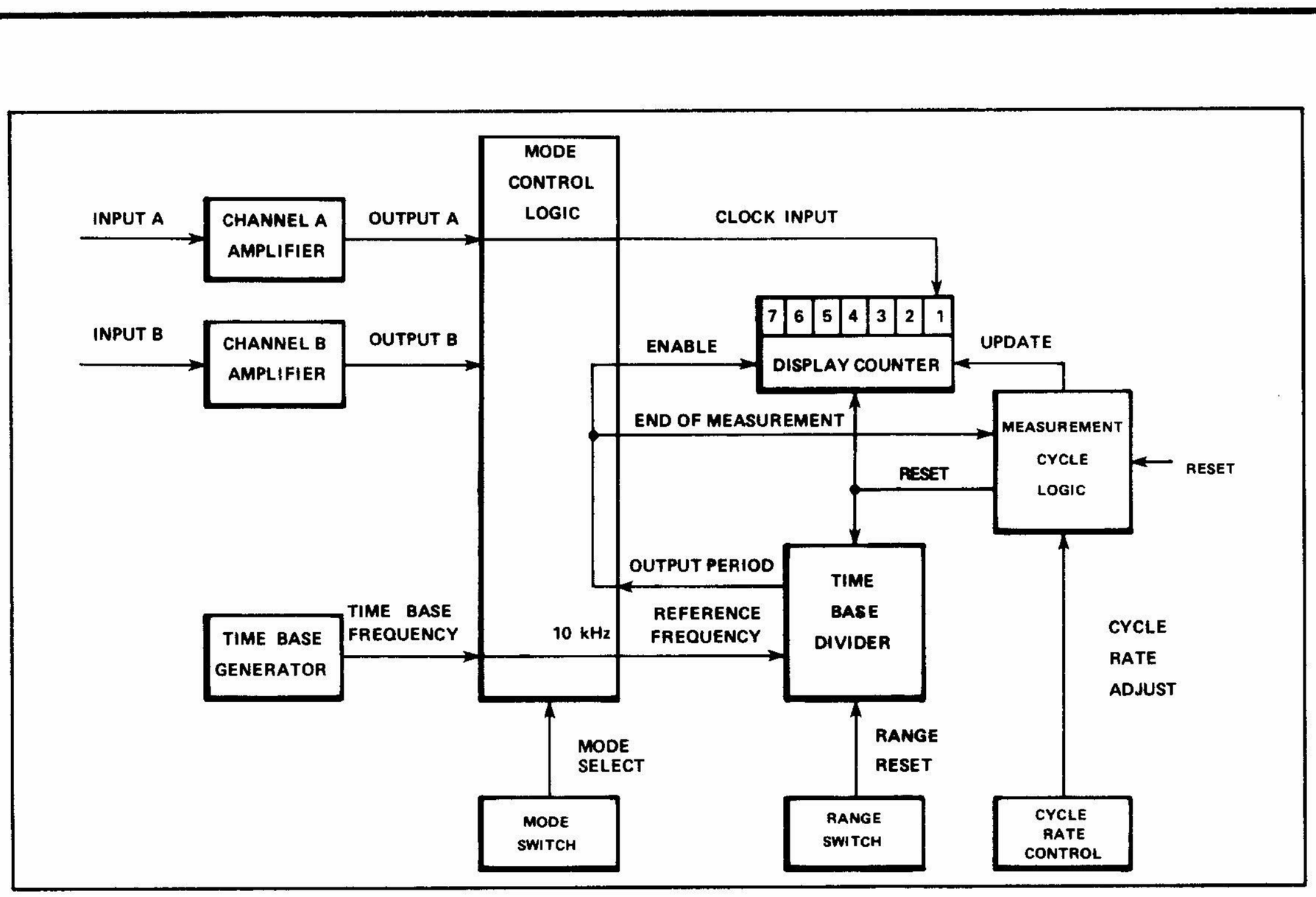


Figure 3-2 . FREQUENCY A FUNCTION,SIMPLIFIED DATA FLOW CHART .

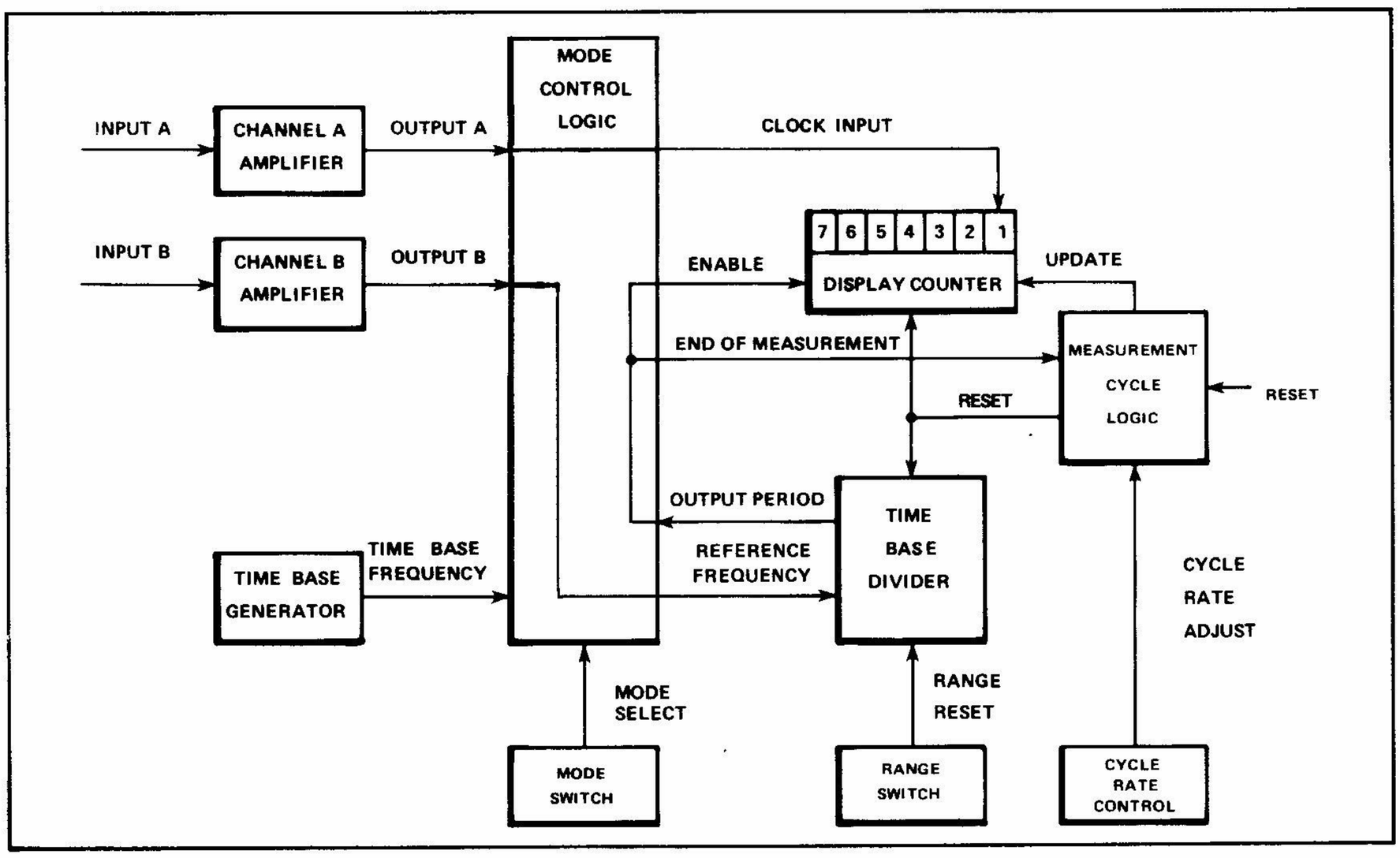


Figure 3-3 . FREQUENCY (A/B)N FUNCTION,SIMPLIFIED DATA FLOW CHART .

3-9 Frequency Mode

3-10 In the frequency mode the counter assumes the configuration shown in Figure 3-2. The input frequency is applied at Input A and routed through the Mode Control Logic to provide the Input Clock for the Display Counter. A 10 kHz Time Base Frequency derived from the Time Base Generator is used as the Reference Frequency for the Time Base Divider. The resulting Output Period generated by the Time Base Divider is selectable by powers of 10 (0.1 ms to 10 s) using the RANGE switch. The precise Output Period is used to enable the Display Counter and allow the Clock Input to accumulate for the specified time. The accumulated count is then displayed as frequency. Frequency resolution is increased as the Output Period of the Time Base Divider is increased.

3-11 Ratio Mode

3-12 In the FREQ (A/B)N mode, ratio measurements are made using the configuration shown in Figure 3-3. The Input A frequency is routed through the Mode Control Logic and used as the Clock Input for the Display Counter. The Input B frequency is routed through the Mode Control Logic and is used as the Reference Frequency to the Time Base Divider. The output of the Time Base Divider can be selected by the RANGE switch to provide an Output Period equal to from 10^0 to 10^5 periods of the Reference Frequency. The selected Output Period enables the Display Counter and as a result, the Input A frequency is accumulated for the duration of the Output Period. At the end of the Output Period the Display Counter is disabled and the EOM signal is generated.

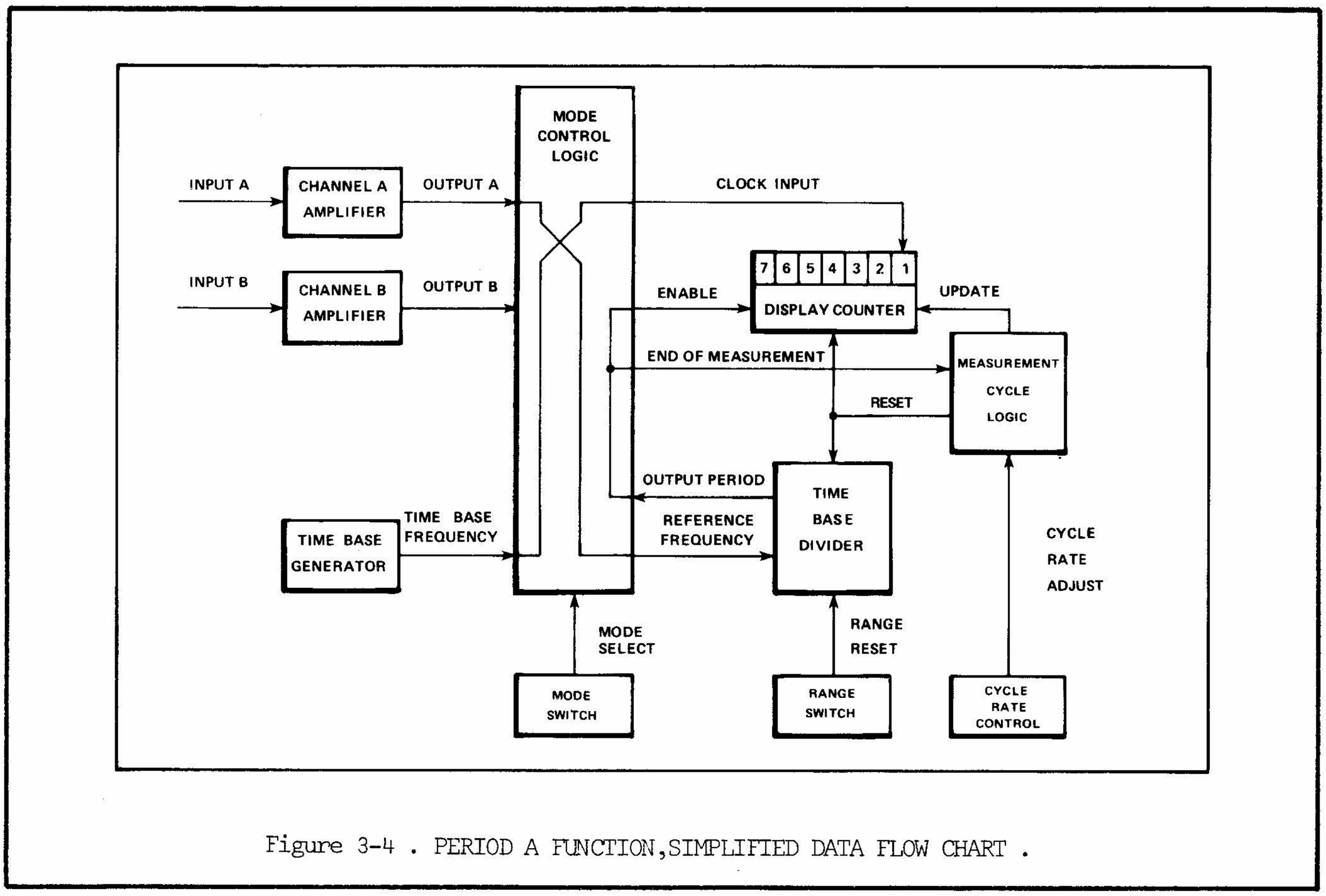


Figure 3-4 . PERIOD A FUNCTION,SIMPLIFIED DATA FLOW CHART .

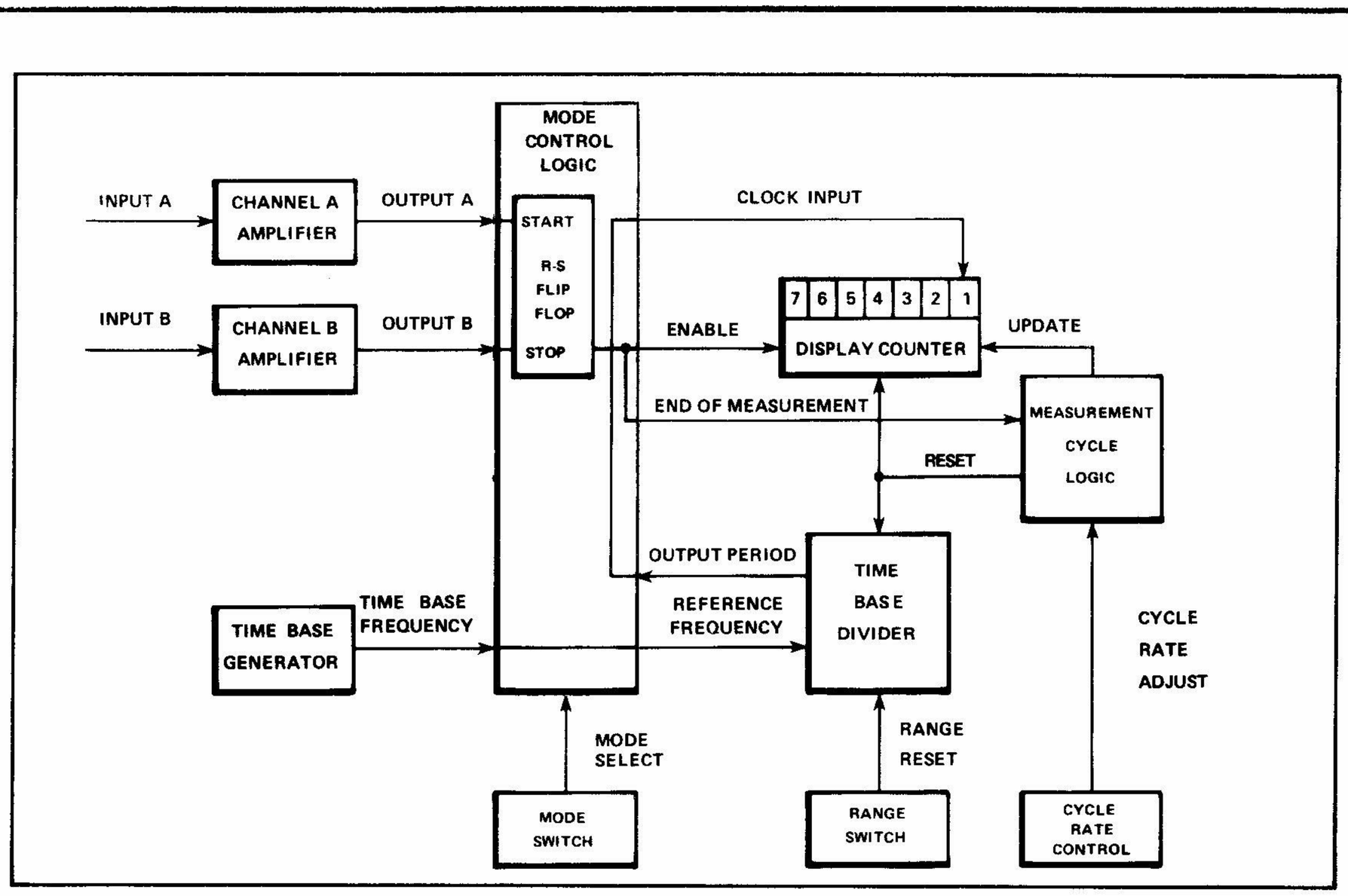


Figure 3-5 . TIME INTERVAL FUNCTION,SIMPLIFIED DATA FLOW CHART .

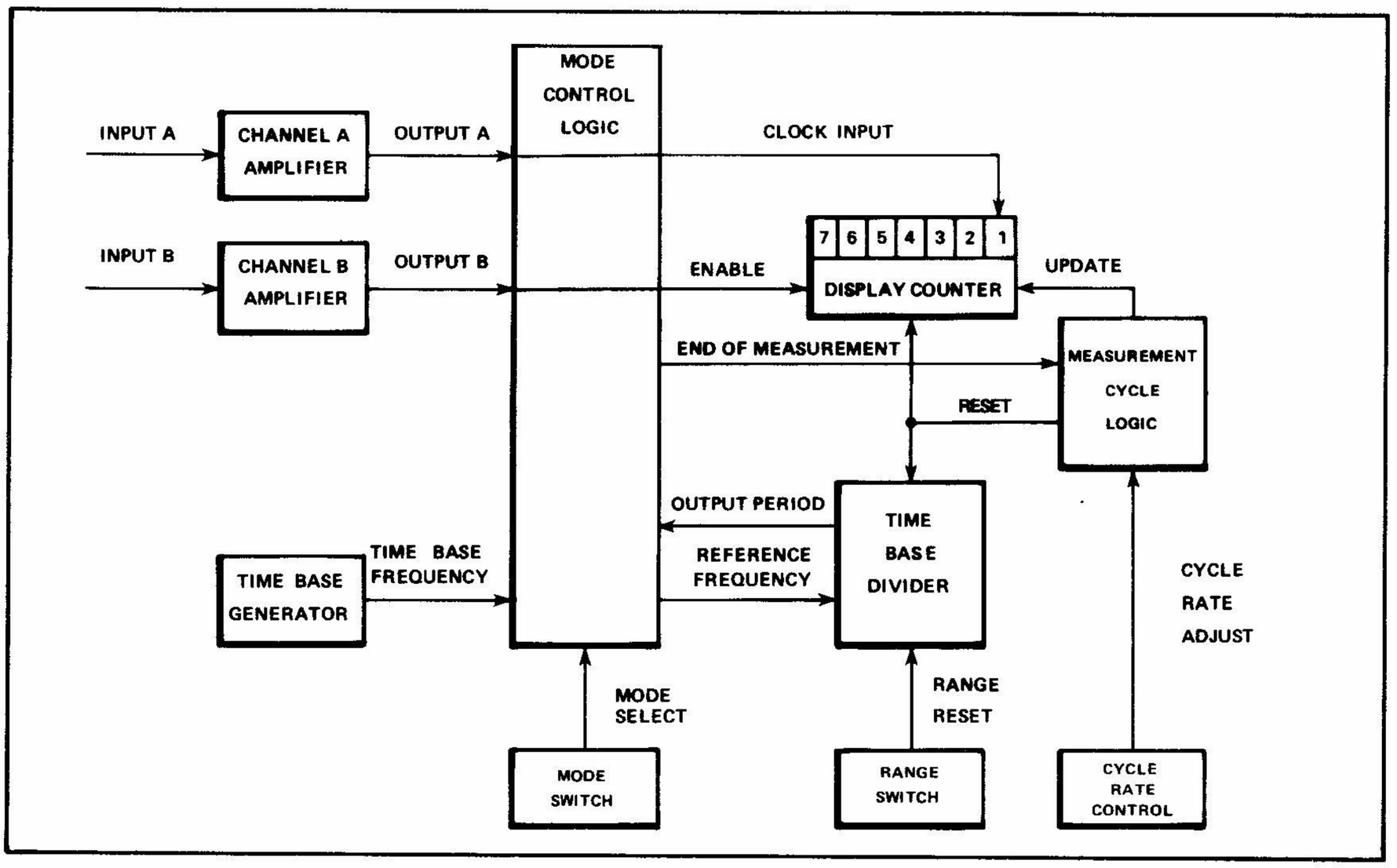


Figure 3-6 . TOTALIZE FUNCTION,SIMPLIFIED DATA FLOW CHART .

3-13 Period Mode

3-14 In the period mode, as shown in Figure 3-4, a 100 kHz signal from the Time Base Generator is routed through the Mode Control Logic and is used as the Clock Input to the Display Counter. This 100 kHz clock is totalized by the counter for multiple periods of the Input A signal. The Time Base Divider receives the Input A signal as the Reference Frequency and provides a timed Output Period equal to 10^0 through 10^5 periods of the Reference Frequency. The duration of the Output Period is selectable using the RANGE switch. At the end of the selected Output Period, the Display Counter is disabled and the EOM signal is generated.

3-15 Time Interval

3-16 In the Time Interval mode, as shown in Figure 3-5, the Display Counter accumulates Clock Input pulses for a time interval started by Input A and stopped by Input B. The Clock Input, as received from the Time Base Divider, is selectable by powers of 10 from 10 MHz to 100 Hz. The Reference Frequency for the Time Base Divider is derived from the 10 MHz output of the Time Base Generator.

3-17 The first input transition detected by the CHANNEL A Amplifier sets an R-S flip-flop in the Mode Control Logic and Enables the Display Counter to start accumulating Clock Input pulses. Some time later, the first input transition detected by the CHANNEL B Amplifier resets the R-S flip-flop and disables the Display Counter. At the same time the End of Measurement signal is generated. The accumulated Clock Input pulses give a direct indication of the time interval between the detection of the A and B input signals.

3-18 Totalize Mode

3-19 In the A GTD BY B mode, as shown in Figure 3-6, an Enable signal derived from Input B allows the Display Counter to totalize the Clock Input pulses derived from Input A. The counter, when repeatedly enabled and disabled using the Input B signal, is not reset. Instead, it sequentially totalizes the Clock Input pulses which occur during each enable period. The Display Counter is reset upon receipt of an external Reset command.

3-20 Self Check Mode

3-21 In the Self Check Function, as shown in Figure 3-7, the logic portion of the 1952B is exercised by the Time Base Generator to verify the overall operation of the counter. The 10 kHz output of the Time Base Generator is processed by the Time Base Divider to provide an Output Period which is selectable in powers of 10 time intervals from 0.1 ms to 10 seconds. The selected time interval (GATE t) is used to Enable the counter and allow a 100 kHz Clock Input, also from the Time Base Generator, to be totalized. When the logic portion of the 1952B is operating properly, the totalized count displayed should be 10, 100, 1000, 10000, 100000 or 1000000 depending on the position of the RANGE switch.

3-22 BLOCK DIAGRAM ANALYSIS

3-23 General

3-24 A block diagram analysis of the functional circuits of the 1952B is given in the following paragraphs. The circuits described correspond to the functional blocks defined in Figure 3-1. Detailed schematics are included at the rear of the manual.

3-25 Power Supply

3-26 The Power Supply consists of a 115/230 V ac transformer, associated switches, fuse, power input connector and regulating circuitry. The function of the power supply is to provide + 5 + 11 and - 11 V dc regulated operating voltages for the 1952B circuitry. The + 5 V dc is used to operate the digital portion of the circuitry while the + 11 and - 11 V dc supplies operate the analog portion of the circuitry. All three operating voltages are generated using conventional isolating and regulating techniques. Refer to the schematic, 1952B - 1001, at the rear of this manual.

3-27 Line power to the power supply is received through the rear panel Input Power Connector, Fuse F1, the front panel CYCLE RATE (power) switch and the 115/230 Voltage select switch. The front panel display illuminates when the regulated + 5 V dc is present.

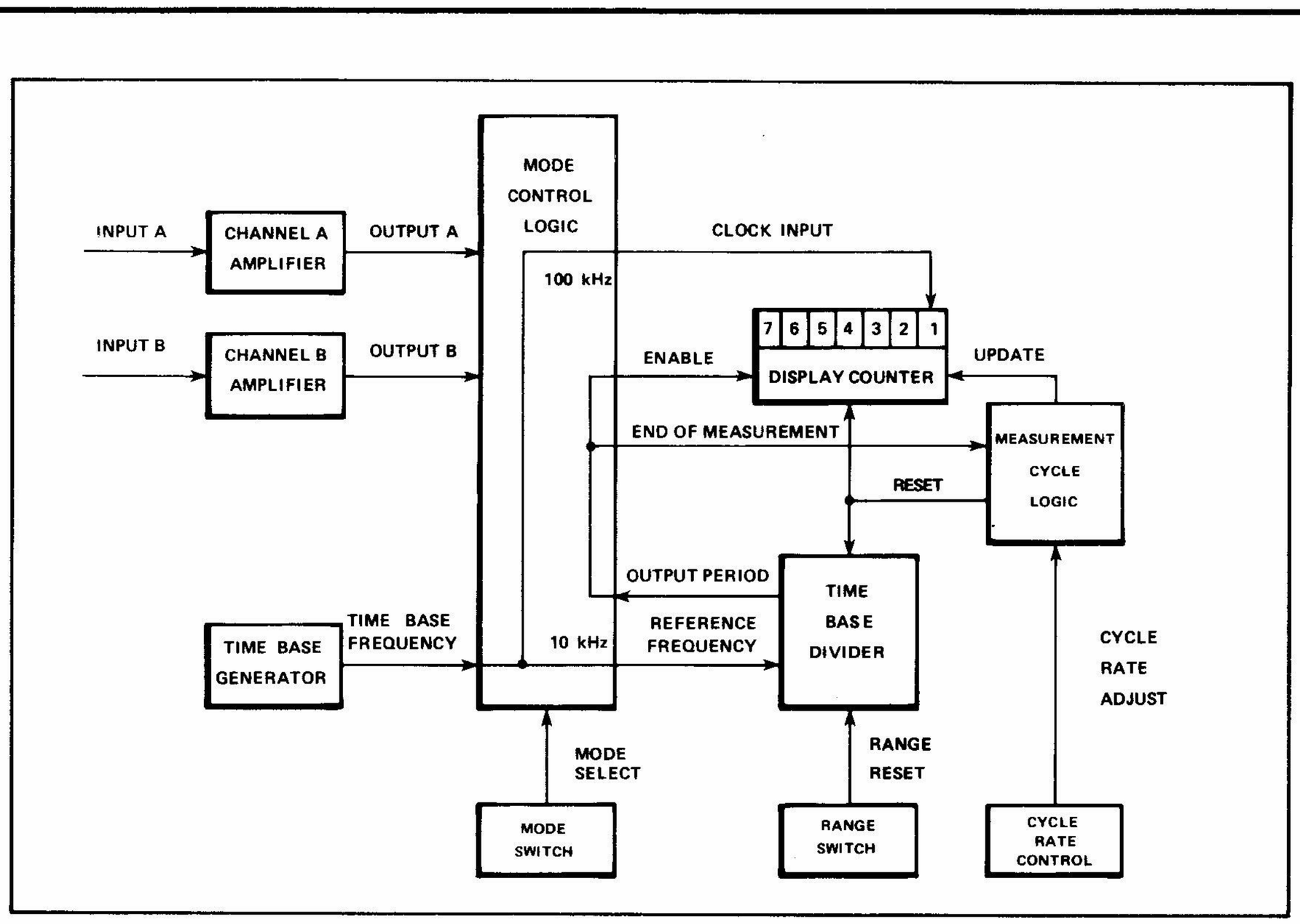


Figure 3-7 . SELF CHECK FUNCTION,SIMPLIFIED DATA FLOW CHART .

3-28 Mode Control Logic

3-29 The mode Control Logic as shown in the schematic at the rear of this manual is controlled by the position of the front panel FUNCTION switch. The truth table given in the schematic defines Inputs to the Mode Control Logic which are required to interconnect the various functional blocks of the 1952B and thus configure each particular operating mode. Each of the possible mode configurations are defined earlier in this section under the overall functional description.

3-30 Channel A and B Amplifiers

3-31 The CHANNEL A and B Amplifiers are electrically similar. Therefore, an analysis of the CHANNEL A amplifier is sufficient to describe both amplifiers.

3-32 The CHANNEL A Amplifier, as shown in Figure 3-8, consists of a high gain differential amplifier, input coupling and attenuator circuits, a Trigger Level Adjust circuit, Slope switch, Status indicators and an inverter. The function of the CHANNEL A Amplifier is to define an acceptable input signal and to produce a TTL compatible, output pulse for each acceptable Input cycle that occurs.

3-33 In operation, front panel slide switches permit the Input signal to be AC or DC coupled and attenuated by a factor of 1 or 10 before being delivered as an input to the high gain

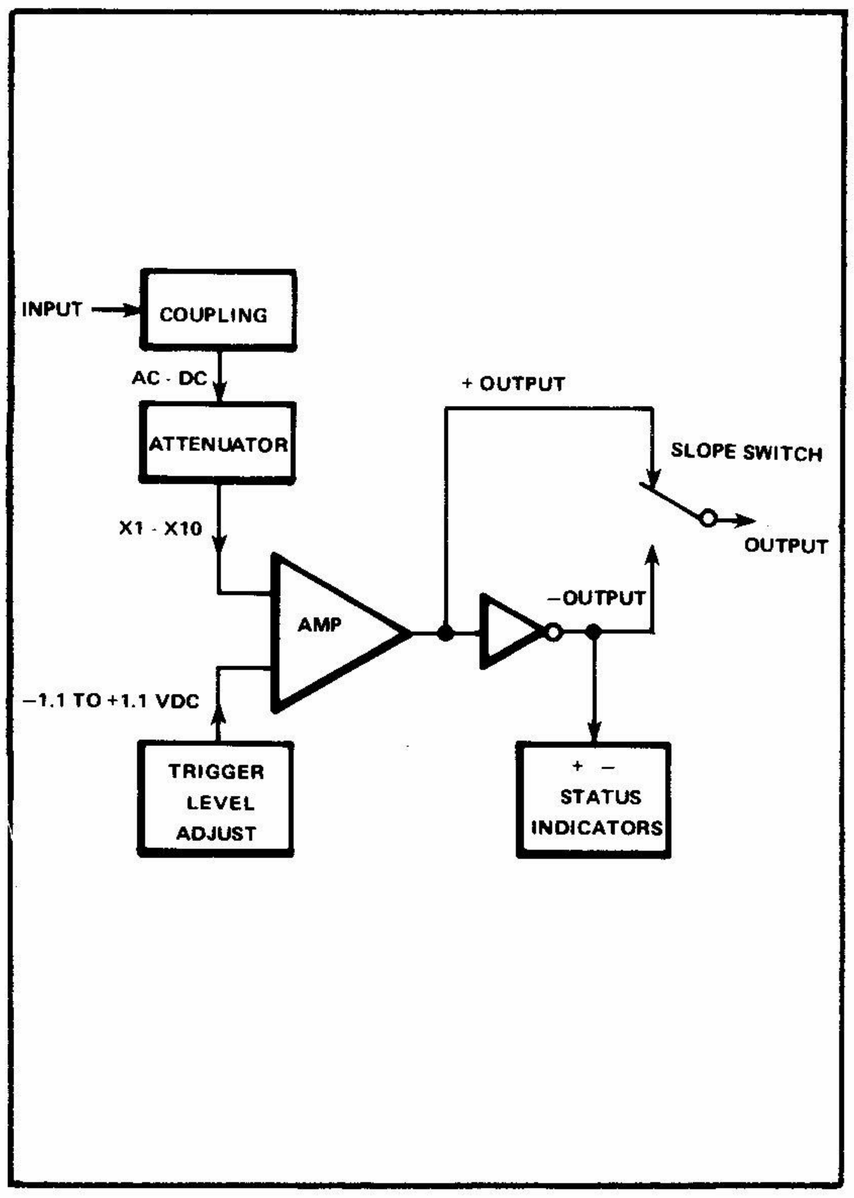


Figure 3-8 . INPUT AMPLIFIER, FUNCTIONAL BLOCK DIAGRAM .

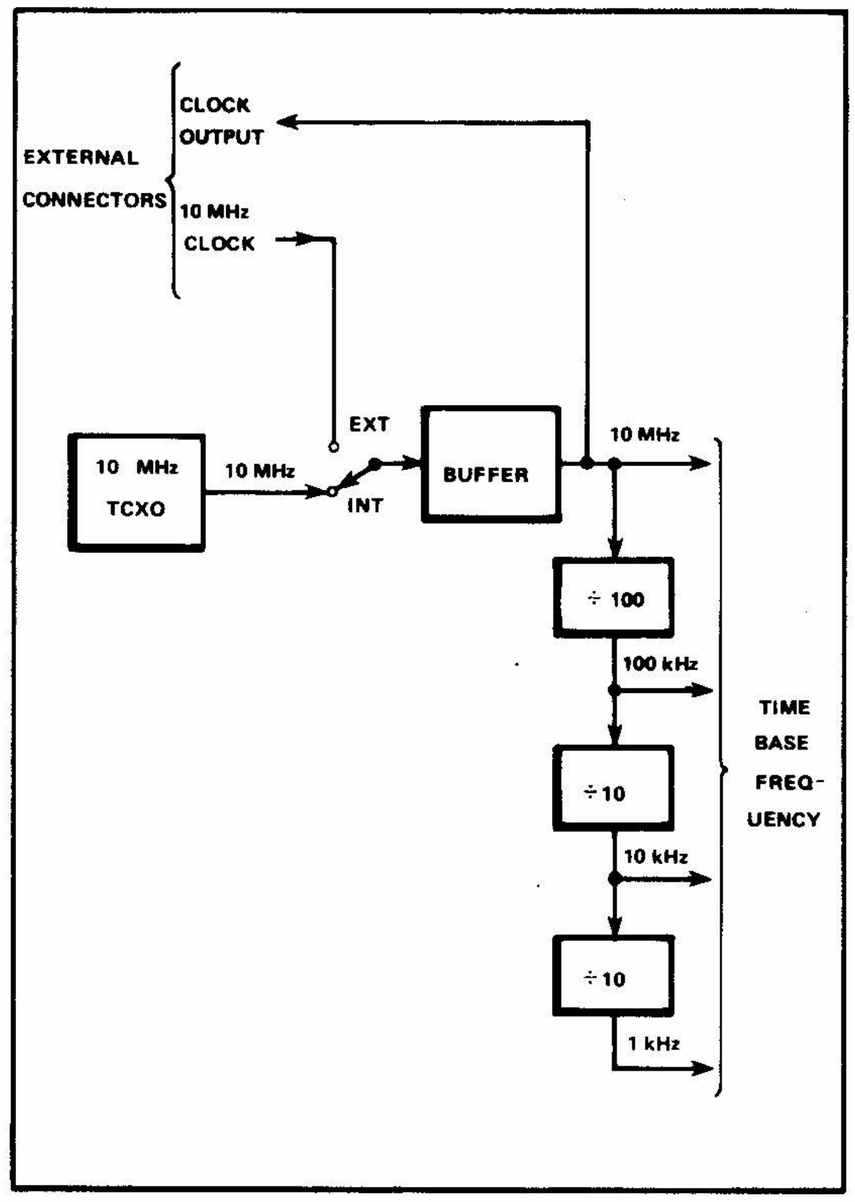


Figure 3-9 . TIME BASE GENERATOR, SIMPLIFIED BLOCK DIAGRAM .

differential amplifier. The high gain amplifier operates as an open loop voltage comparator. The comparators reference voltage is established by the Trigger Level Adjust whose output is continuously adjustable from + 1.1 to - 1.1 V dc. When the input voltage exceeds the reference voltage, the amplifier generates a leading or lagging edge output transition, depending on the position of the Slope Switch. One of the Status Indicators is normally illuminated to indicate the polarity of the input required to produce an output. The illumination pattern is reversed when an output is generated.

3-34 Time Base Generator

3-35 The Time Base Generator, as shown in Figure 3-9, consists of a 10 MHz Crystal Oscillator, a buffer and four BCD counters. The function of the Time Base Generator is to generate four separate output frequencies (10 MHz, 100 kHz, 10 kHz and 1 kHz) using a standard 10 MHz frequency source to the Buffer input. The Buffer, equipped with input overvoltage protection, converts the 10 MHz input signal to a buffered, TTL compatible output. The 100 kHz, 10 kHz and 1 kHz outputs are derived from the 10 MHz output using decade counters operated in series.

3-36 Time Base Divider

3-37 The Time Base Divider, as shown in Figure 3-10, consists of five BCD counters which are connected in series to provide six separate output frequencies. Each frequency is a power of 10 multiple of the Reference Frequency and can be selected for use as an Output Period signal by properly positioning the RANGE switch.

3-38 Display Counter

3-39 The Display Counter, as shown in Figure 3-11, consists of four flip-flops, one LED

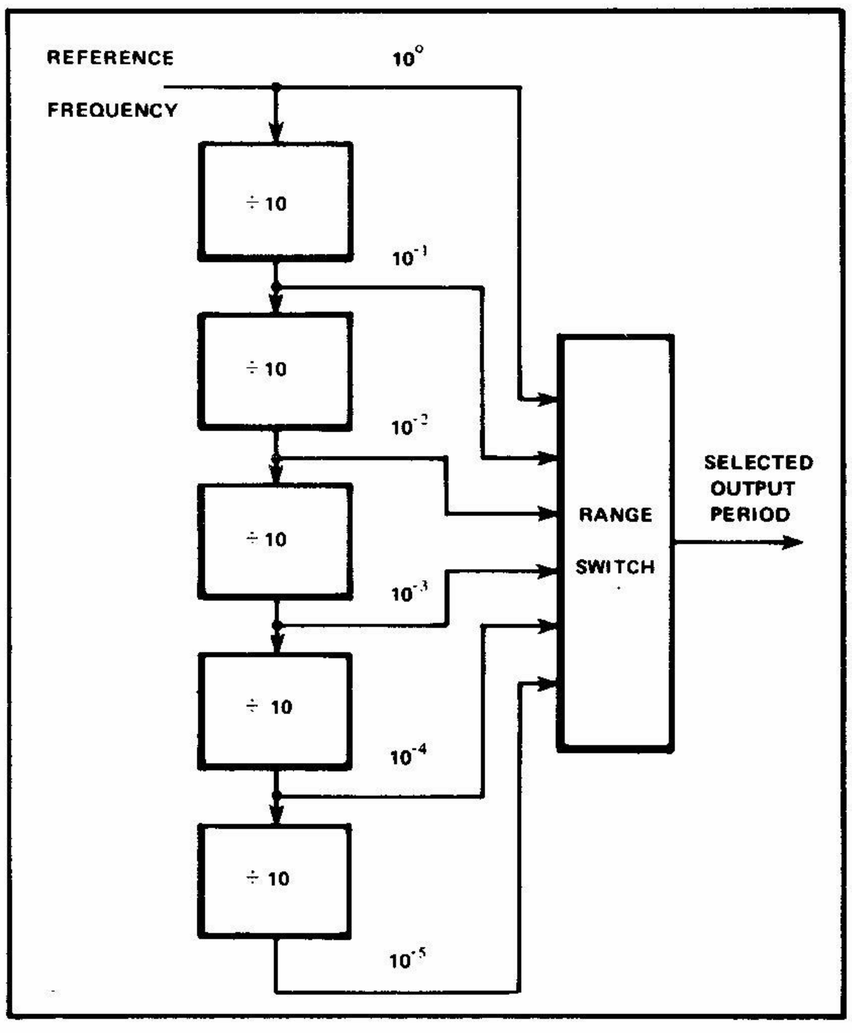


Figure 3-10 . TIME BASE DIVIDER, SIMPLIFIED BLOCK DIAGRAM .

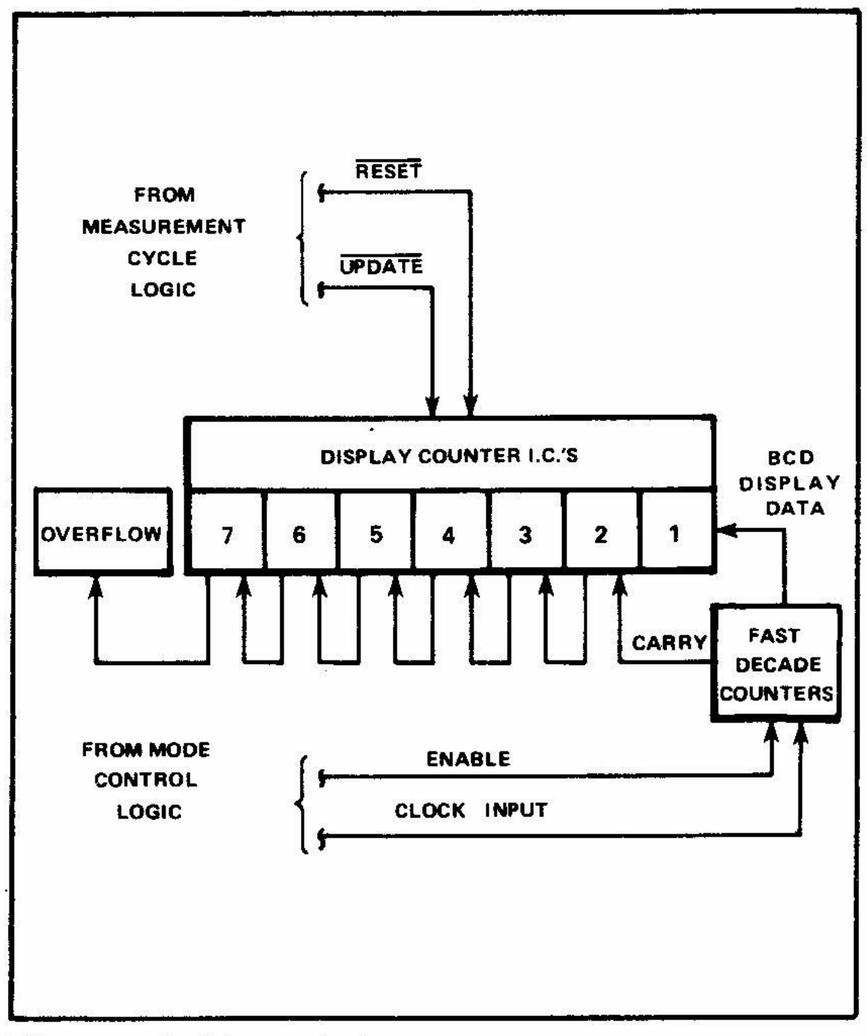


Figure 3-11 . DISPLAY COUNTER, SIMPLIFIED BLOCK DIAGRAM .

and seven Display-Counter (Numeric Display with Logic) IC's. The function of the Display Counter is to totalize, store and display each Clock Input pulse which occurs while the Enable signal is at a logic 1. In operation, seven Display Counter IC's are cascaded to form a seven decade counter. Since the least significant counter decade must be capable of operating at an 80 MHz rate, the counter section of that decade is replaced with a Fast Decade Counter which consists of four separate flip-flops. The Carry output of the Fast Decade Counter is used as a clock input for the second least significant decade while the BCD output of the Fast Decade Counter is used as display data for the least significant decade.

3-40 When the Enable input is at a logic 1, the Clock Input signal is totalized but the accumulated total is not displayed until an Update command (logic 0) is received. The Update command enables a latch in each Display Counter IC to store the output of the associated counter. The data in the latch is displayed as a numeric digit and will continue to be displayed after the Update command is removed. The counters are cleared to zero using the Reset command. Sequentially commanding the Reset and Update commands causes both the counter and display to be set to zero.

3-41 Measurement Cycle Logic

3-42 The Measurement Cycle Logic, as shown in Figure 3-12, consists of a flip-flop, one BCD counter, a one of 10 decoder and an adjustable one-shot. The function of the Measurement Cycle Logic is to interlock and control the actions of both the measurement cycle and the subsequent transfer of accumulated measurement data to the Display Counter readouts. In operation, the BCD counter is initially setting at a count of four indicating that a measurement cycle has been completed and that a Reset command is required to initiate a new measurement. The Reset command can be generated using the manual RESET switch. The manual Reset command resets the counter to zero while the cycle rate one-shot (when timed out) enables the counter to sequence through to the count of nine. The nine count is fedback to the flip-flop and the count sequence is again inhibited. In either event, the reset action initiates a new measurement cycle and causes the BCD counter to be reset to zero by the EOM command. The EOM command remains low for the duration of the measurement cycle and returns to the high state to indicate that the cycle is complete. When the Measurement Cycle is complete, the counter is enabled and advanced by the 500 Hz clock generated by the flip-flop. As count number one is decoded, the Update pulse is generated to transfer accumulated data to the display readout. Count number two is used to trigger the one-shot if the INT - EXT switch is in the INT position. Count number four returns the Measurement Cycle Logic to the initial position. Notice that when the one-shot is used to restart the counter, count number seven is used to generate the Reset pulse.

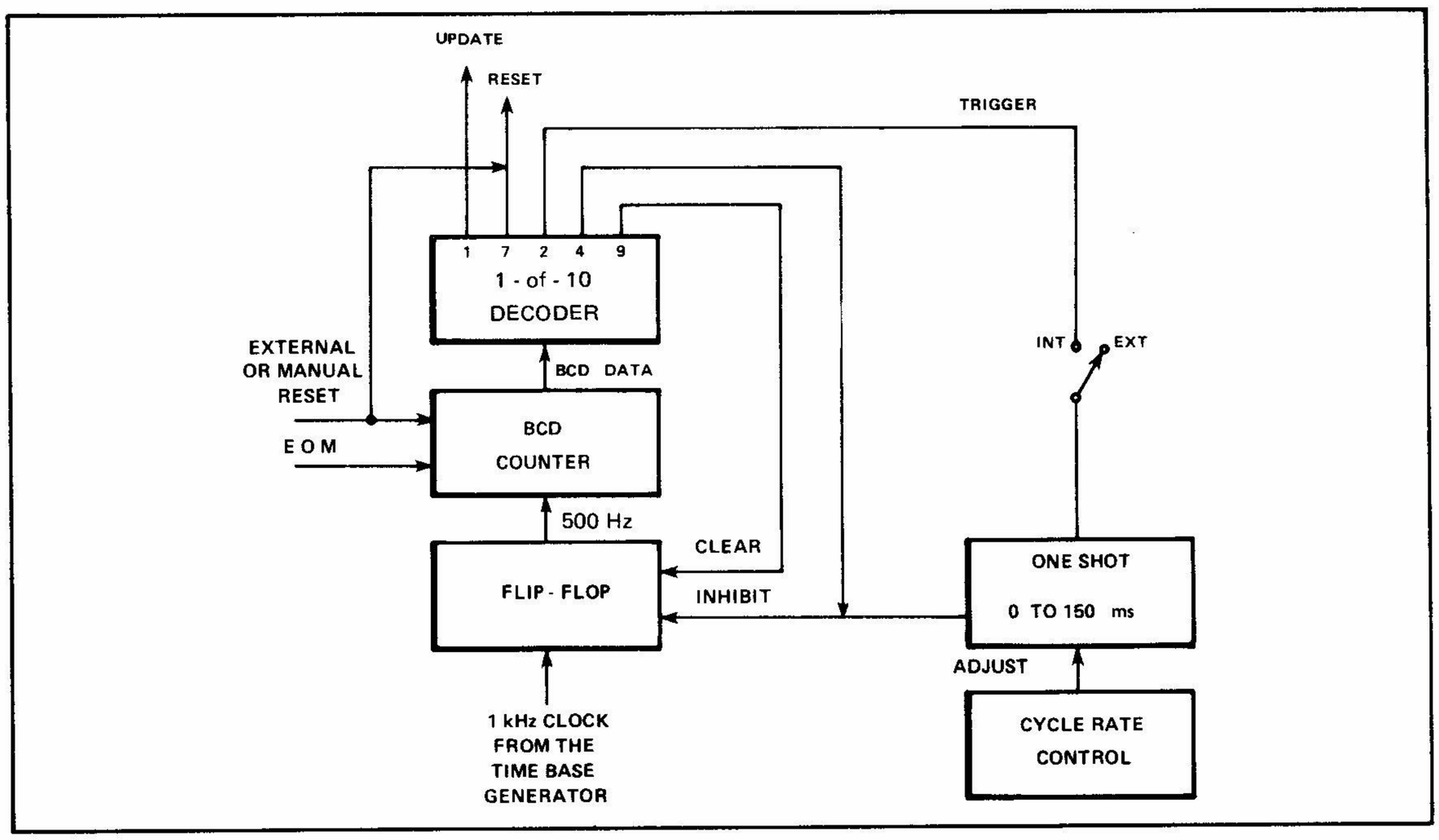


Figure 3-12 . MEASUREMENT CYCLE LOGIC,
SIMPLIFIED BLOCK DIAGRAM .

Section 4

Maintenance

4-1 INTRODUCTION

4-2 This section of the manual contains maintenance information for the Model 1952B. This includes service information, general maintenance, performance test, calibration and troubleshooting. The performance test is recommended as a preventative maintenance tool and should be executed every 90 days to verify proper instrument operation. Troubleshooting information is given in the form of flowcharts at the end of this section. Table 4-1 lists the recommended test equipment required for the performance test and troubleshooting.

Table 4-1 EQUIPMENT REQUIRED FOR PERFORMANCE TEST AND TROUBLESHOOTING

EQUIPMENT TYPE	REQUIRED BASIC SPECIFICATIONS	WHERE USED
Quartz oscillator frequency standard with 10 or 1 & 5 or 50 MHz output	Short term stability to be better than 1×10^{-8} Long term stability to be better than 1×10^{-9} @ 0 - 50°C ambient temperatures	Time Base Oscillator Adjustment
Low Frequency Oscillator	Frequency Range: DC - 10 MHz; Variably attenuated output from 1 mV rms to 1 V or 5 V rms @ 50 ohms output impedance; selectable sinewave and square wave modes;	Performance Check; Calibration
High Frequency Oscillator	Frequency Range: 10 MHz - 100 MHz Variably attenuated calibrated output from 1 mV to 1 V or 5 V rms (50 ohms output impedance); CW mode	Performance Check; Calibration

EQUIPMENT TYPE	REQUIRED BASIC SPECIFICATIONS	WHERE USED
RF Millivoltmeter	Frequency Range: 10 kHz - 100 MHz; Voltage Range: 1 mV - 5 V rms Accuracy: Maximum 3% of reading @ 100 MHz + 1% full scale VSWR: Less than 1:1.15 Power Sensitivity: 200 pW @ 50 ohms Basic Accessories: 50 ohms BNC Adapter;	Peformance Check; Calibration
Multimeter	Capable of measuring DC & AC voltage: DC & AC current; Resistance DC VOLTAGE RANGE of ± 199.9 mV to ± 1999 V with accuracy better than ± 1% of reading AC VOLTAGE RANGE of 199.9 mV to 1999 V with accuracy better than ± 1.5% of reading DIRECT CURRENT RANGE of ± 199.9 uA to ± 1999 mA with accuracy better than ± 1% of reading ALTERNATING CURRENT RANGE of 199.9 uA to 1999 mA with accuracy better than ± 1.5% of reading RESISTANCE RANGE of 199.9 ohms to 19.99 M ohm with accuracy better than ± 1% of reading	Troubleshooting
Oscilloscope	DUAL TRACE; 100 MHz BANDWIDTH 2 mV per division sensitivity; TIME BASE @ 0.05 us/div. with time delay features	Troubleshooting

4-3 SERVICE INFORMATION

4-4 Each instrument that is manufactured by the John Fluke Mfg. Co., Inc. is warranted for a period of one year upon delivery to the original purchaser. The WARRANTY is given on the back of the title page located in the front of this manual.

4-5 Factory authorized calibration and service for each Fluke product is available at various world wide locations. A complete list of these service centers is included with the WARRANTY. Shipping information is given in the operating instructions section of this manual. If requested, an estimate will be provided to the customer before work is begun on instruments that are beyond the warranty period.

4-6 GENERAL MAINTENANCE

4-7 Access Information

4-8 All PCB assemblies used in the Model 1952B can be accessed for maintenance purposes by removing the top and bottom dust covers from the instrument. Four phillips head screws hold each dust cover in place. Once the top dust cover is removed, the Display-Counter PCB and Switch assemblies can be removed using the procedures outlined in the following paragraphs.

4-9 DISPLAY COUNTER PCB ASSEMBLY

4-10 Use the following procedure to remove the Display Counter PCB Assembly from the 1952B.

a. Remove the top and bottom dust covers.

b. Remove the knobs from all front panel controls.

c. Remove the front panel nuts from the FUNCTION, RANGE and RESET switches.

d. Remove the two screws located on both side brackets close to the front panel.

e. Turn the instrument over and unsolder the connections to the CHANNEL A and B, BNC connectors.

f. Unsolder the two wires at the rear of the CYCLE RATE control.

g. Using a rocking motion pull the front panel free of the chassis.

h. Locate and remove the four screws holding the Display Counter PCB Assembly to the front panel.

i. Install the Display Counter PCB Assembly by logically reversing this procedure.

4-11 SWITCH PCB ASSEMBLY

4-12 Use the following procedure to remove the Switch PCB Assembly from the 1952B.

a. Complete steps a. through h. of the procedure given for removing the Display Counter PCB Assembly.

b. Pull the Switch PCB Assembly out of the card edge connector.

c. Install the Switch PCB Assembly by logically reversing this procedure.

4-13 Cleaning

4-14 Clean the Model 1952B periodically to remove dust, grease and other contamination. Use the following procedure:

a. Clean the surface of all PCB's using clean dry air at low pressure (< 120 PSI). If grease is encountered, spray with Freon T.F. Degreaser and remove grime with clean dry air at low pressure.

b. Clean the front panel with a soft cloth dampened with a mild solution of detergent and water.

4-15 Fuse Replacement

4-16 The power fuse F1 is located on the rear panel of the Model 1952B. If replacement is necessary, use the following rated fuses:

a. 1 Amp slo-blo for 115 V ac

b. 1/2 Amp slo-blo for 230 V ac

4-17 Service Tools

4-18 No special tools are required to maintain or repair the Model 1952B Counter/Timer.

4-19 PERFORMANCE TEST

The performance test is designed to verify the overall operation of the Model 1952B. This test can be used as an acceptance check and/or periodic maintenance check. Table 4-1 lists the equipment required to perform this test. If the counter fails any part of the performance test, corrective action is indicated. Troubleshooting information for fault isolation is given later in this section.

4-20 Set-Up Procedure

4-21 Prior to executing the performance test complete the following set-up procedure:

a. Connect the Model 1952B to line power.

b. Rotate the CYCLE RATE control out of the POWER OFF detent and set to the maximum CW position. The front panel display should illuminate.

c. Set the rear panel EXT-INT switch to the INT position.

d. Set the CONT-TRIG switch to the CONT position.

e. Set the CHANNEL A and B Trigger Level Controls to the following positions:

1. Trigger Level Adjust to maximum counter clockwise, PRESET.

2. Slope switch to + (positive).

3. Coupling switch to AC.

4. Attenuator switch to X1.

4-22 Channel A Amplifier Test

4-23 Use the following procedure to test the CHANNEL A Amplifier and Trigger Level Controls:

a. Complete the Set-Up Procedure.

b. Connect a 2 V rms, 1 kHz signal source to CHANNEL A input connector.

c. Observe the CHANNEL A Status Indicators. Both should be illuminated.

d. Change the settings of the Slope, Coupling and Attenuator switches associated with CHANNEL A. The Status Indicators should both remain on.

e. Set the Attenuator switch to the X10 position and the Coupling switch to the AC position.

f. Move the Trigger Level Adjust just out of the Preset detent. The + Status Indicator should be on and the - indicator should be off.

g. Move the Trigger Level Adjust CW to the center of its travel. Both Status Indicators should be lit.

h. Move the Trigger Level Adjust to the maximum CW position. The - Status Indicator should be on the + indicator should be off.

4-24 Channel B Amplifier Test

4-25 Test the CHANNEL B amplifier using the general procedure given in the previous paragraph for testing the CHANNEL A amplifier. Substitute CHANNEL B in all cases where CHANNEL A is mentioned.

4-26 Display Test

4-27 Test the front panel display using the following procedure:

a. Complete the Set-Up Procedure.

b. Refer to Table 4-2 and sequentially select each of the RANGE switch settings for each setting of the FUNCTION switch. As each range is selected ensure that the numeric readout, decimal point location and measurement units are correctly displayed.

c. Set the FUNCTION switch to the A GTD.BY B position.

d. Connect a 1 V rms, 5 Hz signal source to the CHANNEL A Input Connector.

e. Set the CHANNEL A Coupling switch to DC.

f. Rotate the CHANNEL B Trigger Level Adjust CW out of the preset position until the display begins to accumulate the CHANNEL A input frequency. As each count is received, observe the least significant decade and ensure that each number is properly displayed.

g. Slowly increase the input frequency as necessary to check the operation of each decade. When all display decades reach a count of nine, the next input signal will overflow the counter and cause the display to read all zeros. In addition, an OVERFLOW LED is illuminated to indicate that the counter capability has been exceeded.

h. Press the RESET button. The display should reset to zero and the OVERFLOW indicator should be turned off. When the button is released the totalizing process starts over.

TABLE 4-2. RANGE DISPLAY INFORMATION FOR AVAILABLE FUNCTIONS

MODE	RANGE INDICATIONS	DISPLAY FOR AVAILABLE RANGE SETTINGS					
FREQ. A	GATE t DISPLAY MEAS. UNITS	0.1 ms .00 MHz	1.0 ms .000 MHz	10 ms .0000 MHz	0.1 s .00 kHz	1.0 s .000 kHz	10 s .0000 kHz
FREQ.(A/B)N	PER. AVGD. DISPLAY MEAS. UNITS	10^0 0 NONE	10^1 0 NONE	10^2 0 NONE	10^3 0 NONE	10^4 0 NONE	10^5 0 NONE
PERIOD A	PER. AVGD. DISPLAY MEAS. UNITS	10^0 .00 ms	10^1 .000 ms	10^2 .0000 ms	10^3 .00 us	10^4 .000 us	10^5 .0000 us
T.I. A-B	T.I. RES DISPLAY MEAS. UNITS	.1 us .0000 ms	1 us .000 ms	10 us .00 ms	0.1 ms .0000 s	1.0 ms .000 s	10 ms .00 s
A GTD.BY B	RANGE DISPLAY MEAS. UNITS	N/A NONE NONE	N/A NONE NONE	N/A NONE NONE	N/A NONE NONE	N/A NONE NONE	N/A NONE NONE
SELF CHECK	GATE t DISPLAY MEAS. UNITS	0.1 ms 10 NONE	1.0 ms 100 NONE	10 ms 1000 NONE	0.1 s 10000 NONE	1.0 s 100 000 NONE	10 s 1000 000 NONE

4-28 Frequency A Test

4-29 Test the Frequency A mode using the following procedure:

a. Complete the Set-Up Procedure.

b. Set the FUNCTION switch to the FREQ A position.

c. Connect a 1 V rms, 100 kHz signal source to the CHANNEL A Input Connector.

d. Set the RANGE switch to each of the GATE t positions listed in Table 4-3 and ensure the numeric and measurement units display are reading correctly.

Table 4-3. DISPLAY READOUT FOR FREQUENCY A TEST.

GATE t SETTING	NUMERIC READOUT*	MEASUREMENT UNITS
0.1 ms	.10	MHz
1.0 ms	.100	MHz
10 ms	.1000	MHz
0.1 s	100.00	kHz
1.0 s	100.000	kHz
10 s	100.0000	kHz

* The accuracy of the numeric readout is dependent on the frequency accuracy of the 100 kHz signal source.

4-30 Ratio Test

4-31 Test the Frequency (A/B)N ratio mode using the following procedure:

a. Complete the Set-Up Procedure.

b. Set the FUNCTION switch to the FREQ (A/B)N position.

c. Set the SEP - COM switch to the COM position.

d. Connect a 1 V rms, 100 kHz signal source to the CHANNEL A Input Connector.

e. Set the RANGE switch to each of the PER.AVGD (period averaged) positions listed in Table 4-4 and ensure that the display readout is correct for each position. Decimal point and measurement units information are not displayed in the ratio mode.

Table 4-4. DISPLAY READOUT FOR RATIO FUNCTION TEST.

PER AVGD. SETTING**	NUMERIC READOUT
10^0	1
10^1	10
10^2	100
10^3	1000
10^4	10000
10^5	100000

** A ratio of 1 is indicated in all cases. The decimal point is determined by starting at the right of the readout

counting to the left the number of places indicated by the exponent for the PER.AVGD. setting.

4-32 Period A Test

4-33 Check the period A mode using the following procedure:

a. Complete the Set-Up Procedure.

b. Set the FUNCTION switch to the FREQ A position.

c. Connect a 1 V rms, 10 kHz signal source to the CHANNEL A Input Connector.

d. Set the RANGE switch to the 10 s position and adjust the signal source to produce a display of 10.0000 kHz on the 1952B.

e. Set the FUNCTION switch to the PERIOD A position.

f. Set the RANGE switch to each of the PER.AVGD. positions listed in Table 4-5 and ensure that the display readout is correct for each position.

Table 4-5. DISPLAY READOUTS FOR PERIOD A TEST

PER.AVGD. SETTINGS	NUMERIC READOUT*	MEASUREMENT UNITS
10^0	.10	ms
10^1	.100	ms
10^2	.1000	ms
10^3	100.00	us
10^4	100.000	us
10^5	100.0000	us

* The accuracy of the readout is dependent on the frequency accuracy of the 10 kHz signal source.

4-34 Time Interval Test

4-35 Test the time interval mode using the following procedure:

a. Complete the Set-Up Procedure.

b. Set the FUNCTION switch to the FREQ A position.

c. Connect a 1 V rms, 100 Hz signal source to the CHANNEL A Input Connector.

d. Set the RANGE switch to the 10 s position and adjust the signal source to produce a display of 0.1000 kHz on the 1952B.

e. Set the FUNCTION switch to the T.I. A-B position.

f. Set the SEP - COM switch to the COM position.

g. Set the RANGE switch to each of the T.I. RES. (time interval resolution) positions listed in Table 4-6 and ensure that the display readout is correct for each position.

Table 4-6. DISPLAY READOUTS FOR TIME INTERVAL TEST

T.I. RES SETTING	NUMERIC READOUT*	MEASUREMENT UNITS
0.1 us	10.0000	ms
1.0 us	10.000	ms
10 us	10.00	ms
0.1 ms	.0100	s
1.0 ms	.010	s
10 ms	.01	s

* The accuracy of the readout is dependent on the frequency accuracy of the 100 Hz signal source.

4-36 Totalize Test

4-37 Test the totalize mode (A gated by B) using the following procedure:

a. Complete the Set-Up Procedure.

b. Set the FUNCTION switch to the A GTD BY B position.

c. Connect a 1 V rms, 100 Hz signal source to the CHANNEL A Input Connector.

d. Rotate the CHANNEL B Trigger Level Adjust out of the detent position until the -(minus) Status Indicator is illuminated.

e. Press the RESET button. The display should return to 0 (zero). Decimal point and measurement units are not displayed.

f. Rotate the CHANNEL B Trigger Level Adjust until the + (plus) Status Indicator is illuminated. The counter should begin to accumulate and display the 100 Hz input frequency present on the CHANNEL A Input Connector.

g. Rotate the CHANNEL B Trigger Level Adjust until the - Status Indicator is illuminated. The counter should stop and display the accumulated total.

h. Repeat steps f. and g. above. The accumulation process should continue as long as the + Status Indicator is illuminated. Notices that as steps f. and g. are repeated the displayed total is not reset. Instead, new inputs are added to the previous total.

4-38 Self Check Test

4-39 Test the self check mode using the following Procedure:

a. Complete the Set-Up Procedure.

b. Set the FUNCTION switch to the SELF CHECK position.

c. Set the RANGE switch to each of the GATE t positions listed in Table 4-7 and ensure that the display readout is correct for each position. Decimal point and measurement units are not displayed.

Table 4-7. DISPLAY READOUT FOR SELF CHECK TEST

GATE t SETTINGS	NUMERIC READOUT
0.1 ms	10
1.0 ms	100
10 ms	1000
0.1 s	10000
1.0 s	100000
10 s	1000000

4-40 CALIBRATION

4-41 The Model 1952B should be calibrated every 90 days or whenever repairs have been made. Calibration should be done at an ambient room temperature of 25°C ± 5°C. Table 4-1 lists the required equipment.

4-42 Trigger Level Preset Adjust

4-43 Use the following procedure to calibrate the CHANNEL A and B preset trigger levels:

a. Remove the top dust cover from the Model 1952B.

b. Connect the 1952B to line power.

c. Turn-on the 1952B using the CYCLE RATE switch.

d. Set the CHANNEL A and B Attenuator switches to the X10 position.

e. Turn the CHANNEL A and B Trigger Level Controls to the maximum counter-clockwise position (PRESET).

f. Locate the potentiometer directly behind the CHANNEL A Trigger Level Adjust on the main PCB Assembly.

g. Using a suitable screw driver, rotate the potentiometer until the CHANNEL A -(negative) Status indicator is illuminated. Reverse direction, and slowly turn the pot until the + (positive) Status Indicator just turns-on.

h. Click the CHANNEL A Trigger Level Adjust in and out of the PRESET detent. The + Status indicator should always illuminate in the preset detent.

i. Locate the potentiometer directly behind the CHANNEL B Trigger Level Adjust on the main PCB Assembly.

j. Calibrate the CHANNEL B preset trigger level using the procedures given in steps g. and h. above. Substitute CHANNEL B where CHANNEL A is mentioned.

4-44 Timebase Adjust

4-45 Calibrate the 10 MHz timebase oscillator using the following procedure:

a. Remove the top dust cover from the Model 1952B.

b. Connect the 1952B to line power.

c. Turn-on the 1952B using the POWER/CYCLE RATE switch.

d. Set the FUNCTION switch to the FREQ A position.

e. Set the CONT - TRIG switch to the CONT position.

f. Connect the 1 MHz frequency standard to the CHANNEL A Input Connector.

g. Adjust the CHANNEL A trigger level controls to establish the desired trigger condition. Proper triggering occurs when the + and - Status Indicators are both illuminated.

h. Set the RANGE switch to the 10 s GATE t position.

i. Press and release the RESET switch. After ten seconds the 1952B should display a count of 000.0000 Hz and the OVERFLOW LED should be illuminated.

j. If the display is incorrect, locate the TIMEBASE adjustment at the BOTTOM rear of the Main Board PCB Assembly, and using an insulated tuning tool, adjust the oscillator tuning screw.

k. Repeat steps i. and j. of this procedure until the proper display is obtained.

4-46 TROUBLESHOOTING

4-47 The following information is designed to aid in troubleshooting the Model 1952B. Fault isolation is achieved by executing the performance test and isolating the problem to a functional logic group using troubleshooting flowcharts. The flowchart symbols are defined in Figure 4-1 and the troubleshooting flowcharts are given in Figure 4-2.

Figure 4-1. TROUBLESHOOTING FLOWCHART SYMBOLS

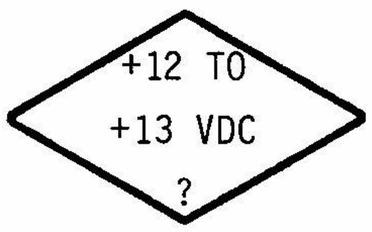
INTERCONNECTS FLOWCHART SYMBOLS AND INDICATES THE DIRECTION OF PROCEDURE FLOW.

START

A TERMINAL POINT - USED TO INDICATE A START, STOP OR INTERRUPT OF PROCEDURE FLOW.

MEASURE DC VOLTAGE ACROSS CAPACITOR C 1

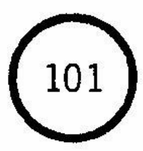
INSTRUCTION OR PROCEDURAL STEP.



DECISION REQUIRED - ALWAYS ASK A QUESTION - THE FLOW FROM THE SYMBOL IS DEPENDENT UPON THE DECISION MADE - USUALLY YES (GO) OR NO (NO GO), BUT MAY HAVE THREE POSSIBILITIES (eg. HIGH, LOW, NORMAL).

SEE FIGURE 2 FOR THE LOCATION OF THE +5 VDC ADJUSTMENT, R8

ANNOTATION - PROVIDES ADDITIONAL INFORMATION FOR THE SYMBOL TO WHICH IT IS CONNECTED. ALWAYS CONNECTED BY A DASHED LINE.



CONNECTOR - EXIT TO, OR ENTRY FROM, ANOTHER PART OF THE FLOWCHART (ASSOCIATED ARROWHEAD SHOWS WHICH).

PERFORM REGULATION TEST

PREDEFINED PROCESS - INDICATES THAT THE PROCEDURE TO BE PERFORMED IS DESCRIBED ELSEWHERE.

Figure 4-2. TROUBLESHOOTING FLOWCHART (Sheet 1 of 6)

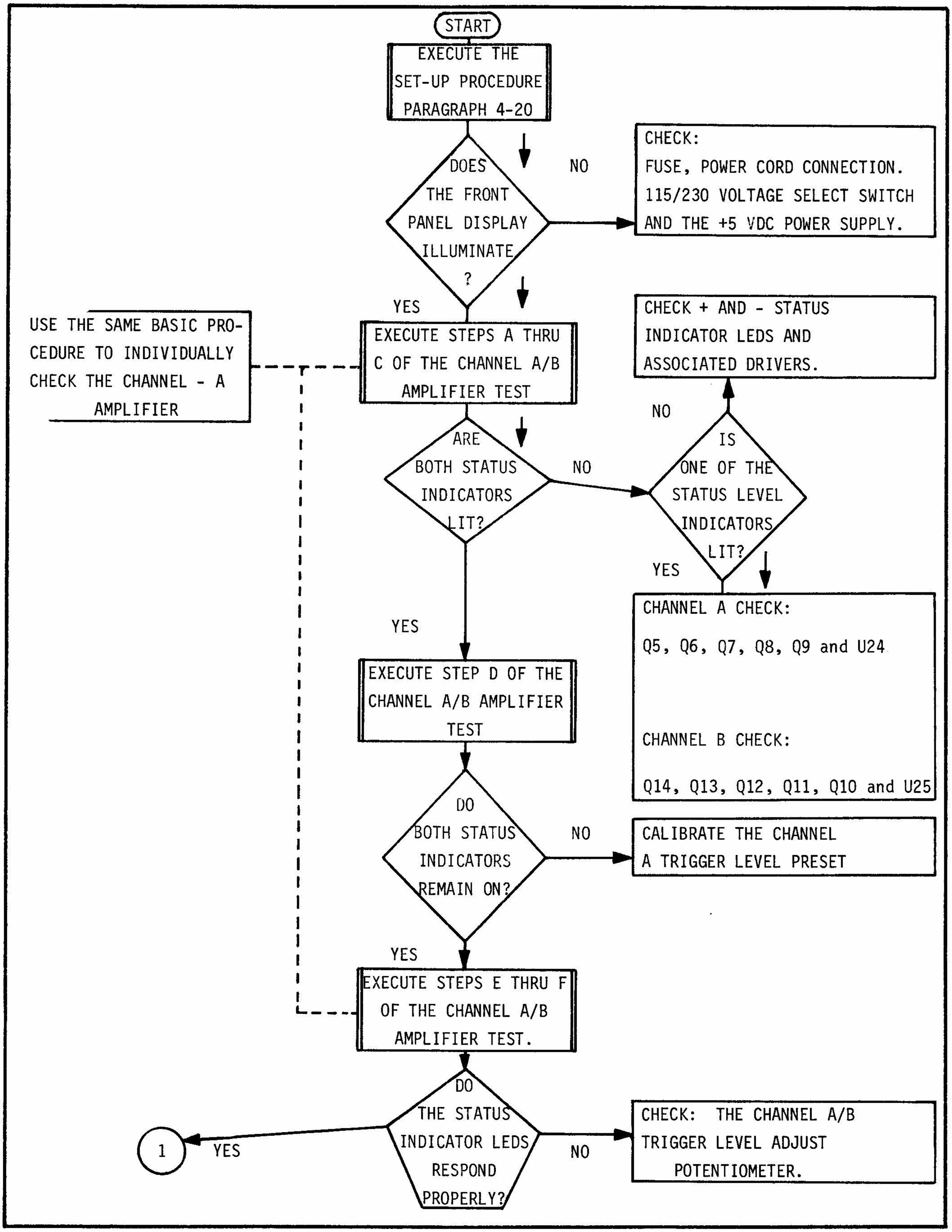


Figure 4-2. TROUBLESHOOTING FLOWCHART (Sheet 2 of 6)

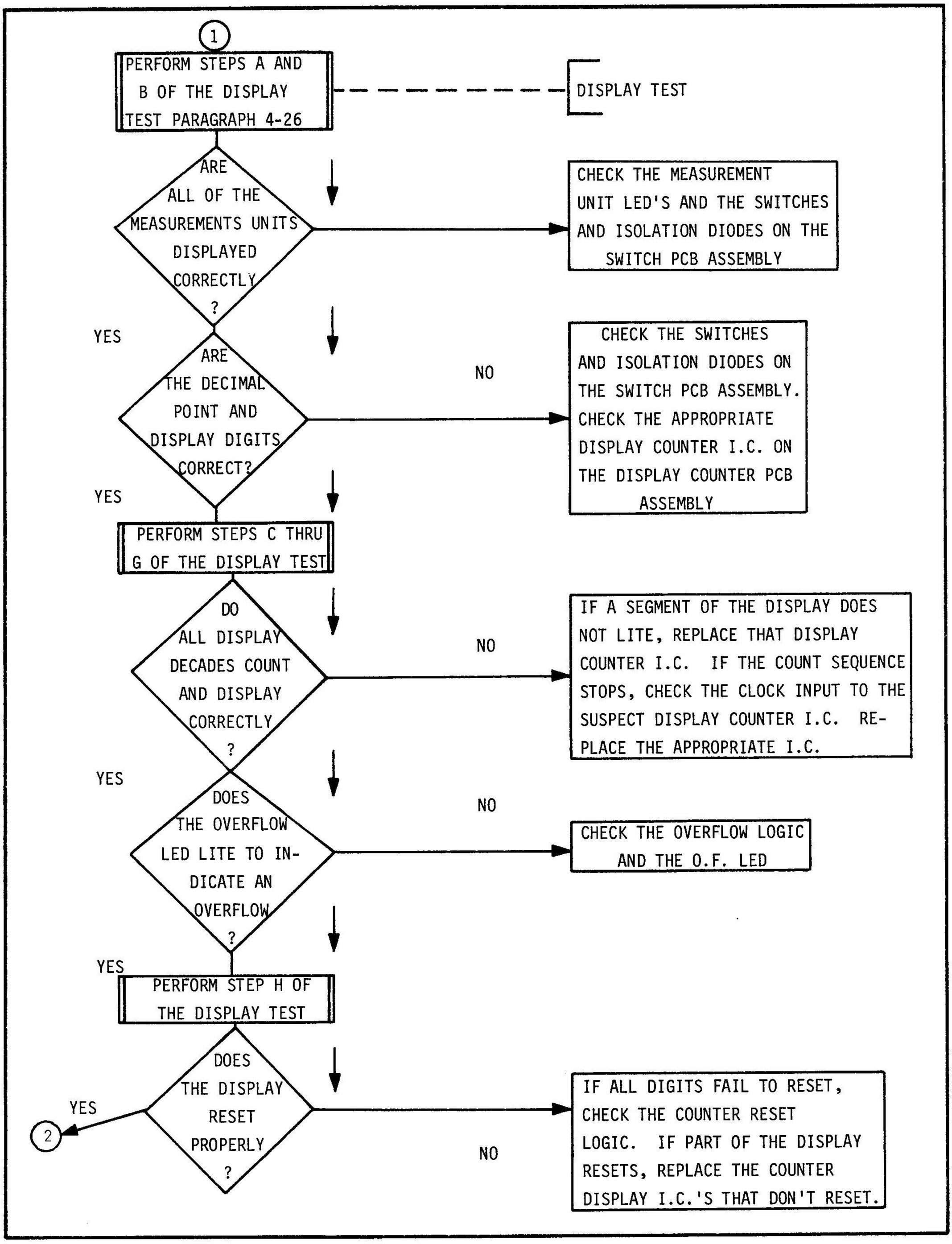


Figure 4-2. TROUBLESHOOTING FLOWCHART (Sheet 3 of 6)

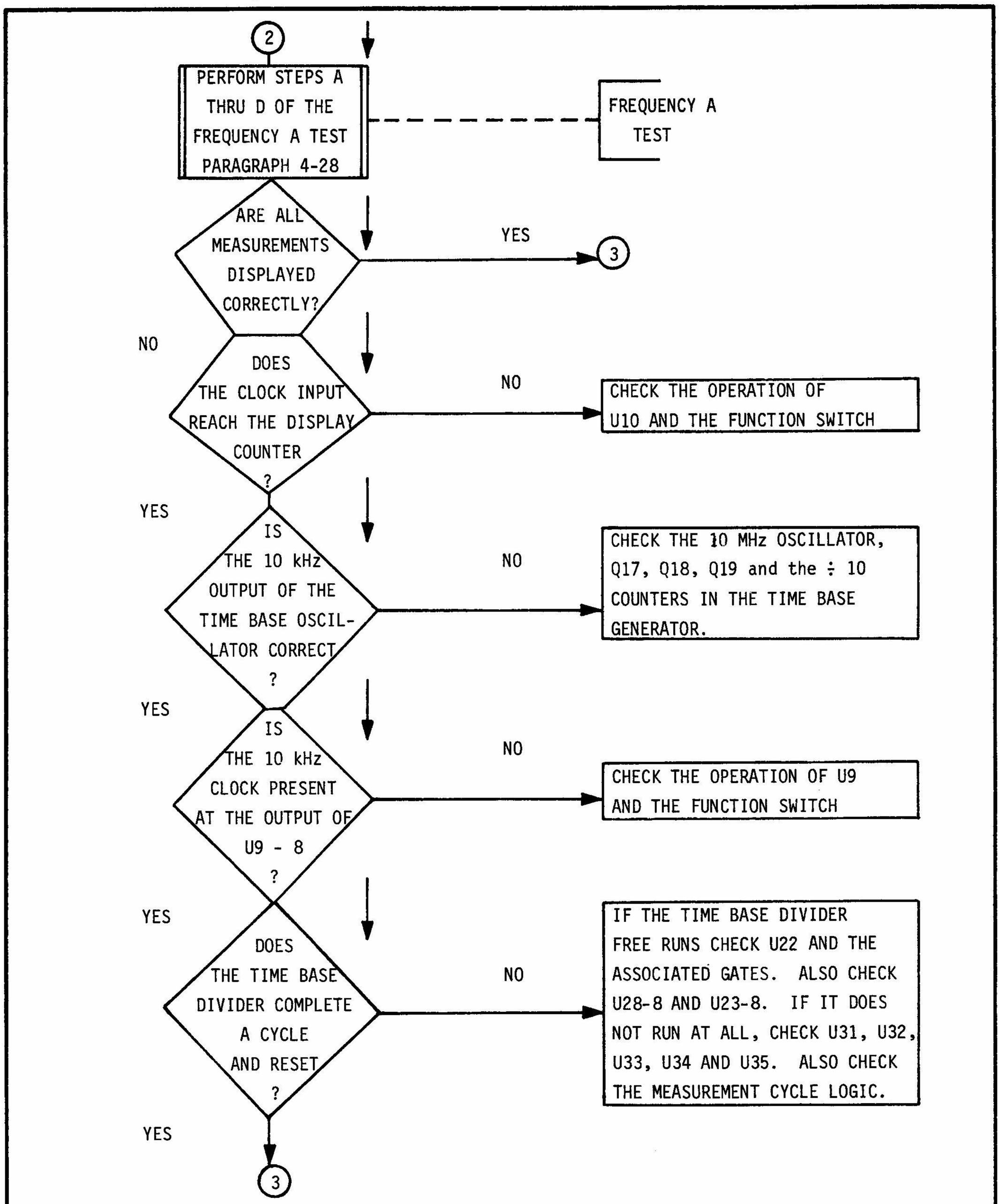


Figure 4-2. TROUBLESHOOTING FLOWCHARTS (Sheet 4 of 6)

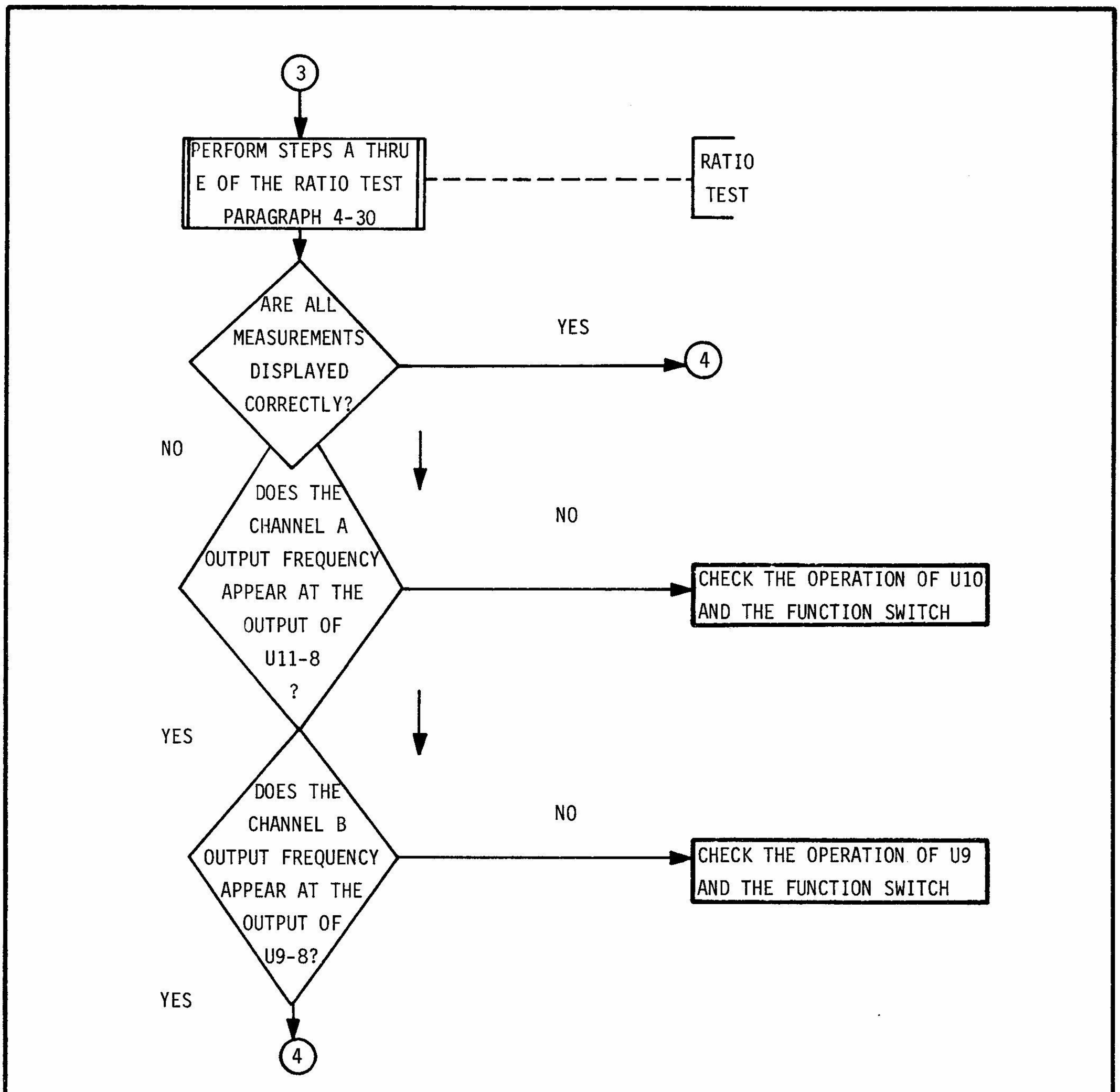


Figure 4-2. TROUBLESHOOTING FLOWCHARTS (Sheet 5 of 6)

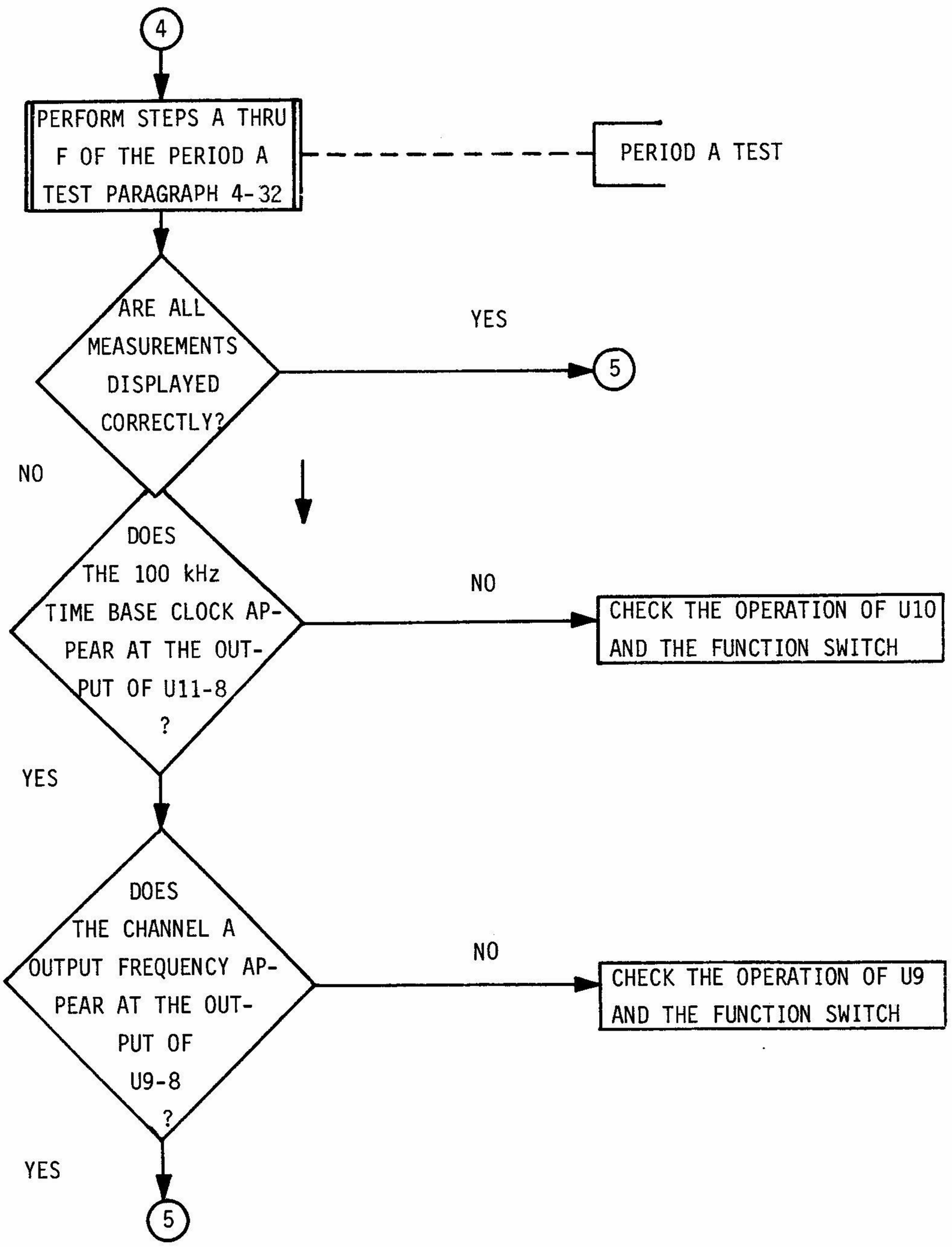
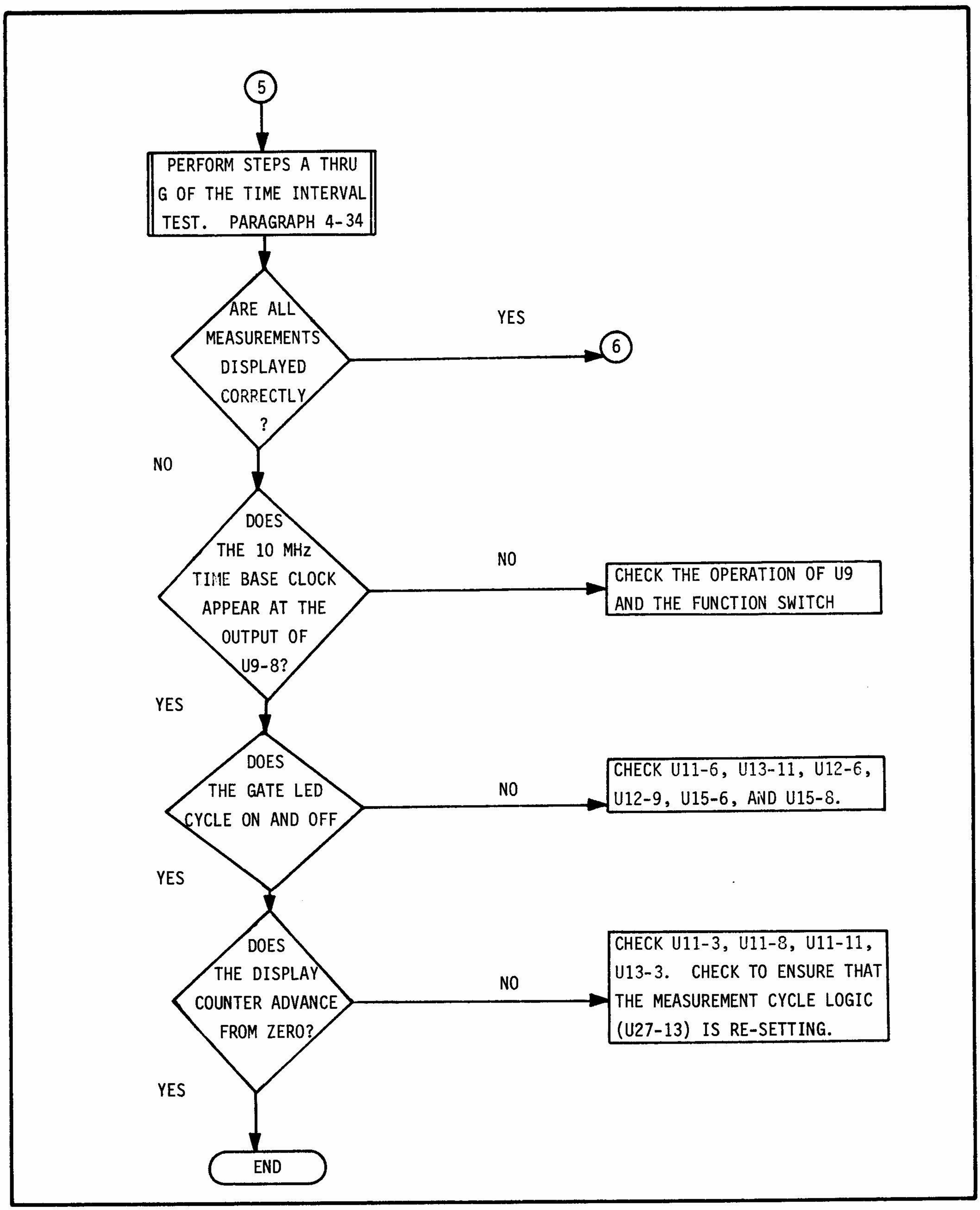


Figure 4-2. TROUBLESHOOTING FLOWCHARTS (Sheet 6 of 6)



Section 5

Lists of Replaceable Parts

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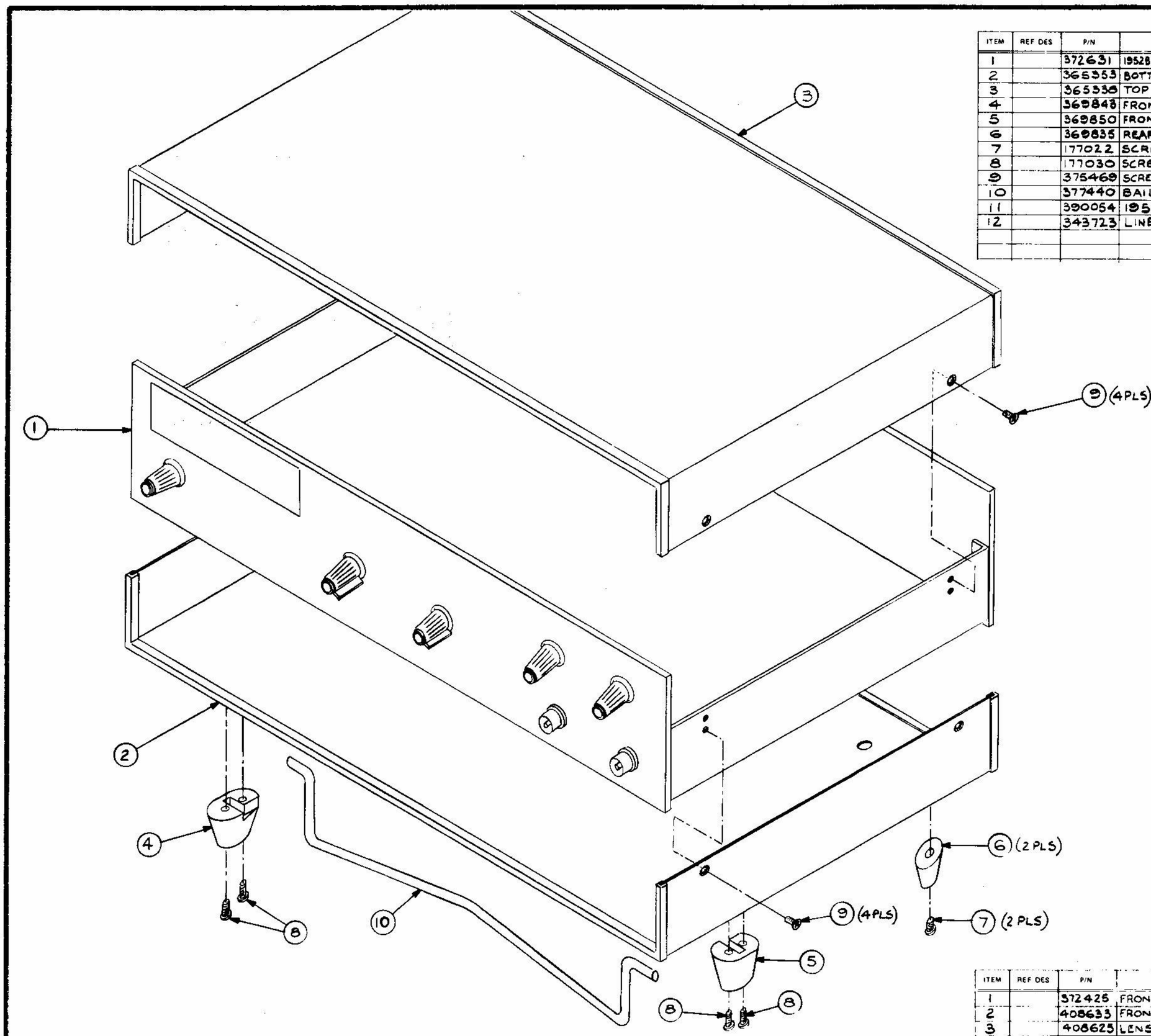
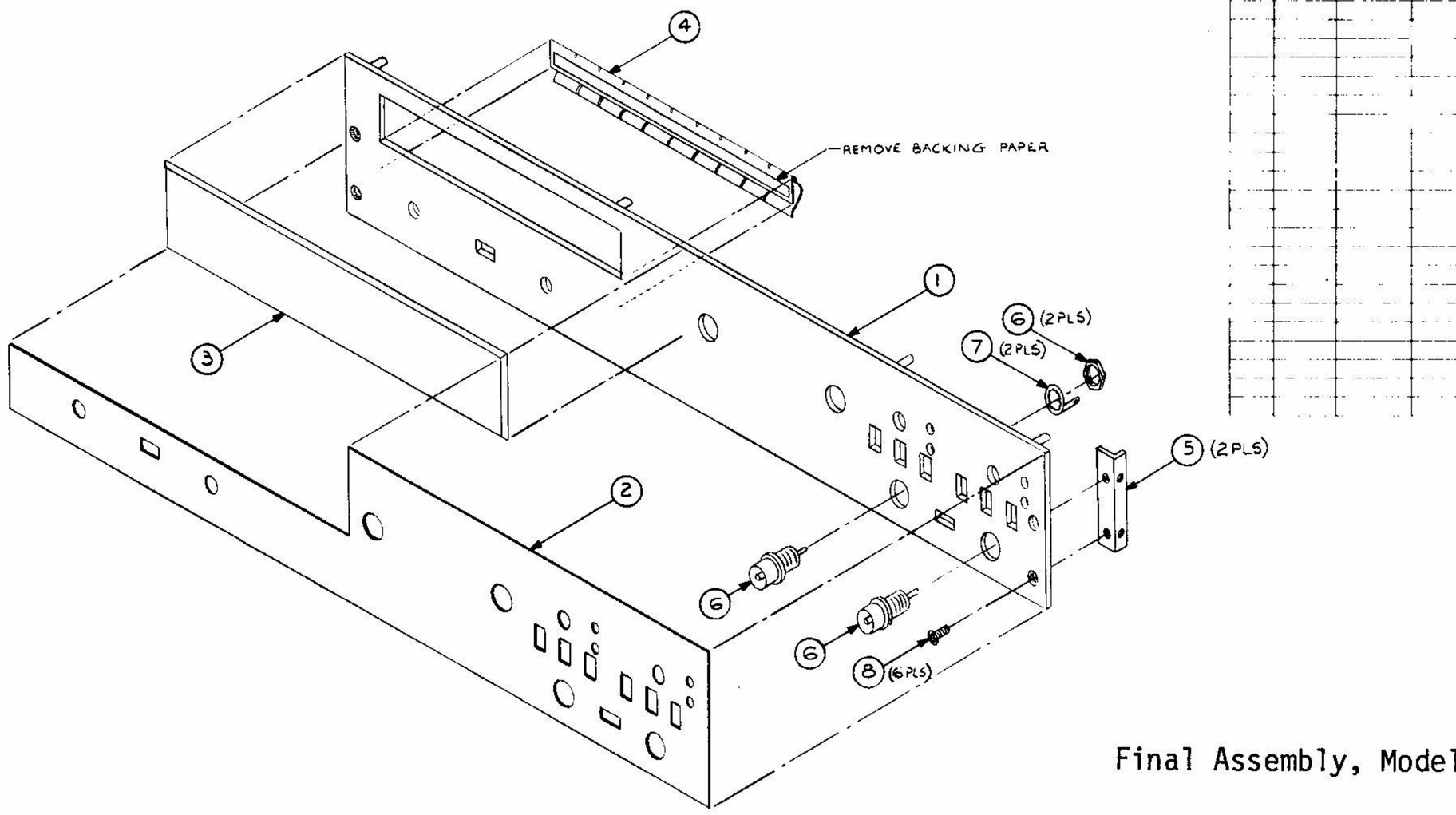
Display Board PCB Assembly	5-5
Input Board PCB Assembly	5-6
Switch Board PCB Assembly	5-7
Front Panel Assembly	5-8
Rear Panel Assembly	5-9

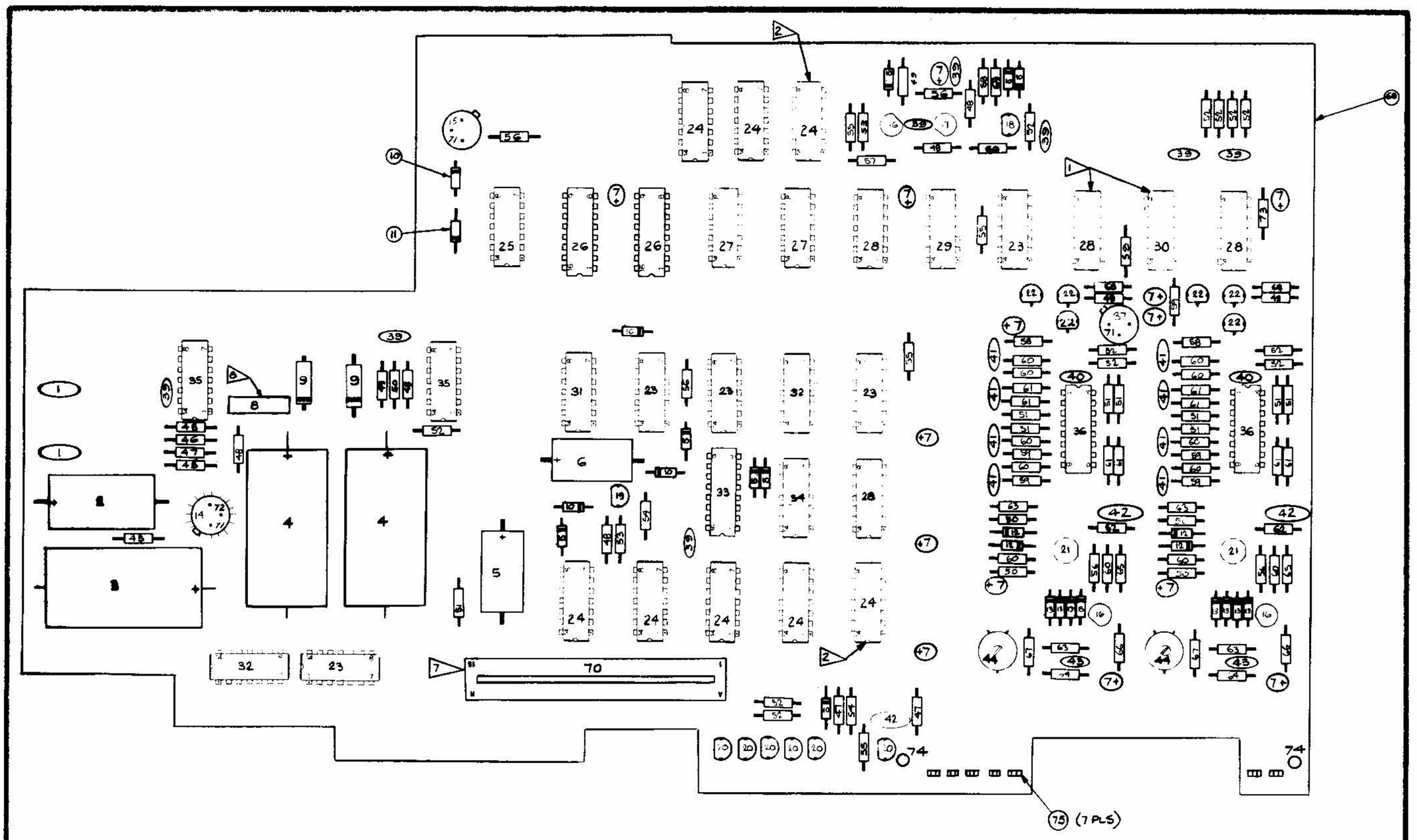
ITEM	REF DES	P/N	NOMENCLATURE	QTY
1		372631	1952B-5001 FINAL ASSY	1
2		365353	BOTTOM COVER	1
3		365338	TOP COVER	1
4		369843	FRONT LEFT FOOT	1
5		369850	FRONT RIGHT FOOT	1
6		369835	REAR FOOT	2
7		177022	SCREW #6-32 × 3/8 PHP	2
8		177030	SCREW #6-32 × 1/2" PHP	4
9		375469	SCREW #4-40 × 1/4 UNDERCUT	8
10		377440	BAIL 11"	1
11		390054	1952B MANUAL	1
12		343723	LINE CORD	1



ITEM	REF DES	P/N	NOMENCLATURE	QTY
1		372425	FRONT PANEL, METAL	1
2		408633	FRONT PANEL DECAL	1
3		408625	LENS	1
4		390567	HEAT SINK	1
5			19528-8013 BRACKET	2
6		178127	BNC JACK	2
7		167015	GROUND LUG 3/8"	2
8		375469	SCREW #4-40×1/4 FHPCSK	4



Final Assembly, Model 1952B

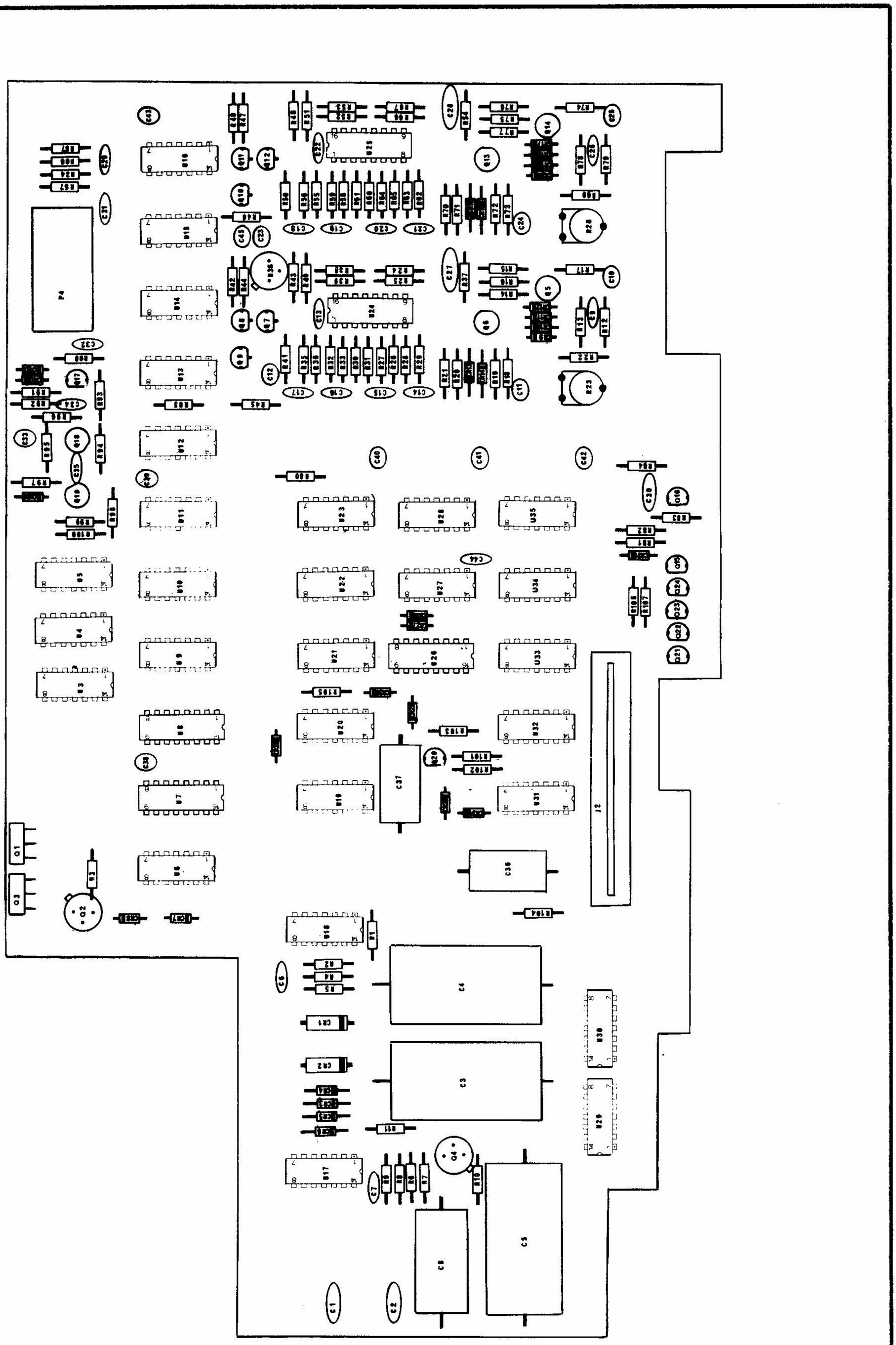


ITEM	REF DES	P/Nº	NOMENCLATURE	QTY
1	C1, C2	188003	CAPACITOR .005uf 3000V	2
2	C8	168153	470uf 25V	1
3	C5	234008	2200uf 25V	1
4	C3, C4	379370	4700uf 10V	2
5	C36	192914	100 uf 16V	1
6	C37	369181	220 uf 16V	1
7	10	193623	10 uf 16V	14
8	CR3-CR6	296509	DIODE BRIDGE KBP02	
9	CR1, CR2	187716	IN4720	2
10	11	364224	IN914	12
11	CR7	159798	ZENER IN751A	1
12	CR13-CR16	369595	FH1100 OR HPA3082-2835	4
13	12	381806	IN3062	8
14	Q4	246462	TRANSISTOR 2N2905	1
15	Q2	150359	2N3053	1
16	Q6, Q11, Q13	369645	2N4274	3
17	Q18	369603	2N4257	1
18	Q17	386094	SX 3819	1
19	Q20	369652	SX 4061	1
20	15	369678	TIS 98	6
21	Q6, Q13	379271	E421	2
22	Q7-Q12	369629	2N5771	6
23	14	292953	I.C. 7400	5
SEE SHT 1 OF 2 → 24	16	293159	7490	8
25	U6	363457	74S11	1
26	U7, U8	363440	74S112	2
27	U9, U10	379289	74S64	2
28	5	363580	74S00	4
29	U12	310227	7474	1
30	U15	413617	74S04	1
31	U19	292995	7410	1
32	U22, U29	368571	7473	2
33	U26	293142	7442	1
34	U27	320754	74196	1
35	U17, U18	379420	723	2
36	U24, U25	369702	10116	2
37	U36	375790	LM309H	1
38				
39	17	369140	CAPACITOR .001 uf	8

ITEM	REF DES	P/Nº	NOMENCLATURE	QTY
40	C22, C36	369579	CAPACITOR .01uf 50V	2
41	C14-C21	369157	22pf 500V	8
42	C27, C28, C30	369199	.1 uf 25V	3
43	C9, C26	369132	47pf 500V	2
44	R23, R68	369553	RESISTOR TRIMPOT 10K	2
45	R7, R9, R10	170720	1/4W; 5% CC 2K7	3
46	R8	148098	6K8	1
47	R6, R81, R84	148056	3K3	3
48	18	147868	10	5
49	19	148049	2K2	4
50	20	190371	1K2	5
51	21	148015	820	9
52	22	148023	1K	12
53	R99, R101	147975	390	2
54	R82, R103	193441	180K	2
55	23	147918	68	4
56	24	148007	680	5
57	R98	148114	15K	1
58	R41, R50, R92	160804	270	3
59	25	147959	220	6
60	26	147983	470	13
61	27	148031	1K5	8
62	R37, R54	148130	22K	2
63	28	147942	180	6
64	R12, R79	148189	100K	2
65	R15, R76	148072	4K7	2
66	R17, R74	175042	1K8	2
67	R22, R69	198390	2M2	2
68		372490	19528-3001 MAIN PCB	1
69	R91	182204	RESISTOR 1/4W; 5% CC 1M	1
70	J2	369884	30PIN EDGE CONN.	1
71	Q2, Q4, U36	152207	TRANSI-PAD	3
72	Q4	370155	HEAT SINK	1
73	R88	147926	RESISTOR 1/4W; 5% CC 100	1
74		375840	GUIDE PINS	2
75		376574	AMP PINS	7

NOTES:

1. C10, C11, C12, C23, C24, C25, C33, C38, C39, C40, C41, C42, C43, C45.
2. CR8, CR31, CR21, CR22, CR23, CR24, CR25, CR26, CR27, CR28, CR29, CR30.
3. CR9, CR10, CR11, CR12, CR17, CR18, CR19, CR20.
4. Q15, Q16, Q21, Q22, Q23, Q24.
5. U13, U20, U21, U23, U30.
6. U3, U4, U5, U31, U32, U33, U34, U35.
7. U11, U14, U16, U28.
8. C6, C7, C29, C31, C32, C34, C35, C44.
9. R2, R94, R96, R102, R11.
10. R5, R44, R47, R97.
11. R4, R18, R20, R71, R73.
12. R30, R31, R38, R39, R52, R53, R60, R61, R104.
13. R1, R34, R40, R43, R48, R51, R57, R86, R87, R90, R106, R107.
14. R80, R83, R85, R100.
15. R14, R77, R95, R105, R3.
16. R26, R29, R45, R46, R62, R65.
17. R16, R19, R27, R28, R35, R36, R55, R56, R63, R64, R72, R75, R93.
18. R24, R25, R32, R33, R58, R59, R66, R67.
19. R13, R21, R42, R49, R70, R78.

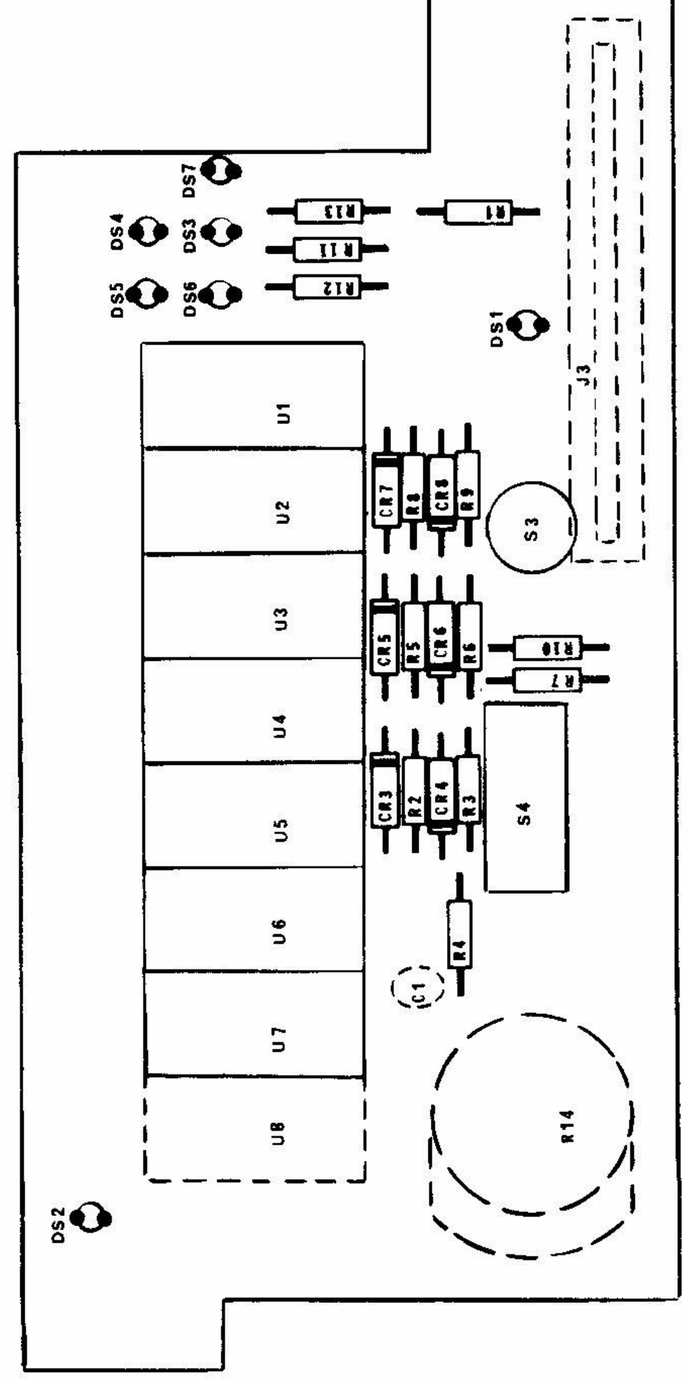


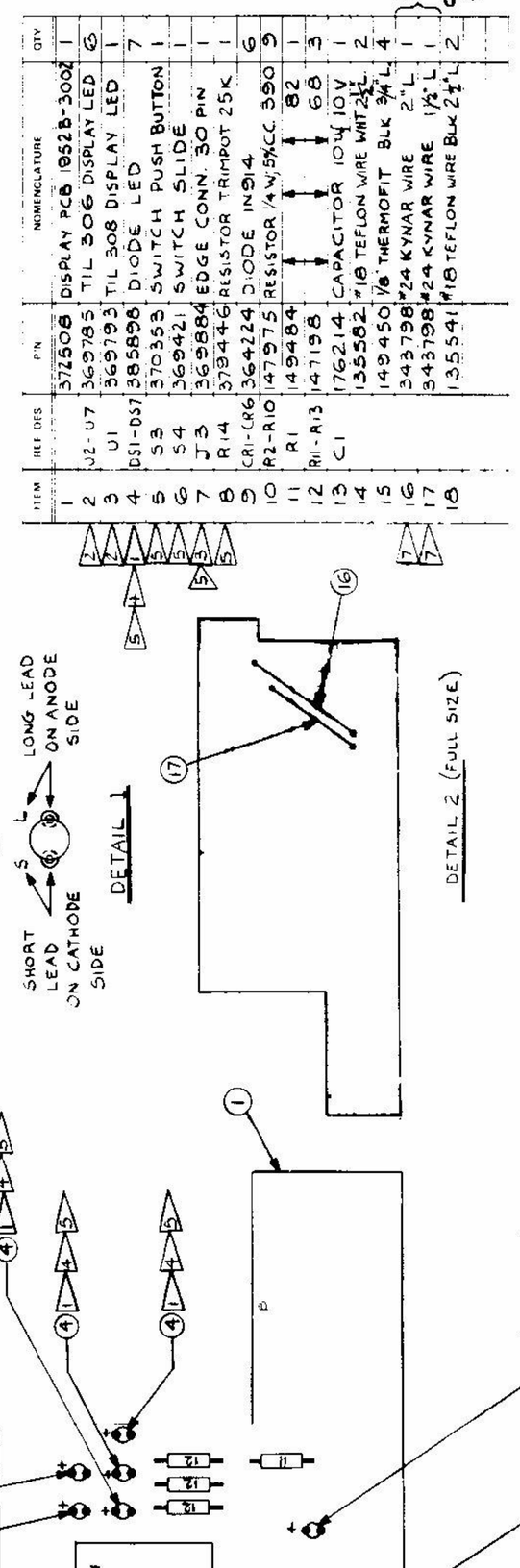
Main Board PC Assembly - Component Designation

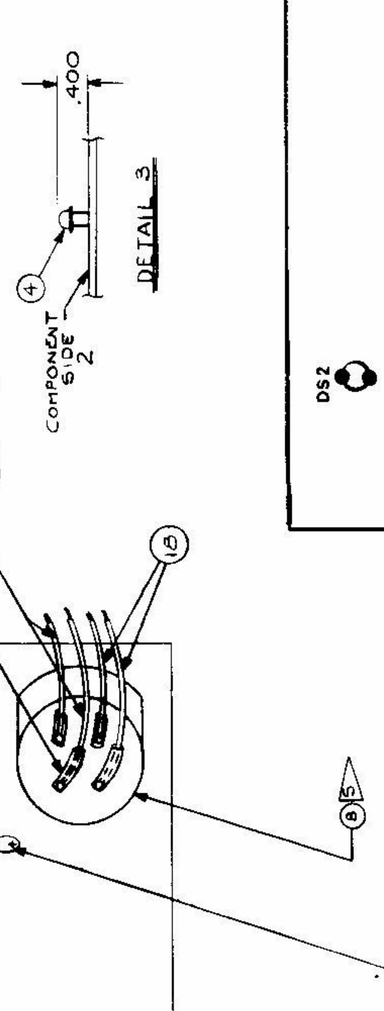
ITEM	REF DES	P'N	NOMENCLATURE	QTY
1		372508	DISPLAY PCB 1952B-3002	1
2	U2-U7	369785	TIL 306 DISPLAY LED	6
3	U1	369793	TIL 308 DISPLAY LED	1
4	DS1-DS7	385898	DIODE LED	7
5	S3	370353	SWITCH PUSH BUTTON	1
6	S4	369421	SWITCH SLIDE	1
7	J3	369884	EDGE CONN. 30 PIN	1
8	R14	379446	RESISTOR TRIMPOT 25K	1
9	CR1-CR6	364224	DIODE IN914	6
10	R2-R10	47975	RESISTOR 1/4W 5%CC 390	9
11	R1	149484	82	1
12	R11-R13	147198	68	3
13	C1	176214	CAPACITOR 10µ 10V	1
14		135582	#18 TEFLON WIRE WHT 2½"	2
15		149450	⅛" THERMOFIT BLK ¾" L	4
16		343798	#24 KYNAR WIRE 2" L	1 FOR 1952B-03
17		343798	#24 KYNAR WIRE 1½" L	1
18		135541	#18 TEFLON WIRE BLK 2½" L	2 OPTION ONLY.

NOTES:

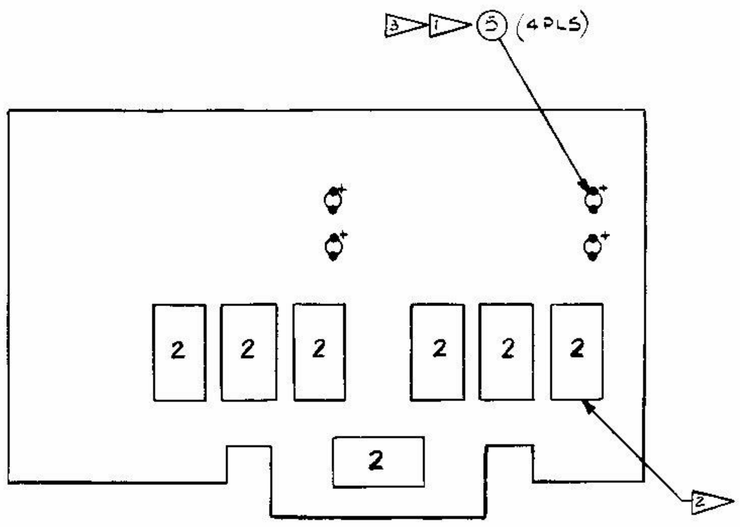
1. SEE DETAIL 1
2. ITEM 2&3 TO BE INSTALLED WITH DISPLAY SECTION AT TOP AND APPROX. .030 CLEARANCE FROM PCB FOR VENTILATION.
3. ITEM 7 MUST HAVE ITS EARS REMOVED.
4. SEE DETAIL 3
5. ITEMS 4, 5, 6, 7 & 8 MUST BE INSTALLED PERPINDICULAR TO PCB.
6. FOR 1952B-03 OPTION, INSTALL U8 & SEE DETAIL 2 FOR JUMPER WIRES.
7. FOR 1952B-03 OPTION, JUMPER W3 TO A1 AND W4 TO B1. CUT LAND WHERE SHOWN
8. REFERENCE SCHEMATIC 1952B-1002
9. ASSEMBLE PER S.O.P. 111.8
10. TRIM LEADS .065 LONG MAX PER SOP 89.15.
11. CLEAN ASSEMBLY PER S.O.P. 89.4, HANDLE CLEANED ASSY BY EDGES ONLY.
12. FOR 1952B-17 OPTION JUMPER W3 TO A1 AND W4 TO B1. CUT LAND WHERE SHOWN.
13. ITEM 2 MUST HAVE SAME CODING, U2 THRU U7.

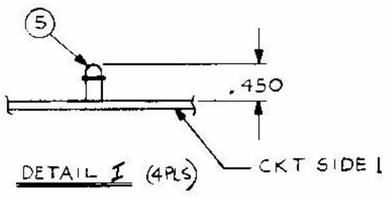




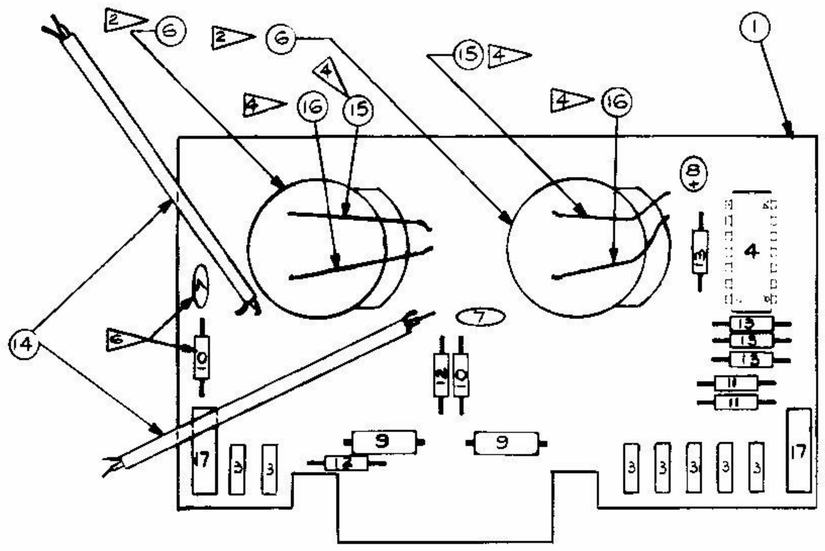


Display Board PC Assembly



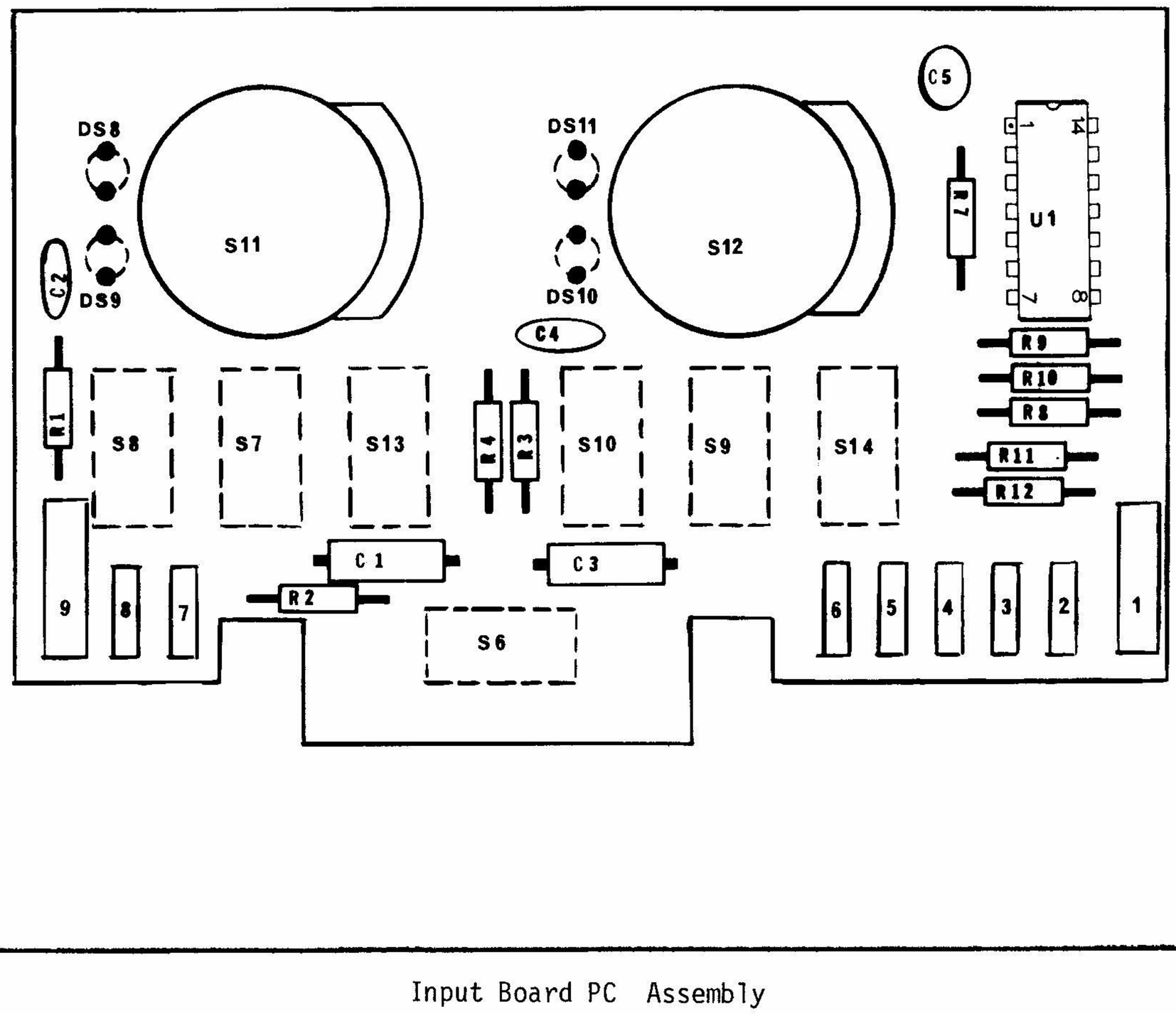


ITEM	REF DES	P N	NOMENCLATURE	QTY
1		372524	INPUT PCB 1952B-3004	1
2	5	380113	SLIDE SWITCH	7
3	P1	375329	AMP PIN RECEPTACLE	7
4	U1	413617	I.C. 74S04	1
5	DS8-DS11	385898	DIODE LED	4
6	R5, R6	370247	TRIMPOT 10K	2
7	C2, C4	105908	CAPACITOR 1pf/250V	2
8	C5	193623	10µ/10V	1
9	C1, C3	161992	.1µ/250V	2
10	R1, R3	285338	RESISTOR ¼W, 5% CC 910K	2
11	R11, R12	193441	180K	2
12	R2, R4	148189	100K	2
13	R7-R10	149484	82	4
14		190322	COAX CABLE 2¾" L.	2
15		115808	#22 TEFLON, VIOLET 2" L.	2
16		115766	#22 TEFLON, GRAY 2" L.	2
17	P1	49112	CONN TEST JACK	2

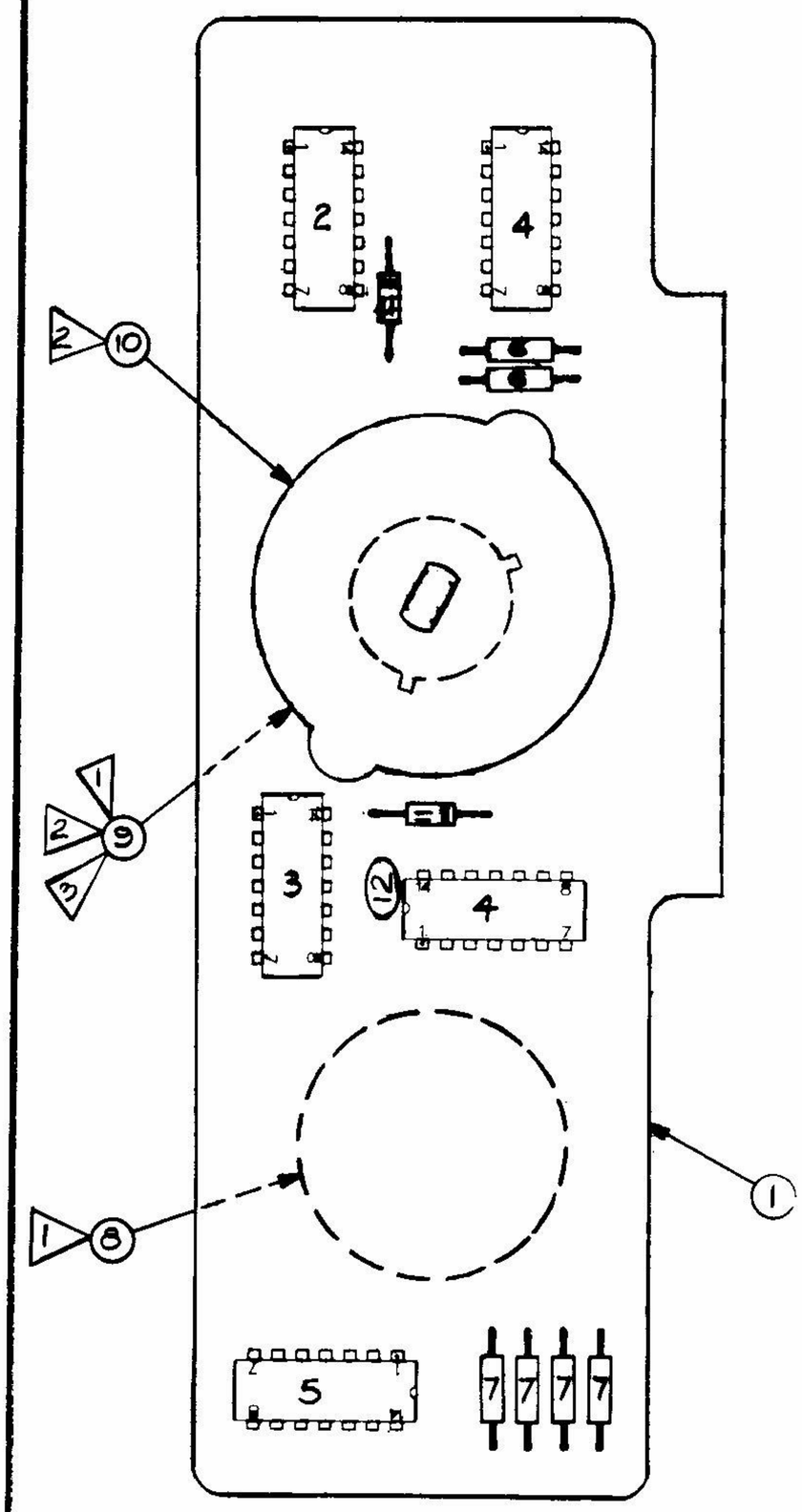


NOTES:

1. THE POSITIVE LEAD OF ITEM 5 IS THE LONG LEAD.
2. ITEMS 2 & 6 MUST BE INSTALLED PERPINDICULAR TO PCB.
3. SEE DETAIL I
4. FINISHED LENGTH OF CABLE 2"
5. SG 57, 58, 59, S10, S13 & S14.
6. TRIM LEADS OF THESE COMPONENTS AS CLOSE TO PCB AS POSSIBLE.
7. SEE SHT 2 OF 2 FOR WIRE LIST



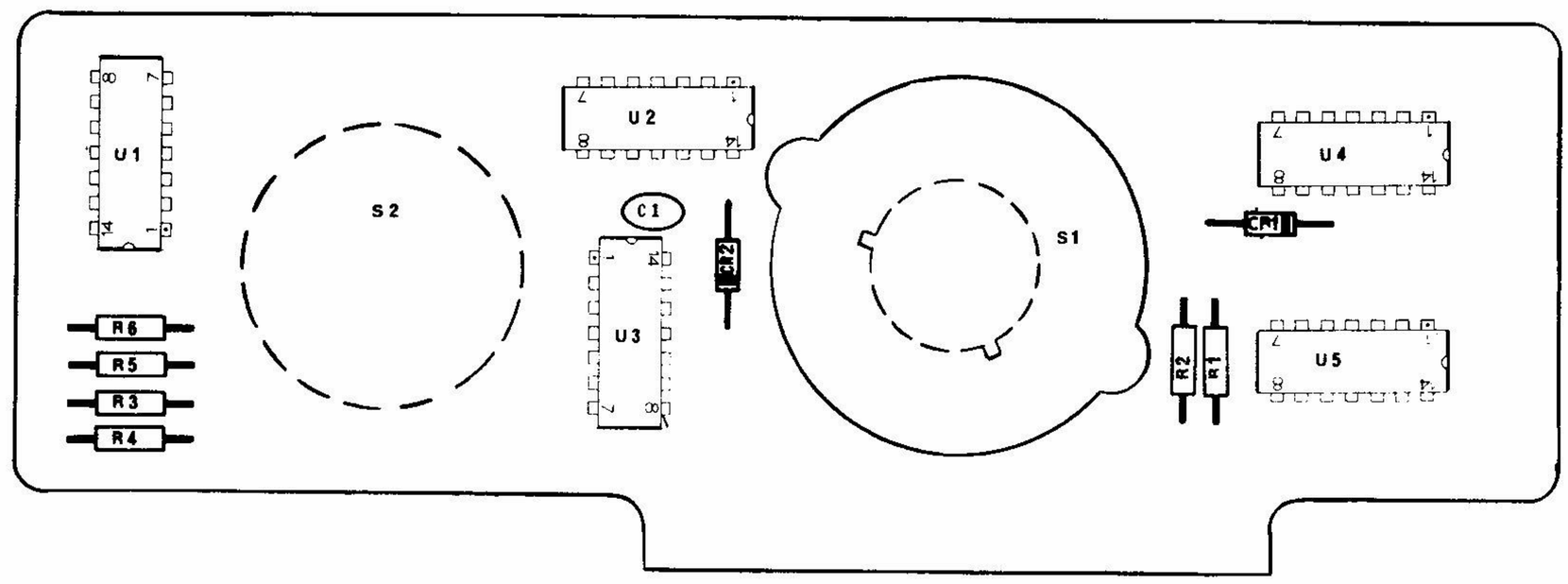
Input Board PC Assembly



ITEM	REF DES	P/N	NOMENCLATURE	QTY
1		372516	1952B-3003 SWITCH PCB	1
2	U4	292953	I.C. 7400	1
3	U2	292979	7404	1
4	U3, U5	292995	7410	2
5	U1	293019	7430	1
6	R1, R2	147959	RESISTOR, 1/4 W, 5% CC. 220	2
7	R3-R6	148023	RESISTOR, 1/4W, 5% CC. 1K	4
8	S2	412957	FUNCTION SWITCH	1
9	S1	413567	RANGE SWITCH	1
10	S1	413872	SWITCH WAFER	1
11	CR1, CR2	364224	DIODE IN914	2
12	C1	193623	CAPACITOR 10uf 16V	1

NOTES:

1. INSTALL ITEMS 8 & 9 ON CKT 1 SIDE.
2. ROTAR OF ITEM 10 MUST BE CLOCKED CORRECTLY WITH SHAFT OF ITEM 9
3. ITEM 9 IS SHOWN AT ITS CCW EXTREME POSITION WHEN VIEWED FROM CKT 1 SIDE.
4. REFERENCE SCHEMATIC 1952B-1003

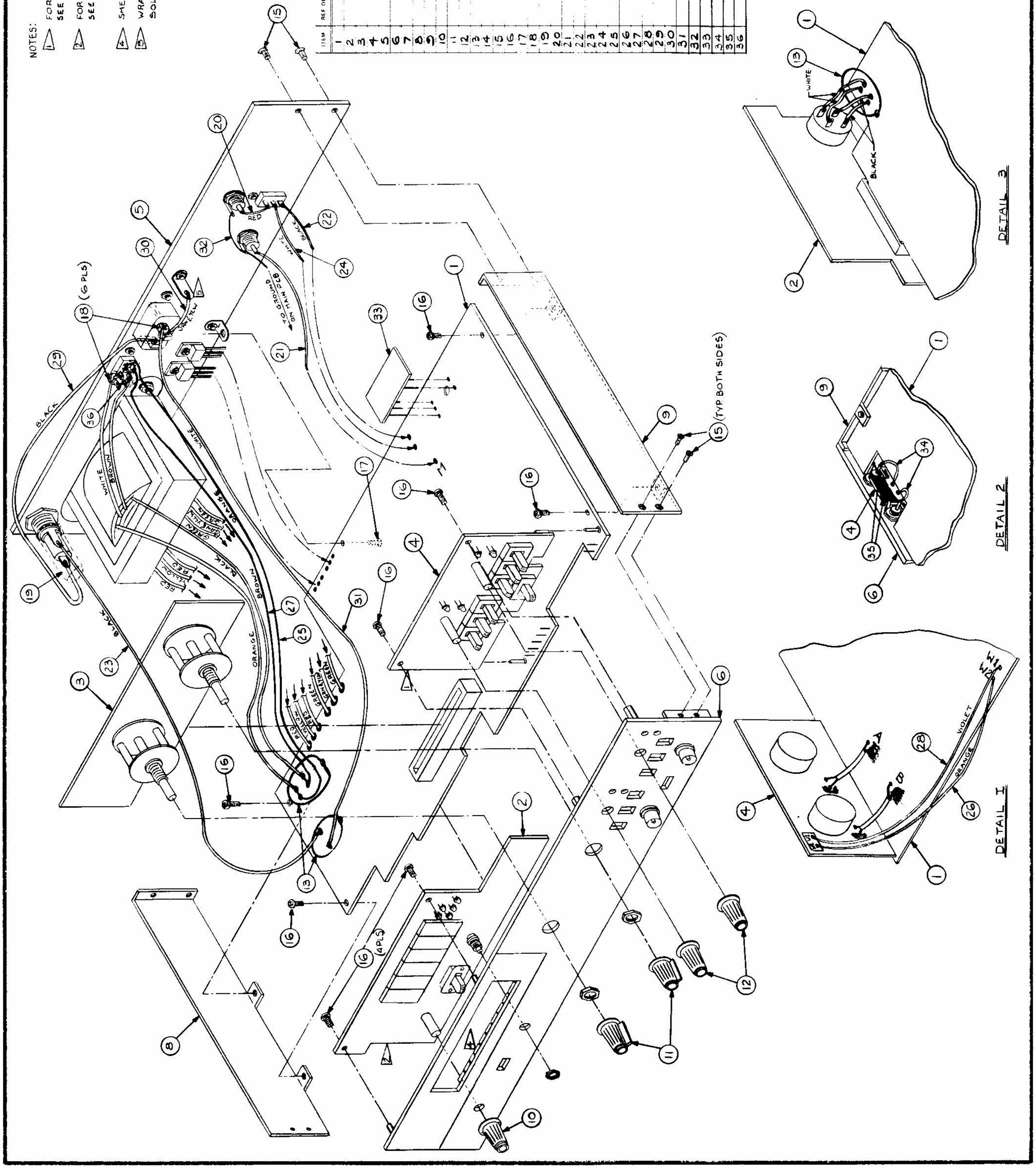


Switch Board PC Assembly

NOTES:

1. FOR WIRING INPUT PCB ASSY ITEM 4 SEE DETAIL 1 & DETAIL 2
2. FOR WIRING DISPLAY PCB ASSY ITEM 2 SEE DETAIL 3 SHT 2 OF 2.
4. SMEAR WITH THERMAL COMPOUND.
5. WRAP ITEM 30 TWICE THRU HOLE AND SOLDER

ITEM	REF DES	P/N	NOMENCLATURE	QTY
1		372540	1952B-4001 MAIN PCB ASSY	1
2		372557	1952B-4002 DISPLAY PCB ASSY	1
3		372573	1952B-4003 SWITCH PCB ASSY	1
4		372599	1952B-4004 INPUT PCB ASSY	1
5		377317	1952B-4452 REAR PANEL ASSY	1
6		372623	1952B-4451 FRONT PANEL ASSY	1
7		372466	1952B-2008 LEFT BRACKET	1
8		372474	1952B-2009 RIGHT BRACKET	1
9		365015	KNOB, GREEN DECAL	1
10		341404	POINTER KNOB	2
11		341388	KNOB PUTTY DECAL	2
12		172080	CABLE CLAMP	3
13		375469	SCREW #4-40 x 1/4" FHP	8
14		129890	SCREW #4-40 x 1/4" PHP	8
15		228890	SCREW 8-32 x 1/4" PHP	1
16		113852	THERMOFIT #12 x 3/4" L	4
17		196683	THERMOFIT 1/2" x 1 1/2" L	1
18		115576	WIRE #22, TF, RED x 2" L	1
19		156667	WIRE #22, TF, WHITE x 3" L	1
20		115774	WIRE #22, TF, BLACK x 2" L	1
21		115774	WIRE #22 TF, BLACK x 7" L	1
22		115667	WIRE #22 TF WHITE x 2" L	1
23		115790	WIRE #22 TF, ORANGE x 6" L	1
24		115790	WIRE #22, TF, ORANGE x 7" L	1
25		115782	WIRE #22 TF, BROWN x 6" L	1
26		135459	WIRE #22 TF VIOLET x 7" L	1
27		135541	WIRE #18 TF, BLACK x 7"	1
28		386177	WIRE #18 TF, GRN/YLW x 3" L	1
29		135582	WIRE #18 TF, WHITE x 10" L	1
30		115469	WIRE #22 BUS x 2" L	1
31		406926	1952B-4010 TIMEBASE PCB ASSY	1
32		115667	WIRE #22 TF WHITE x 1" L	2
33		115469	WIRE #22 BUS x 1" L	2
34		115469	WIRE #22 BUS x 1/2" L	1
35				
36				



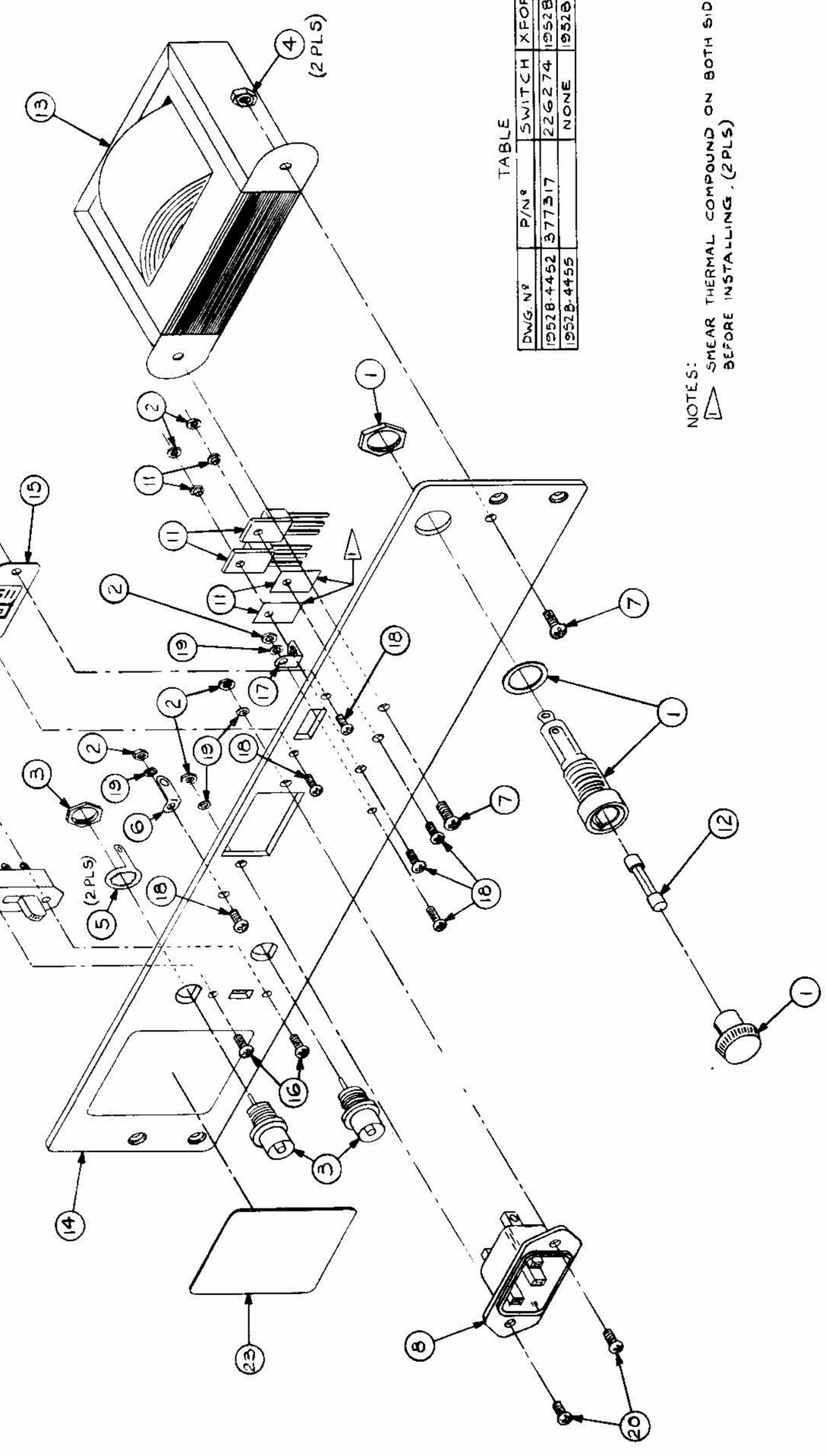
Front Panel Assembly

ITEM	REF DES	P/N	NOMENCLATURE	QTY
1		100107	FUSE HOLDER	1
2		110635	NUT #4-40	8
3		179127	BNC JACKS	2
4		152819	SELF-LOCKING NUT 6-32	2
5		167015	SOLDER-LUG #8 3/8	2
6		102558	GROUND-LUG #4-40	1
7		152173	SCREW #6-32 x 1/2" PHP	2
8		284166	LINE RECEPTACLE	1
9		261362	NUT # 2-56	2
10		369421	SLIDE SWITCH	1
11		369660	TRANSISTOR TIP 32	2
12		369819	FUSE 1 AMP	1
13		SEE TABLE	TRANSFORMER	1
14		377325	REAR PANEL 19528-2013	1
15		SEE TABLE	SWITCH 115/230V	1
16		379214	SCREW #2-56 x 3/8" PHP	2
17		306225	RIGHT ANGLE BRACKET	1
18		256164	SCREW #4-40 x 3/8 PHP	6
19		110403	INTERNAL WASHER #4	6
20		145813	SCREW #4-40 x 5/8 PHP	2
21				
22		110676	INTERNAL WASHER #2	2
23		385849	19528-8006 DECAL, OPTION	1

TABLE

DWG Nº	P/Nº	SWITCH	XFORMER	P/Nº
19528-4452	377317	226274	19528-6560	370544
19528-4455		NONE	19528-6510	370551

NOTES:
1. SMEAR THERMAL COMPOUND ON BOTH SIDES BEFORE INSTALLING. (2 PLS)



Rear Panel Assembly

Section 6
Option & Accessory Information

6-1 INTRODUCTION

6-2 This section of the manual contains information pertaining to the accessories and -03 option available for your instrument.

6-3 ACCESSORY INFORMATION

6-4 1952B rack adapter M00-200-614. Printer Interface Kit 2010A - 7011.

6-5 OPTION INFORMATION

6-6 The -03 option provides a 8 digit LED display and includes a Temperature Compensated Crystal Oscillator (TCXO) which has timebase stability and temperature dependent characteristics which are superior to the oscillator normally installed in the standard unit.

The TCXO specifications are as follows:

FREQUENCY	10.00 MHz
AGING RATE (per month)	$< \pm 3 \times 10^{-7}$
TEMPERATURE STABILITY 0 - 50°C 20 - 30°C	$< \pm 2 \times 10^{-6}$ $\pm 5 \times 10^{-7}$ typ.
LINE VOLTAGE (± 10% CHANGE)	$< \pm 1 \times 10^{-7}$

Section 7

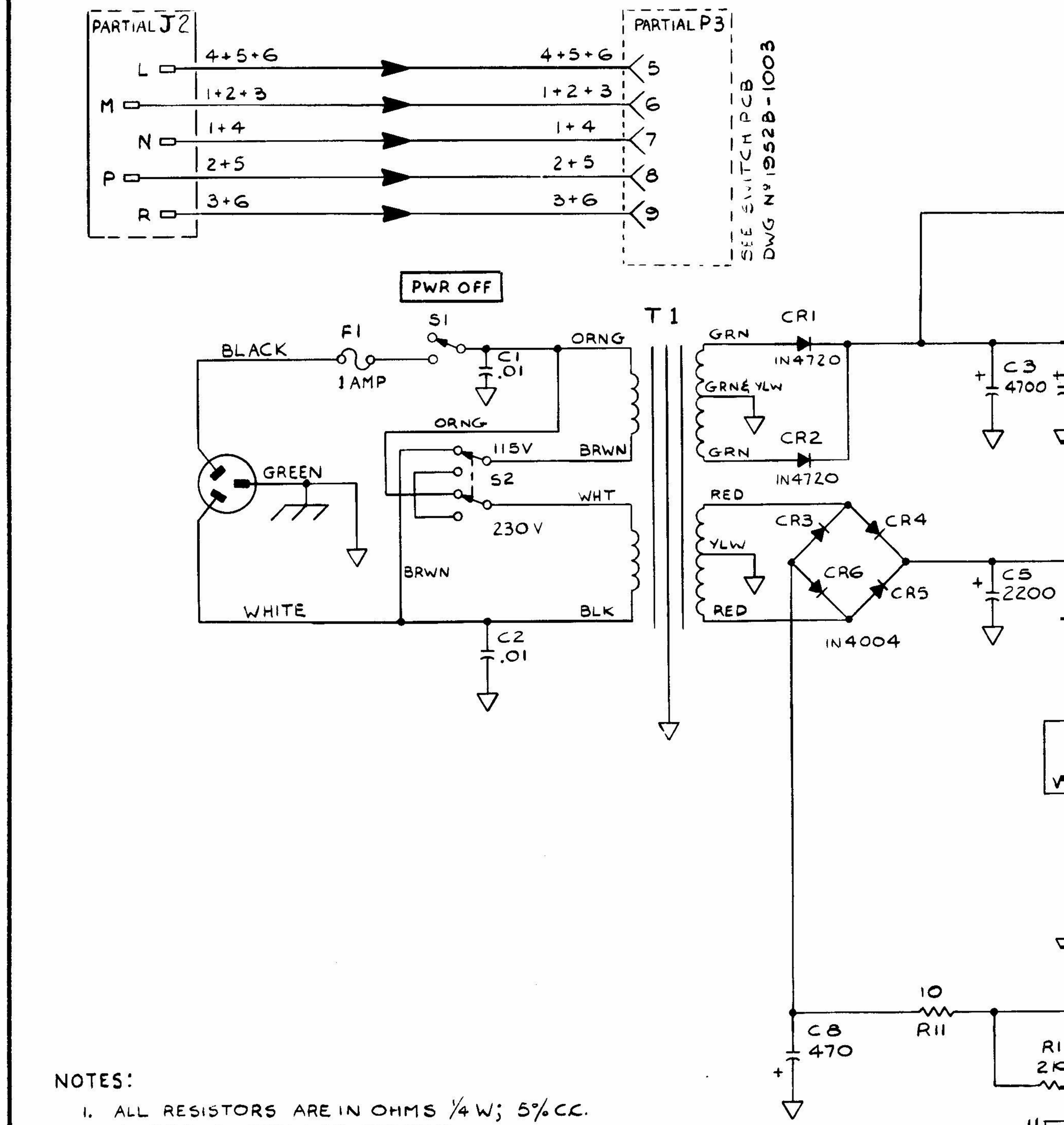
General Information

7-1. This section of the manual contains generalized user information as well as supplemental information to the List of Replaceable parts contained in Section 5. The following information is presented in this section:

List of Abbreviations
Federal Supply Codes for Manufacturers
Fluke Technical Service Centers — U.S. and Canada
Sales and Service Locations — International
Sales Representatives — U.S. and Canada

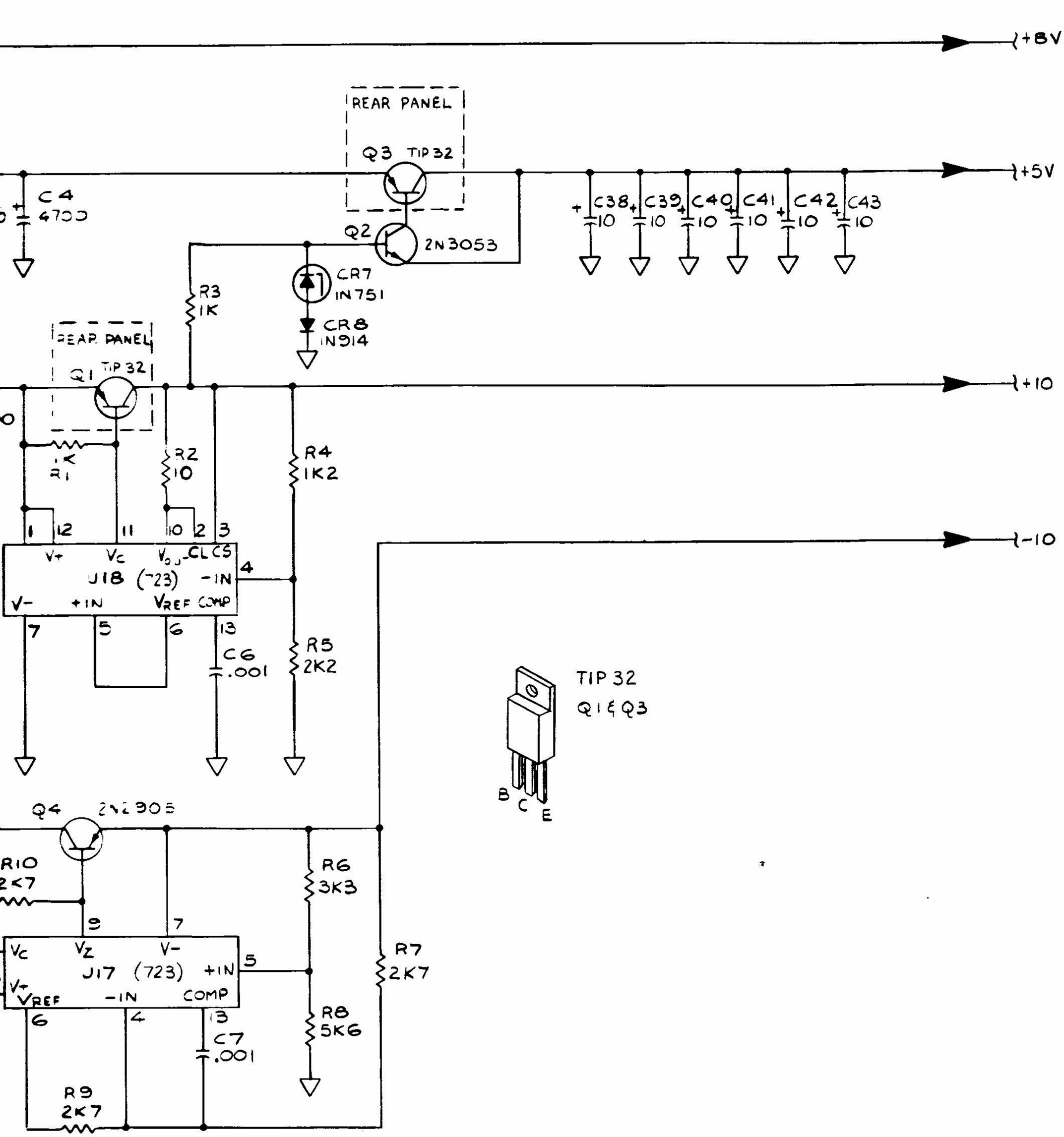
List of Abbreviations and Symbols

A or amp	ampere	H	henry	pF	picofarad
ac	alternating current	hd	heavy duty	pn	part number
af	audio frequency	hf	high frequency	(+) or pos	positive
a/d	analog-to-digital	Hz	hertz	pot	potentiometer
assy	assembly	IC	integrated circuit	p-p	peak-to-peak
AWG	american wire gauge	if	intermediate frequency	ppm	parts per million
B	bel	in	inch(es)	PROM	programmable read-only memory
bcd	binary coded decimal	intl	internal		
^{o}C	Celsius	I/O	input/output	psi	pound-force per square inch
cap	capacitor	k	kilo (10^3)	RAM	random-access memory
ccw	counterclockwise	kHz	kilohertz	rf	radio frequency
cer	ceramic	kΩ	kilohm(s)	rms	root mean square
cermet	ceramic to metal(seal)	kV	kilovolt(s)	ROM	read-only memory
ckt	circuit	lf	low frequency	s or sec	second (time)
cm	centimeter	LED	light-emitting diode	scope	oscilloscope
cmrr	common mode rejection ratio	LSB	least significant bit	SH	shield
		LSD	least significant digit	Si	silicon
comp	composition	M	mega (10^6)	serno	serial number
cont	continue	m	milli (10^{-3})	sr	shift register
crt	cathode-ray tube	mA	milliampere(s)	Ta	tantalum
cw	clockwise	max	maximum	tb	terminal board
d/a	digital-to-analog	mf	metal film	tc	temperature coefficient or temperature compensating
dac	digital-to-analog converter	MHz	megahertz		
		min	minimum	tcxo	temperature compensated crystal oscillator
dB	decibel	mm	millimeter		
dc	direct current	ms	millisecond	tp	test point
dmm	digital multimeter	MSB	most significant bit	u or μ	micro (10^{-6})
dvm	digital voltmeter	MSD	most significant digit	uhf	ultra high frequency
elect	electrolytic	MTBF	mean time between failures	us or μs	microsecond(s) (10^{-6})
ext	external			uut	unit under test
F	farad	MTTR	mean time to repair	V	volt
^{o}F	Fahrenheit	mV	millivolt(s)	v	voltage
FET	Field-effect transistor	mv	multivibrator	var	variable
ff	flip-flop	MΩ	megohm(s)	vco	voltage controlled oscillator
freq	frequency	n	nano (10^{-9})	vhf	very high frequency
FSN	federal stock number	na	not applicable	vlf	very low frequency
g	gram	NC	normally closed	W	watt(s)
G	giga (10^9)	(–) or neg	negative	ww	wire wound
gd	guard	NO	normally open	xfmr	transformer
Ge	germanium	ns	nanosecond	xstr	transistor
GHz	gigahertz	opnl ampl	operational amplifier	xtal	crystal
gmv	guaranteed minimum value	p	pico (10^{-12})	xtlo	crystal oscillator
		para	paragraph	Ω	ohm(s)
gnd	ground	pcb	printed circuit board	μ	micro (10^{-6})



NOTES:

1. ALL RESISTORS ARE IN OHMS ¼ W; 5% C.C. UNLESS OTHERWISE STATED.
2. ALL CAPACITORS ARE IN MICROFARADS, UNLESS OTHERWISE STATED.
3. S1 SWITCHES SHOWN IN OFF POSITION
4. 115/230 V SWITCH S2, SHOWN IN 115 V POSITION.



Main PCB Power Supply Schematic - 1952B-1001 - Sheet 1 of 5

CHANNEL A

INPUT PCB ASSY
DWG.Nº 1952B-1004

B INPUT

A INPUT

PARTIAL P1

4) +10V

7) -10V

8) +5V

2) TRIGGER LEVEL A

1) GND

9) GND

2) TRIGGER LEVEL B

3)

SEE INPUT PCB DWG Nº 1952B-1004

CR9

CR10

+ C11 10

R12 180

R13 100K

C9 47pf

CR11

CR12

2N4274 Q5

R14 680

4K7 R15

R16 470

1K8 R17

C10 10 +

R18 1K2

R20 1K2

R21 180

+ C12 10

470 R19

CR13

CR14

Q6 E421

R22 2M2

R23 10K PRESET ADJUST

R37 22K

C27 .1

.01 C13

U24

10116

R24 1K5

CR20

CR19

+ C24 10

R79 180

R78 100K

C26 47pf

CR18

CR17

R73 1K2

R71 1K2

180 R70

+ C23 10

.01 C22

U25

10116

470 R72

CR16

CR15

Q13 E421

2N4274 Q14

R77 680

4K7 R76

R75 470

1K8 R74

C25 10 +

R69 2M2

R68 10K PRESET ADJUST

R54 22K

C28 .1

R67 1K5

NOTES:
1. ALL RESISTORS ARE IN OHMS 1/4 W; 5% C.C. UNLESS OTHERWISE STAT
2. ALL CAPACITORS ARE IN MICROFARADS, UNLESS OTHERWISE STATE

A

U24 PIN 11:
V_{BB} REFERENCE ECL LEVEL

R29 220 C14 22pf R30 820 C16 22pf R38 820 +10V

470 R28 — U24 10116 — 470 R36 — U24 10116 — 270 R41

R27 470 — R35 470 — Q7 5771

220 R26 22pf C15 R31 820 — 22pf C17 R39 820

R25 K5 — R32 1K5 — R33 1K5 — R40 1K — R42 180 Q8 — 1K R43

10 C45 +8V — -10V — +5V R45 220

2 OUT U36 IN 1 GND — R44 2K2 — Q9 5771 — A OUT

CHANNEL B

R62 220 C21 22pf R61 820 — C19 22pf R53 820 — +10V

470 R63 — U25 10116 — 470 R55 — U25 10116 — R50 270

R64 470 — R56 470 — Q12 5771

220 R65 C20 22pf R60 820 — 22pf C18 R52 820

R66 1K5 — R59 1K5 — R58 1K5 — R51 1K — R49 180 5771 Q11 — 1K R48

SEE SHT 3 OF 5

-10V — +5V R46 220

R47 2K2 — 5771 Q10 — B OUT

U25 PIN 11:
V_{BB} REFERENCE ECL LEVEL.

ATED.
TED.

Main PCB Input Amplifier Schematic - 1952B-1001 - Sheet 2 of 5

SEE SHT 2 OF 5 { A OUT

REAR PANEL

GATE

GATE

INPUT PCB ASSY DWG NO. 19528-1004

W1

W2

TRIG. LEDS A

TRIG. LEDS B

R88 100

U15

U14

U16

U13

74S04

7400

1K R34 +5V

1K R57

C29 .001

1K R86 +5V

1K R87

C31 .001

MARKER GENERATION

SLOPE A

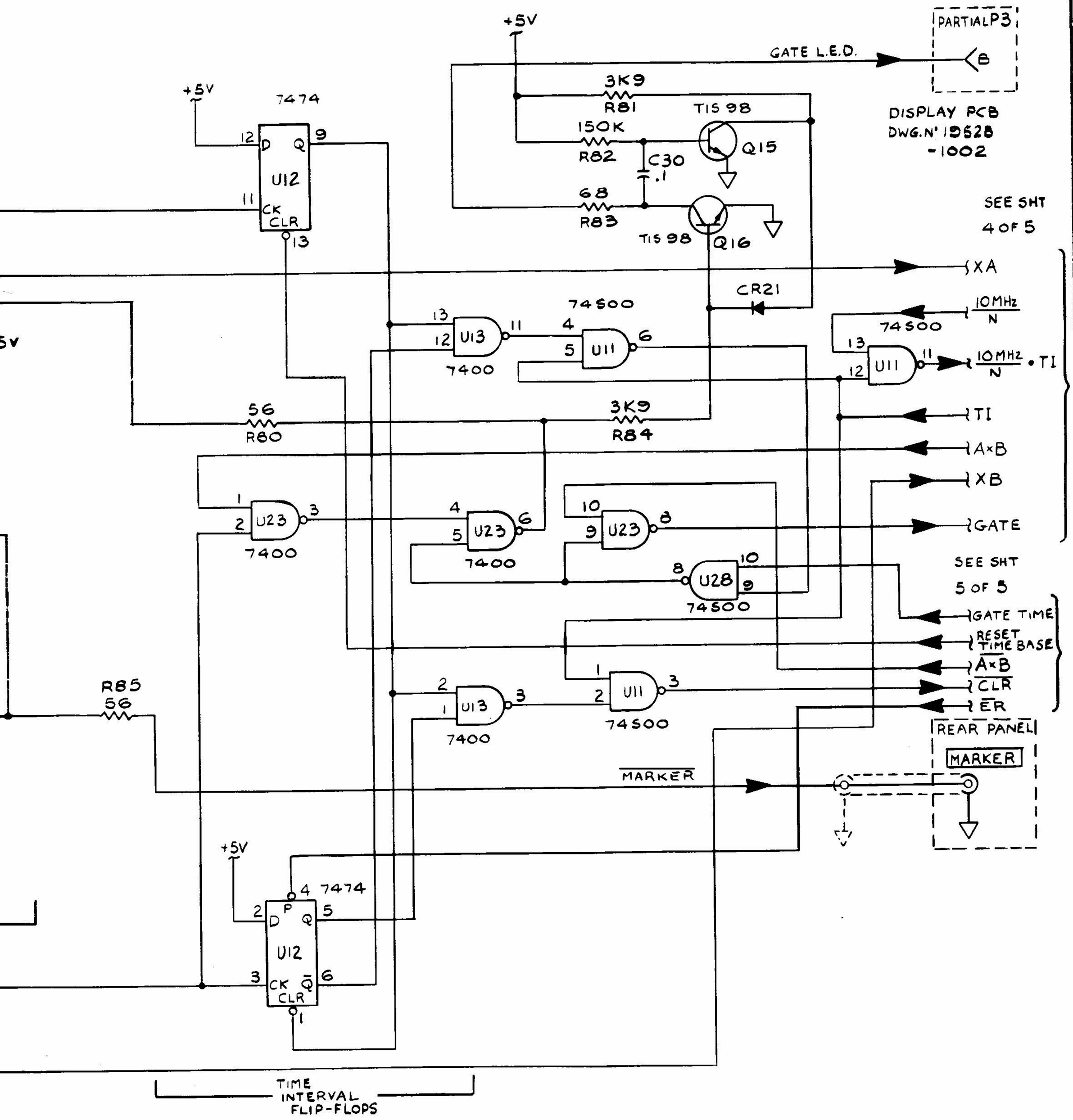
SLOPE B

INPUT PCB DWG Nº 19528-1004

PARTIAL P1

SLOPE SELECTION

SEE SHT 2 OF 5 { B OUT



Main PCB Schematic - 1952B-1001 - Sheet 3 of 5

SEE SHT 3 OF 5

GATE

XA

10 MHz / N

A×B

×B

SEE SWITCH PCB DWG. Nº 1952B-1003

10MHz / N

1

D — A×B

B — PA+CHK

9 — +5V

14 — PA

12 — A/B

11 — FA+FAB+A×B

15 — FA+CHK

PARTIAL J2

SEE SHT 3 OF 5

10MHz / N • TI

TI

REAR PANEL

EXT. CLOCK

S3

+10V

TIMEBASE DWG NO. 1952B-1010

3 R89 1K

6 4 U10

10 9 U10

74S64

2 3 U10

1 13 U10

8 U10

T.P. B

9 10 U11 8

74S00

ACC. CLK.

10MHz / N • TI

6 4 5 U9

10 9 U9

74S64

8 U9

2 3 U9

1 13 U9

10 KHz

C33 10

R93 470

R94 10

2N4274 Q18

C32 .001

R90 10K

Q17

CR22 CR23

R91 1M

SX3819

R95 680

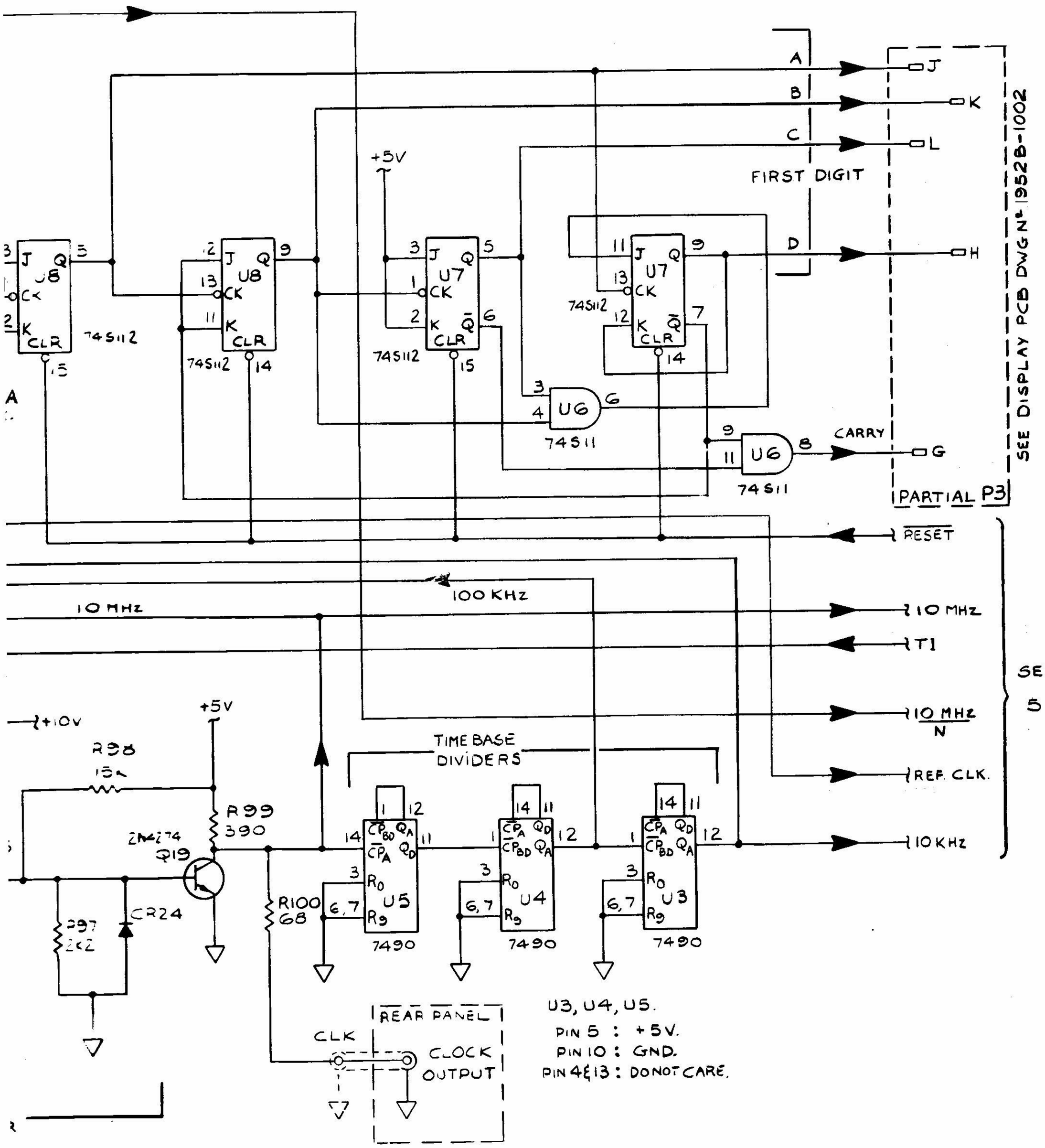
R92 270

C34 .001

NOTES:

1. ALL RESISTORS ARE IN OHMS, 1/4 W; 5% C.C., UNLESS OTHERW
2. ALL CAPACITORS ARE IN MICROFARADS, UNLESS OTHERWISE
3. ADD 1K RESISTOR 1/4 W 5% C.C., TO REDUCE OUTPUT LEVEL O

1952B



ISE STATED.
STATED.
: TCXO (ONLY FREQ'D)

Main PCB Schematic - 1952B-1001 - Sheet 4 of 5

T.P. A

R101
390

SEE SHT 3 OF 5

GATE TIME

RESET TIMEBASE

$\overline{CLR}$

$\overline{A \times B}$

$\overline{ER}$

SEE SHT 4 OF 5

10 MHz N

REF. CLK.

TI

$\overline{RESET}$

10 MHz

10 KHz

GND

1 2 U21 3 7400

GATE TIME F.F.

+5V

7473

14 J Q 12
1 CK U22
3 K $\overline{Q}$ 13
CLR 2

4 5 U21 6 7400

12 13 U23 11 7400

12 13 U28 11 74S00

CR25

7400
11 U20 13 12

CYC DE

CR26

CR27

13 12 U21 11 7400

9 10 U21 8 7400

3 U28 2 74S00 1

4 5 U28 6 74S00

6 7
R(1) R(2)
U35
$\overline{CP}_{BD}$ Q_A 12
Q_D $\overline{CP}_A$ 11 14
7490

U34 7490

U33 7490

U32 7490

REFERENCE (TIMEBASE) DIVIDERS

PARTIAL J2

2 R1
3 R2
4 R3
5 R4
6 R5
7 R6
C GND
8 $\overline{A \times B}$
E FA+CH
F PA+TI
G PA
A TI
10 RESET
J GND

Q24 TIS98
Q23 TIS98
Q22 TIS98
Q21 TIS98

R106
1K

680
R107

UNITS ANNUNCIATION

$\overline{FA+CHK}$ 2
$\overline{PA+TI}$ 3
$\overline{PA}$ 4
$\overline{TI}$ F

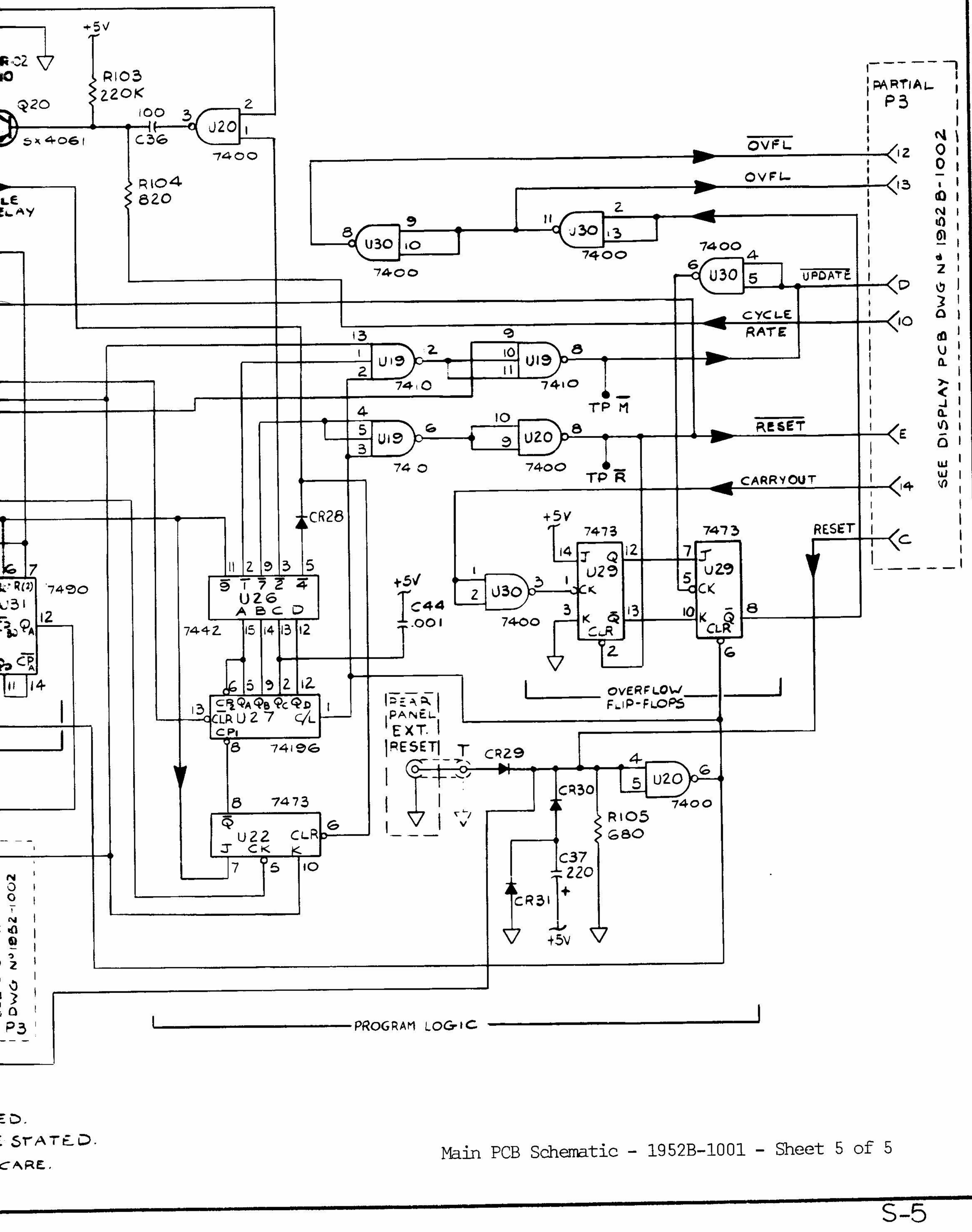
PARTIAL P3

SEE DISPLAY PCB DWG Nº 19682-1002

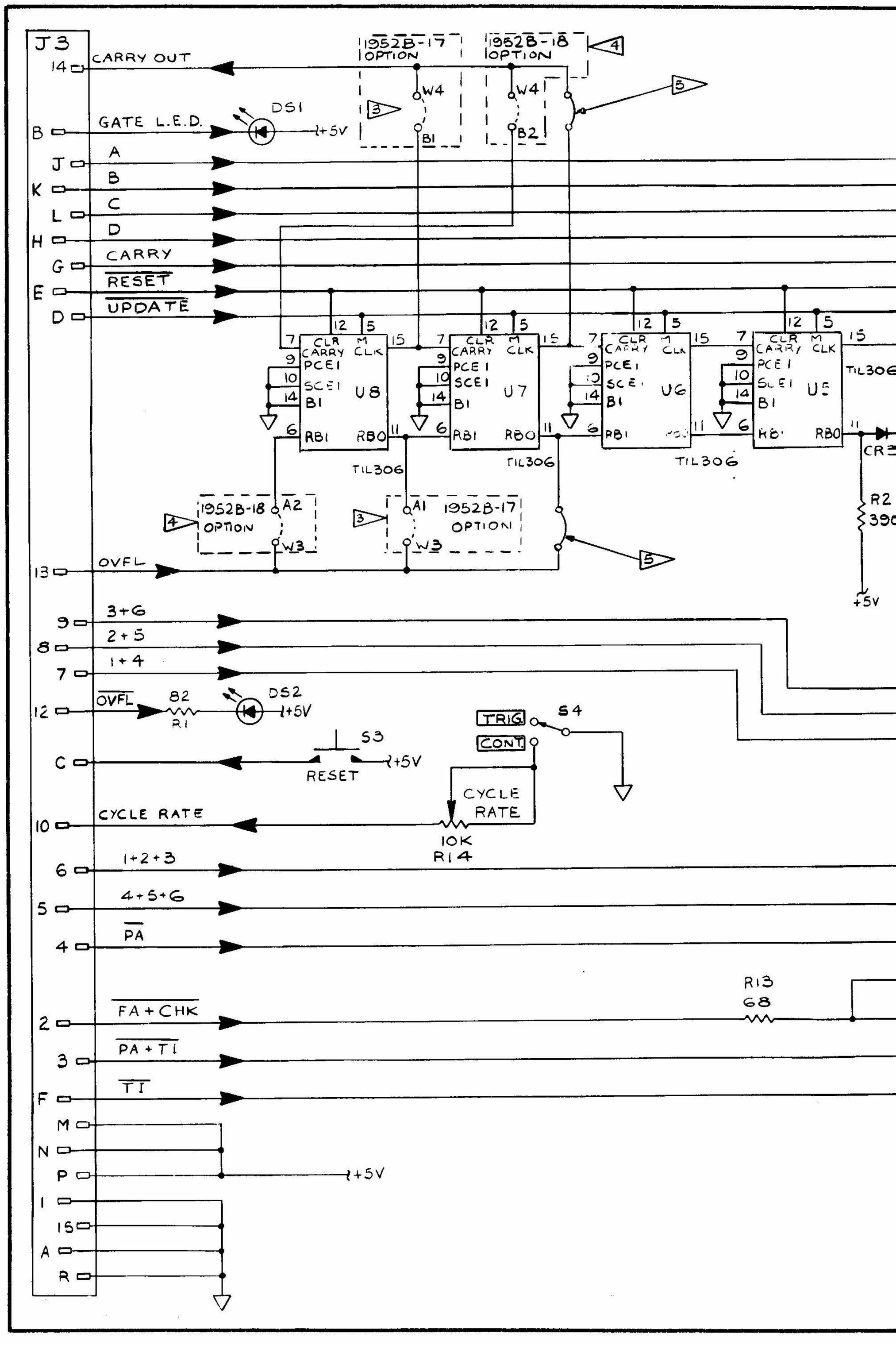
SEE SWITCH PCB DWG Nº 19528-1003

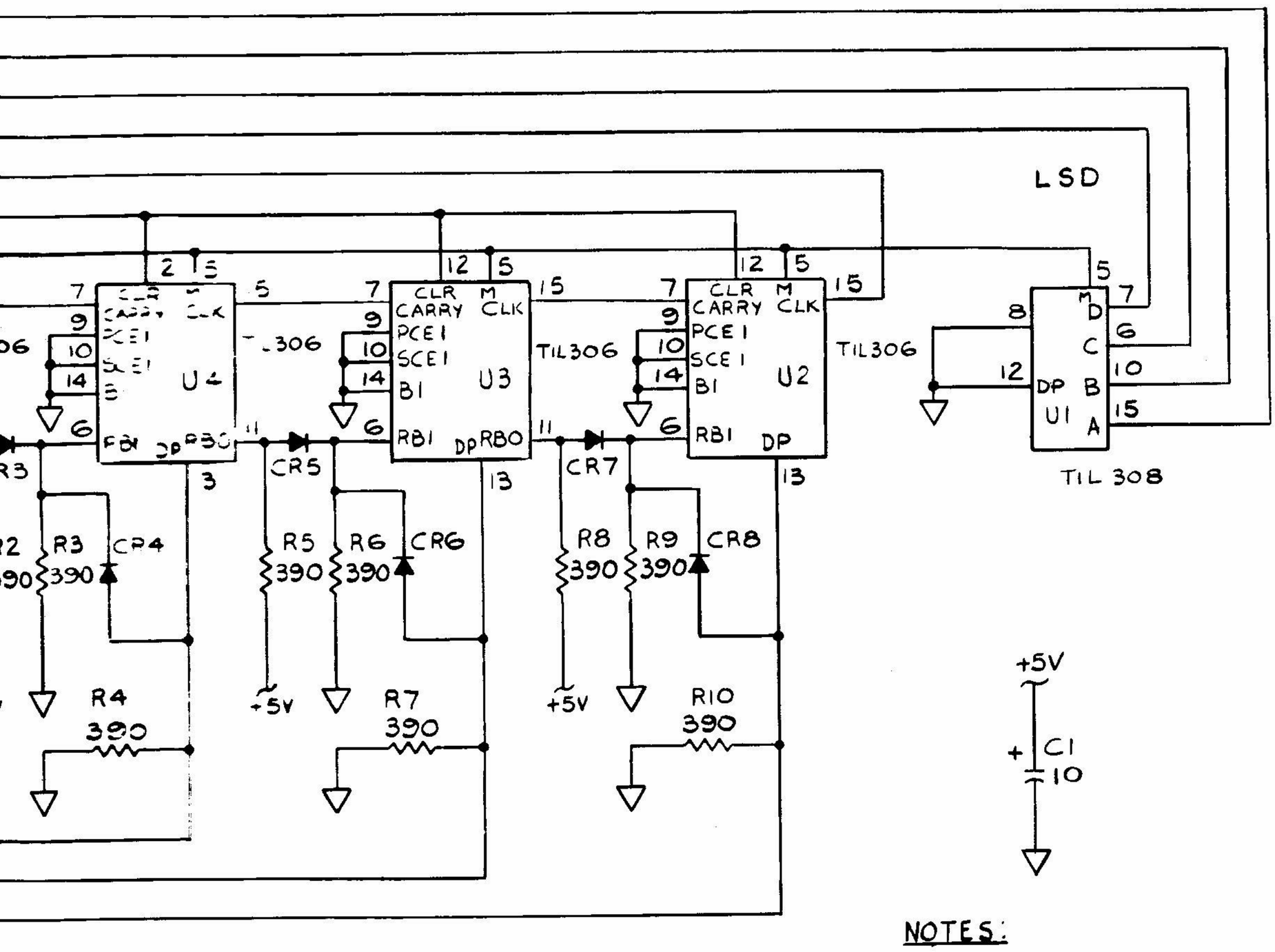
NOTES

1. ALL CAPACITORS ARE IN MICROFARADS, UNLESS OTHERWISE STATED.
2. ALL RESISTORS ARE IN OHMS, 1/4 W; 5% C.C. UNLESS OTHERWISE ST
3. U31, U32, U33, U34 & U35, PIN 5: +5V, PIN 10: GND, PINS 4&13: DO NOT CA



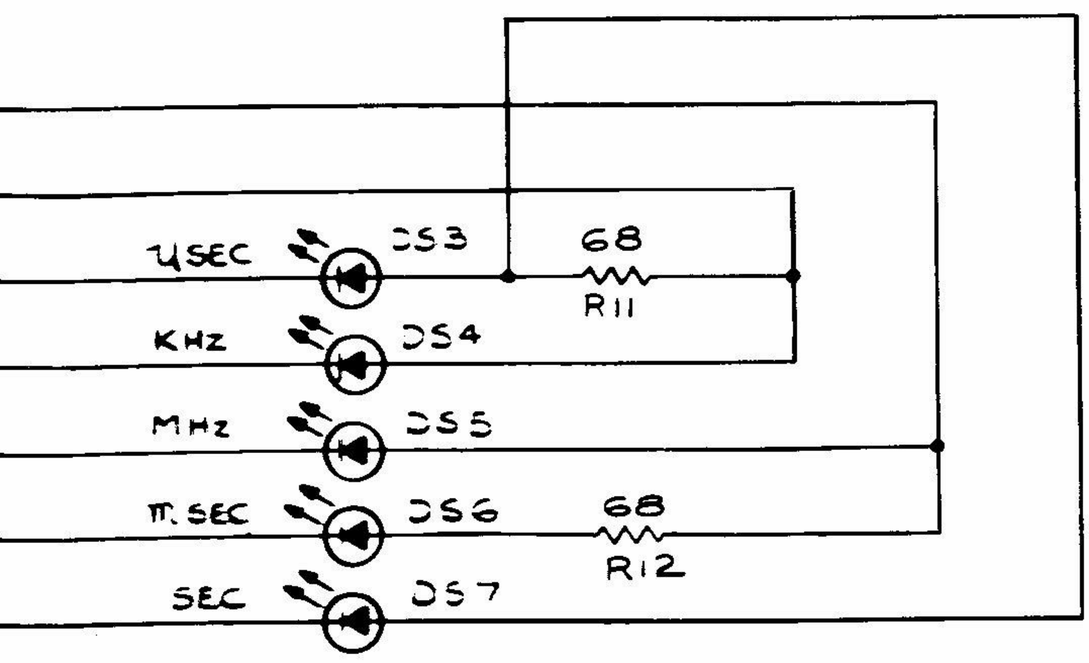
Main PCB Schematic - 1952B-1001 - Sheet 5 of 5


J3
14 CARRY OUT
B GATE L.E.D.
DS1
+5V
J A
K B
L C
H D
G CARRY
E RESET
D UPDATE
1952B-17 OPTION
1952B-18 OPTION
W4
W4
B1
B2
3
4
5
U8
U7
U6
U5
CLR
CARRY
M CLK
PCE
SCE
BI
RBI
RBO
TIL306
A2
A1
W3
W3
OVFL
13
9 3+6
8 2+5
7 1+4
12 OVFL
R1
82
DS2
+5V
C
S3
RESET
+5V
TRIG
CONT
S4
10 CYCLE RATE
10K
R14
CYCLE RATE
6 1+2+3
5 4+5+6
4 PA
2 FA+CHK
3 PA+TI
F TI
M
N
P
+5V
1
15
A
R
R13
68
R2
390
+5V



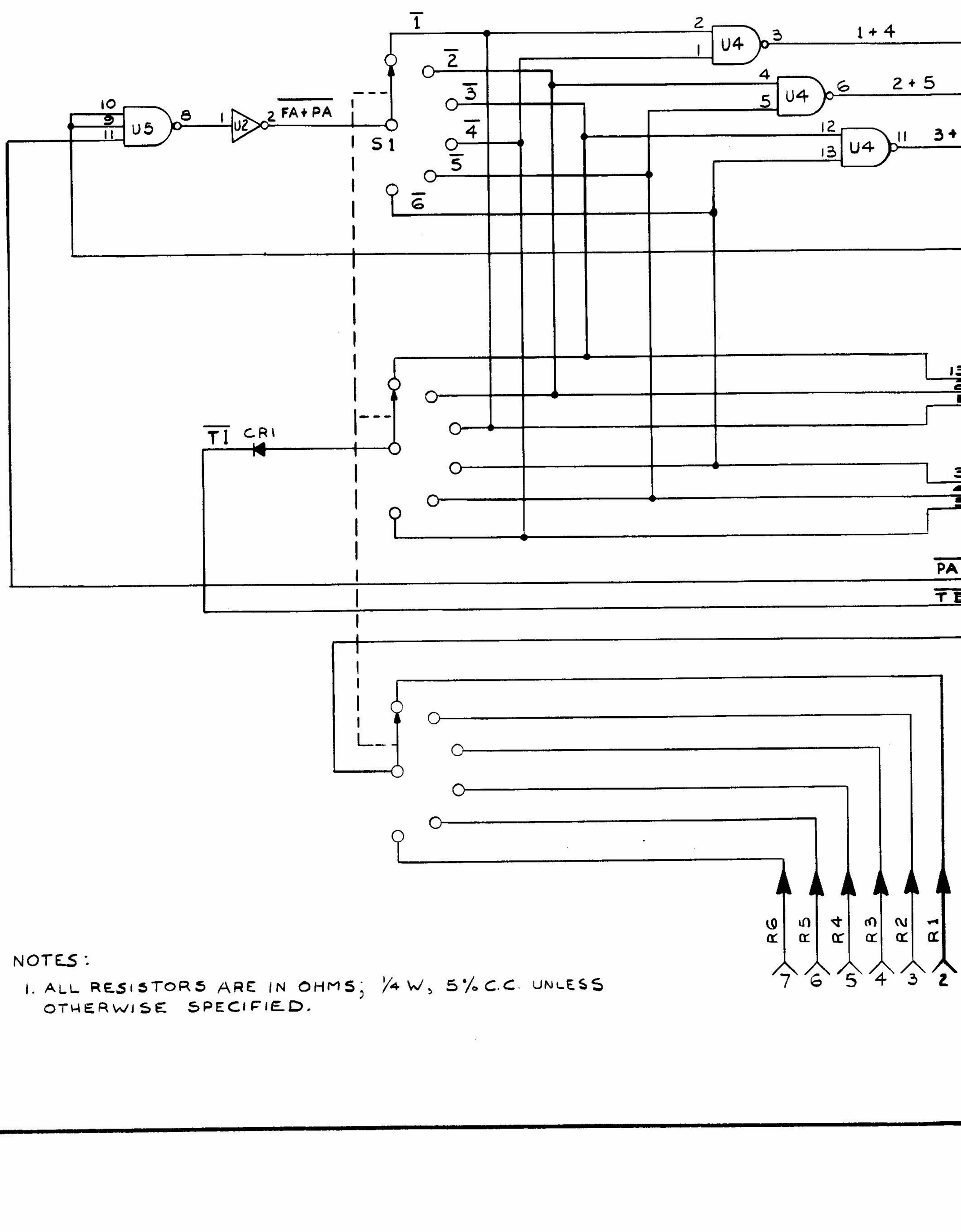
NOTES:

1. ALL RESISTORS ARE IN OHMS, 1/4 W; 5% C.C. UNLESS OTHERWISE STATED.
2. ALL CAPACITORS ARE IN MICRO-FARADS, UNLESS OTHERWISE STATED.
3. FOR 7 DIGIT OPTION INSTALL JUMPER WIRES FROM W3 TO A1 & W4 TO B1
4. FOR 8 DIGIT OPTION INSTALL JUMPER WIRES FROM W3 TO A2 & W4 TO B2.
5. FOR 7 DIGIT OPTION AND 8 DIGIT OPTION CUT LAND WHERE SHOWN. (2 PLS.)



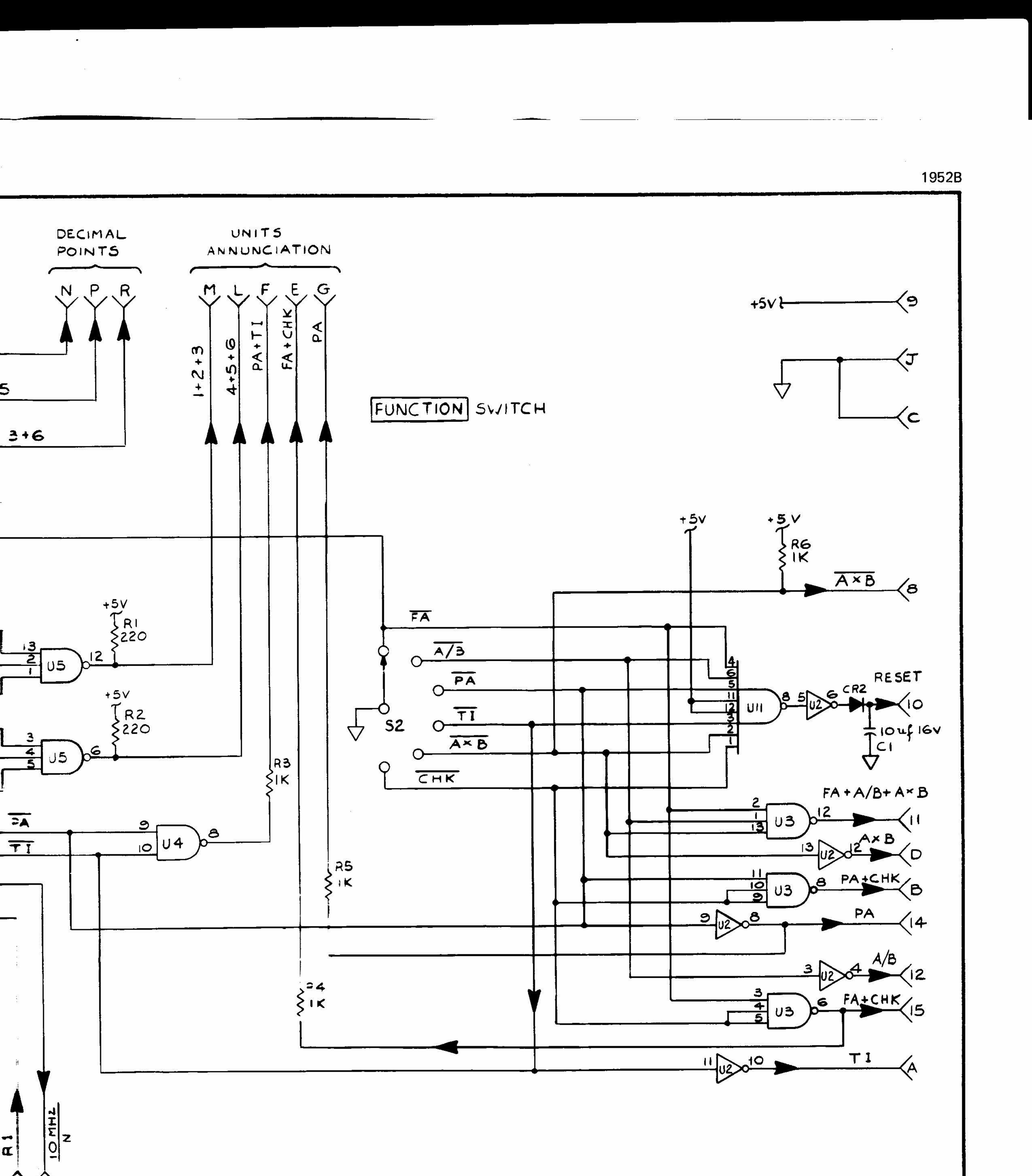
Display PCB Schematic - 1952B-1002 - Sheet 1 of 1

RANGE SWITCH

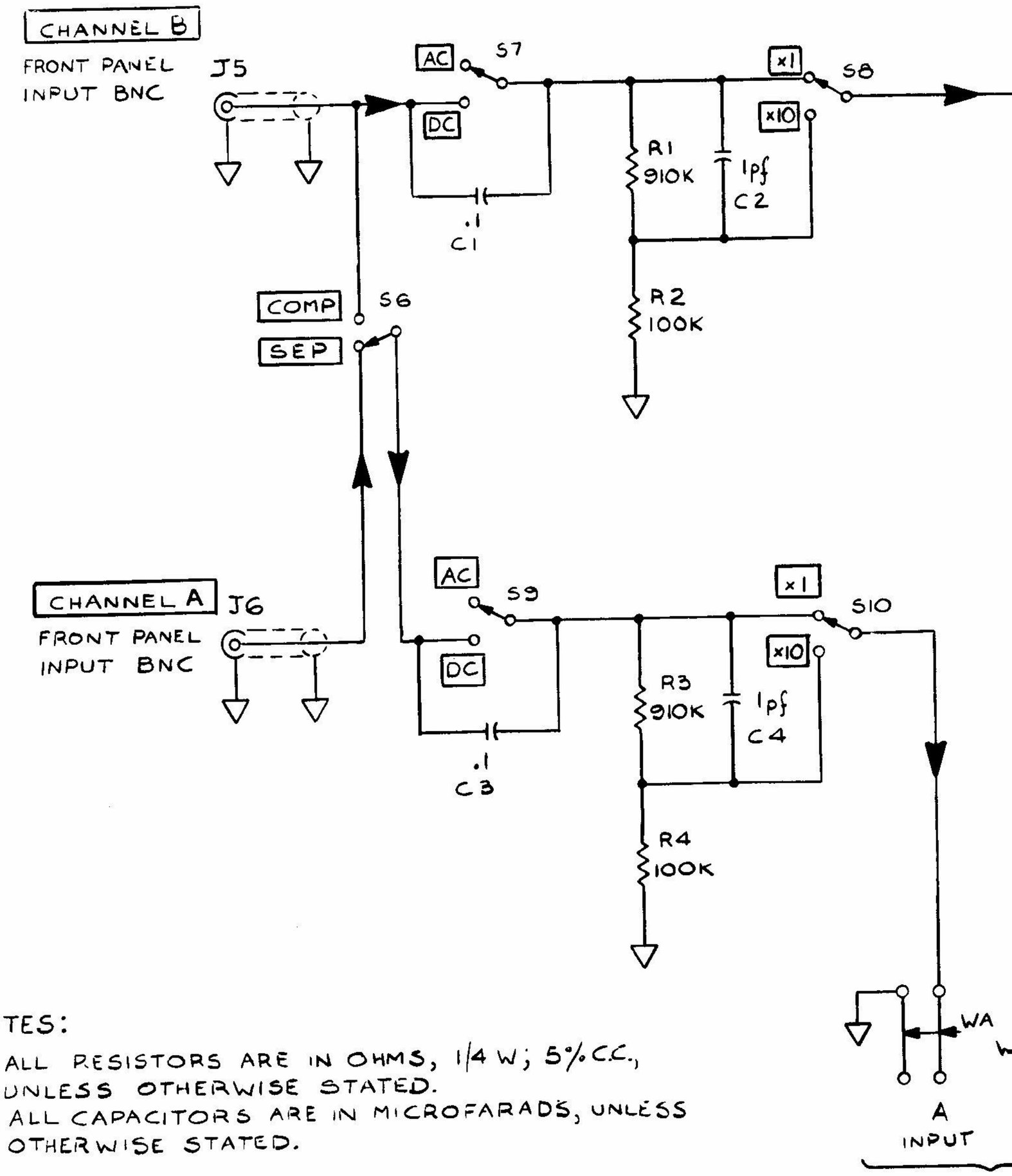


NOTES:

1. ALL RESISTORS ARE IN OHMS; 1/4 W, 5% C.C. UNLESS OTHERWISE SPECIFIED.



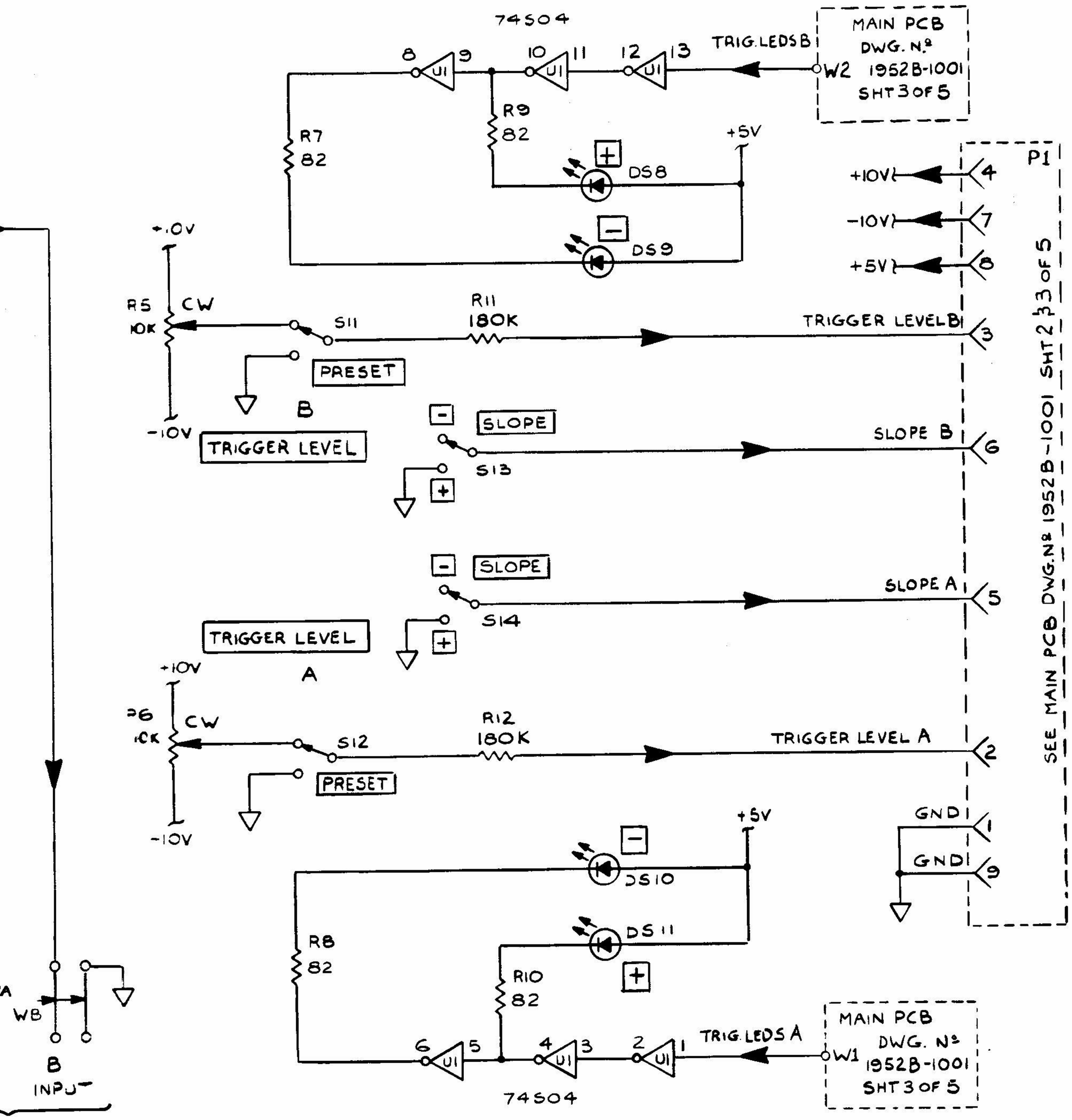
Switch PCB Schematic - 1952B-1003 - Sheet 1 of 1



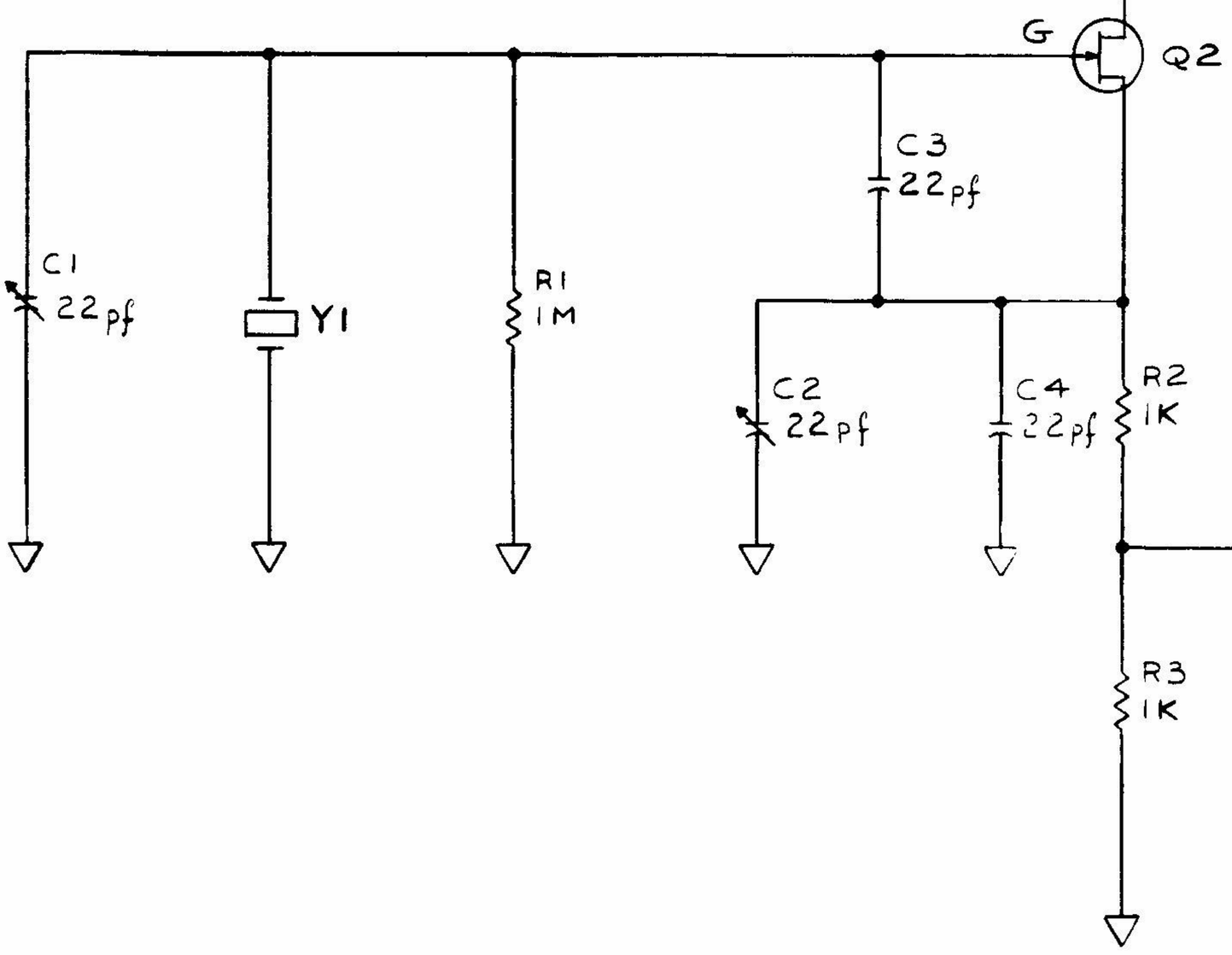
NOTES:

1. ALL RESISTORS ARE IN OHMS, 1/4 W; 5% C.C., UNLESS OTHERWISE STATED.
2. ALL CAPACITORS ARE IN MICROFARADS, UNLESS OTHERWISE STATED.

TO MAIN
DWG. Nº
SHT 2 OF

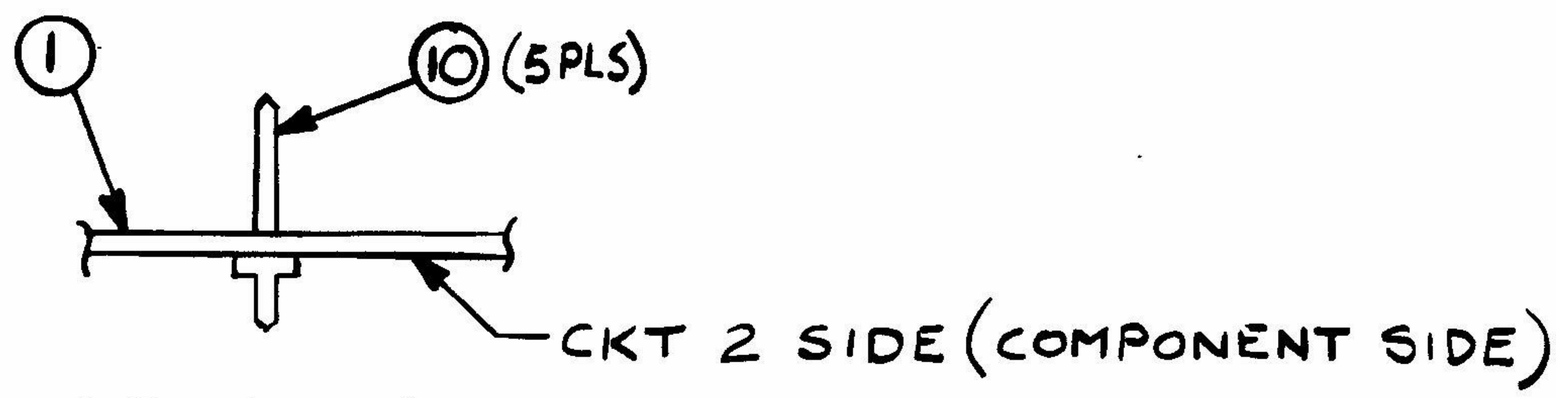


Input PCB Schematic - 1952B-1004 - Sheet 1 of 1



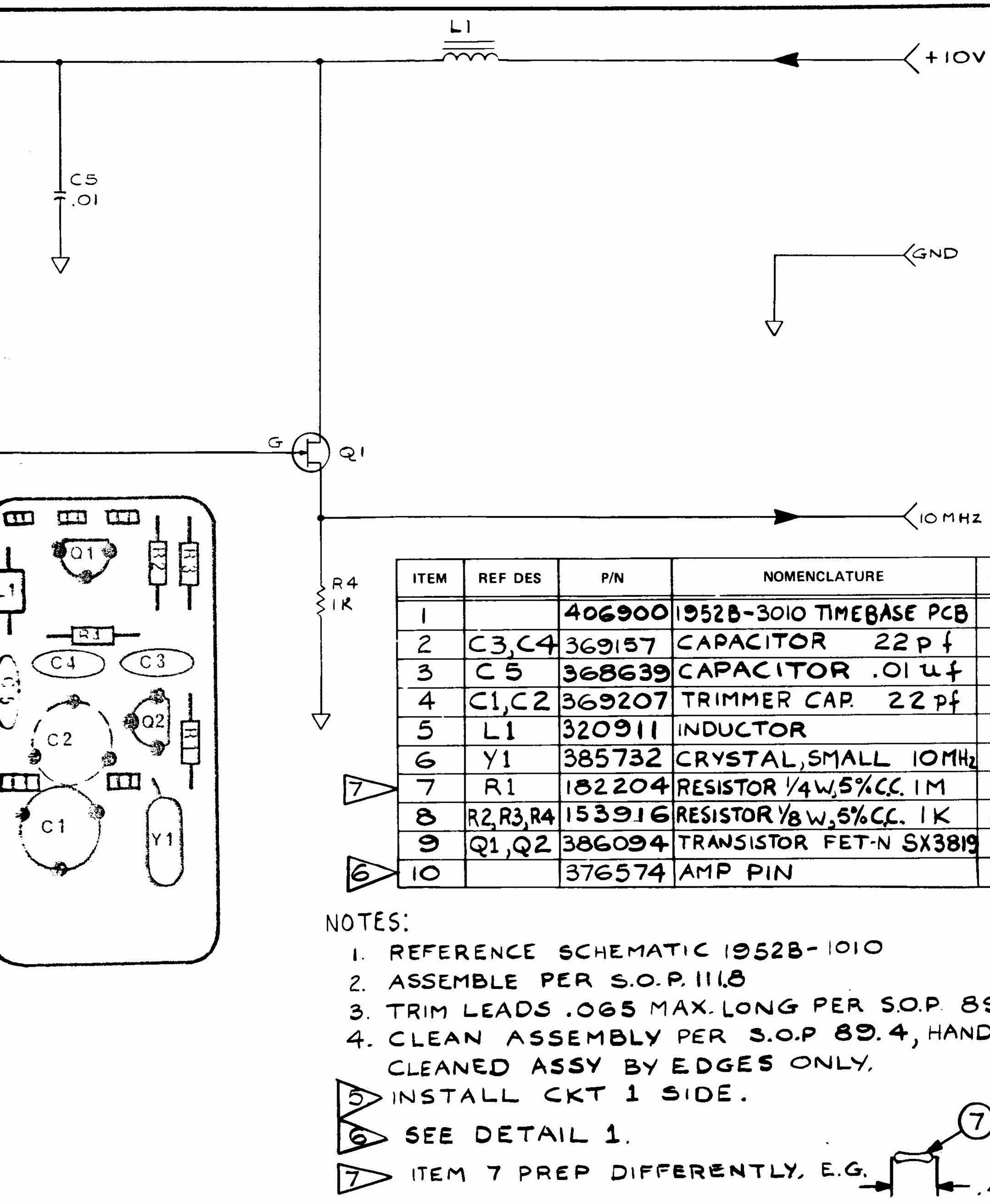
NOTES:

1. ALL RESISTORS ARE IN OHMS, 1/8 W; 5% C.C. UNLESS OTHERWISE SPECIFIED.
2. ALL CAPACITORS ARE IN MICROFARADS, UNLESS OTHERWISE SPECIFIED



DETAIL 1

1952B

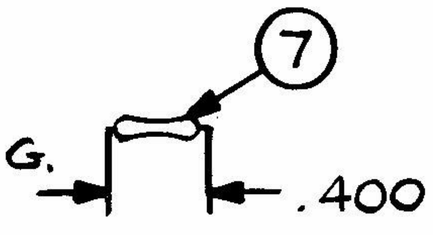


ITEM	REF DES	P/N	NOMENCLATURE	QTY
1		406900	1952B-3010 TIMEBASE PCB	1
2	C3,C4	369157	CAPACITOR 22pf	2
3	C5	368639	CAPACITOR .01 uf	1
4	C1,C2	369207	TRIMMER CAP. 22 pf	2
5	L1	320911	INDUCTOR	1
6	Y1	385732	CRYSTAL, SMALL 10MHz	1
7	R1	182204	RESISTOR 1/4W, 5% C.C. 1M	1
8	R2,R3,R4	153916	RESISTOR 1/8W, 5% C.C. 1K	3
9	Q1,Q2	386094	TRANSISTOR FET-N SX3819	2
6	10		376574 AMP PIN	5

NOTES:

1. REFERENCE SCHEMATIC 1952B-1010
2. ASSEMBLE PER S.O.P. 111.8
3. TRIM LEADS .065 MAX. LONG PER S.O.P. 89.19
4. CLEAN ASSEMBLY PER S.O.P 89.4, HANDLE CLEANED ASSY BY EDGES ONLY.

5. INSTALL CKT 1 SIDE.
6. SEE DETAIL 1.
7. ITEM 7 PREP DIFFERENTLY, E.G.



Timebase PCB Schematic - 1952B-1010 - Sheet 1 of 1

593-2747

14
E

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n
-

Change/Errata Information

Issue No: 4 6/79

This change/errata contains information necessary to ensure the accuracy of the following manual. Enter the corrections in the manual if either one of the following conditions exist:

1. The instrument's pcb revision letter is equal to or higher than that which is indicated at the beginning of the change.

2. No revision letter is indicated at the beginning of the change/errata.

MANUAL

Title: 1952B UNIVERSAL COUNTER–TIMER
Print Date: DECEMBER 1975
Rev and Date: ------

C/E PAGE EFFECTIVITY

Page No.	Print Date
1	4/77
2	4/77
3	4/77
4	4/77
5	9/77
6	6/79
7	6/79

Page 2-1 Para 2-7C: Change both references to slo-blo fuses to fast-blo

Page 4-4 Para 4-16: Change both references to slo-blo fuses to fast-blo.

Page 4-10 Add the following after paragraph 4-41:

4-41A + 5 Volt dc calibration

4-41B Use the following procedure to calibrate the +5V dc power supply.

a. Connect the test multimeter between +5V dc (positive end of C47 or J2-9) and ground.

b. Adjust R108 for a reading between 4.8 and 5.0V dc.

Page 5-2 Top drawing and parts list:

Add the following items to the parts list:

13; ; 419374; Decal, option; 1

14; ; 406918; Timebase, 1952B-4010T; 1

Add the following items to the drawing:

The option decal is attached to the rear panel on the right corner.

The timebase is installed on the right rear corner of the main pcb.

Bottom drawing and parts list:

Add or change the following items on the parts list:

4; ; 372458; Heatsink; 1952B-2004; 1

5; ; 372482; Bracket, 1952B-2010; 2

9; ; 331249; Thermo setting tape 3/8" x 5"; 2

10; ; 393975; Decal, serial number (M00-800-555); 1

Add the following items to the drawing:

Item 9, is used to attach item 3 to item 1, with one strip above and one below the indicator opening.

Item 10, is attached to the low center portion of the front panel.

Page 5-3 Make the following changes to the drawing:

Diode labeled "10" at top left of drawing: Renumber as "11" and reverse the cathode band.

Diode labeled "11" at top left of drawing: Renumber as "10" and reverse the cathode band.

Add variable resistor labeled 74 between the IC labeled 25 and the resistor labeled 56.

Relabel the above resistor from 56 to 77.

Add a capacitor labeled 67 below IC 32 in the lower left corner.

Relabel resistor 51 between the large capacitors 4 and 5 as 49.

Relabel resistor 54 at the bottom right center of the drawing as 78.

Add a capacitor labeled 40 beneath the resistor relabel 78, and left of the resistor labeled 55.

Change the items below to read as changed:

3; C5; 392720; Capacitor 2200 μF, 25V; 1

5; C36; 182832; Capacitor 33 μF, 10V; 1

8; CR3 - CR6; 296509; Diode Bridge KBP02; 1

10; 11 ; 203323; Diode 1N4448; 12

14; Q4; 269076; Transistor 2N2905; 1
20; 13 ; 218396; Transistor 2N3904; 6
40; 29 ; 369579; Capacitor .01 μF, 50V; 3
45; R7, R9, R10, R104; 170720; Resistor; 1/4W, 5%, CC, 2K7; 4
51; 21 ; 148015; Resistor, 1/4W, 5%, CC, 820; 8
54; R103; 193441; Resistor, 1/4W, 5%, CC, 180K; 1
56; 24 ; 148007; Resistor, 1/4W, 5%, CC, 680; 4
74; R108; 275735; Resistor, Trimpot 100Ω; 1
76; C46; 277202; Capacitor, 43 pF, 500V; 1
77; R3; 147967; Resistor, 1/4W, 5%, CC, 330; 1
78; R82; 182212; Resistor, 1/4W, 5%, CC, 150K; 1
21 ; Delete R104
24 ; Delete R3
29 C13, C22, C47

Page 5-4 Make the following changes to the drawing:
Add a variable resistor between R3 and U6 at the top left of the drawing.
Add a capacitor C46 below U29 at the lower left of the drawing.
Add a capacitor C47 next to R83 and beneath R82 at the bottom center of the drawing.

Page 5-5 Change the following items on the parts list:
9; CR1 - CR6; 203323; Diode 1N4448; 6
10; R2, R3, R5, R6, R8, R9; 147975; Resistor, 1/4W, 5%, CC, 390; 6
Delete R4, R7 and R10 from the reference designator drawing, then delete the corresponding item 10 locations on the parts list drawing.

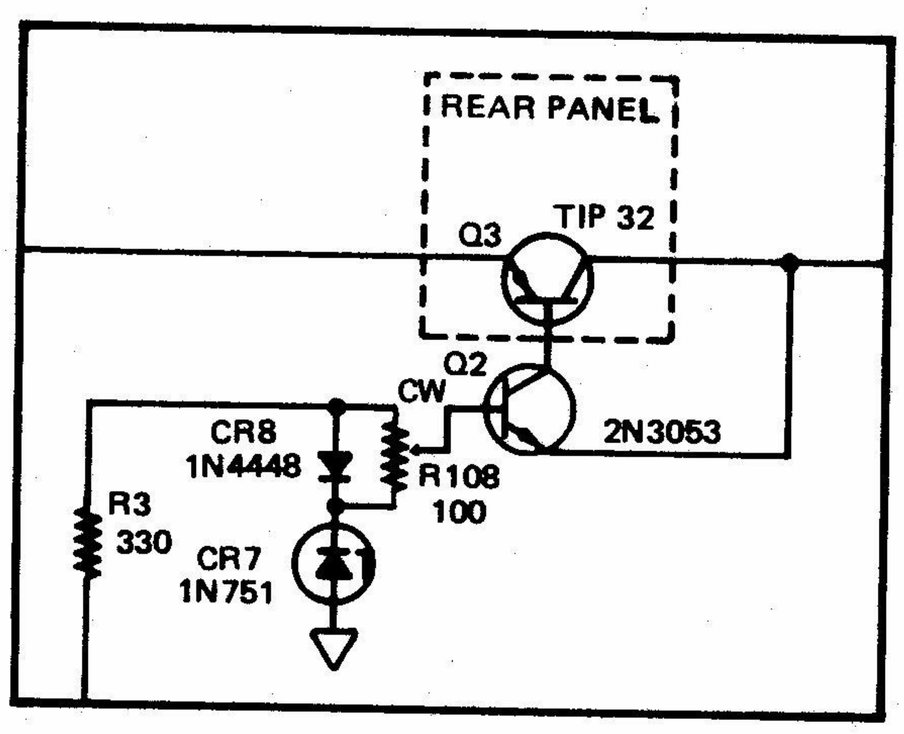
Page 5-6 Change the following items on the parts list:
9; C1, C3; 441345; Capacitor; 1 μF 250V; 2
14; ; 433334; Coaxial Cable Assy; 2

Page 5-7 Change the following items on the parts list:
11; CR1, CR2; 203323; 1N4448; 2

Page 5-8 Change the following items on the parts list:
2; ; 372565; 1952B-4002 Display PCB Assy; 1
3; ; 372581; 1952B-4003 Switch PCB Assy; 1
11; ; 449355; Pointer, Knob; 2
14; ; 312538; Washer; 1
28; ; 115808; Wire #22 Violet X 7" L; 1
33; ; 115790; Wire #22 Orange X2" L; 2
37; ; 175976; Sleeving 1"; 2
38; ; 110510; Nut 3/8 - 32 Hex; 2
Make the following changes to the parts drawing:
Delete the present item 33.
Change the unnumbered single pole switch on the rear panel to a double pole.
Connect one orange wire (new item 33) between the bottom connector added to the switch, and a connection point on the main pcb directly behind the black wire from the other side of the switch. Connect the second orange wire from the new center connector on the switch to a point on the pcb to the left of the other new wire.

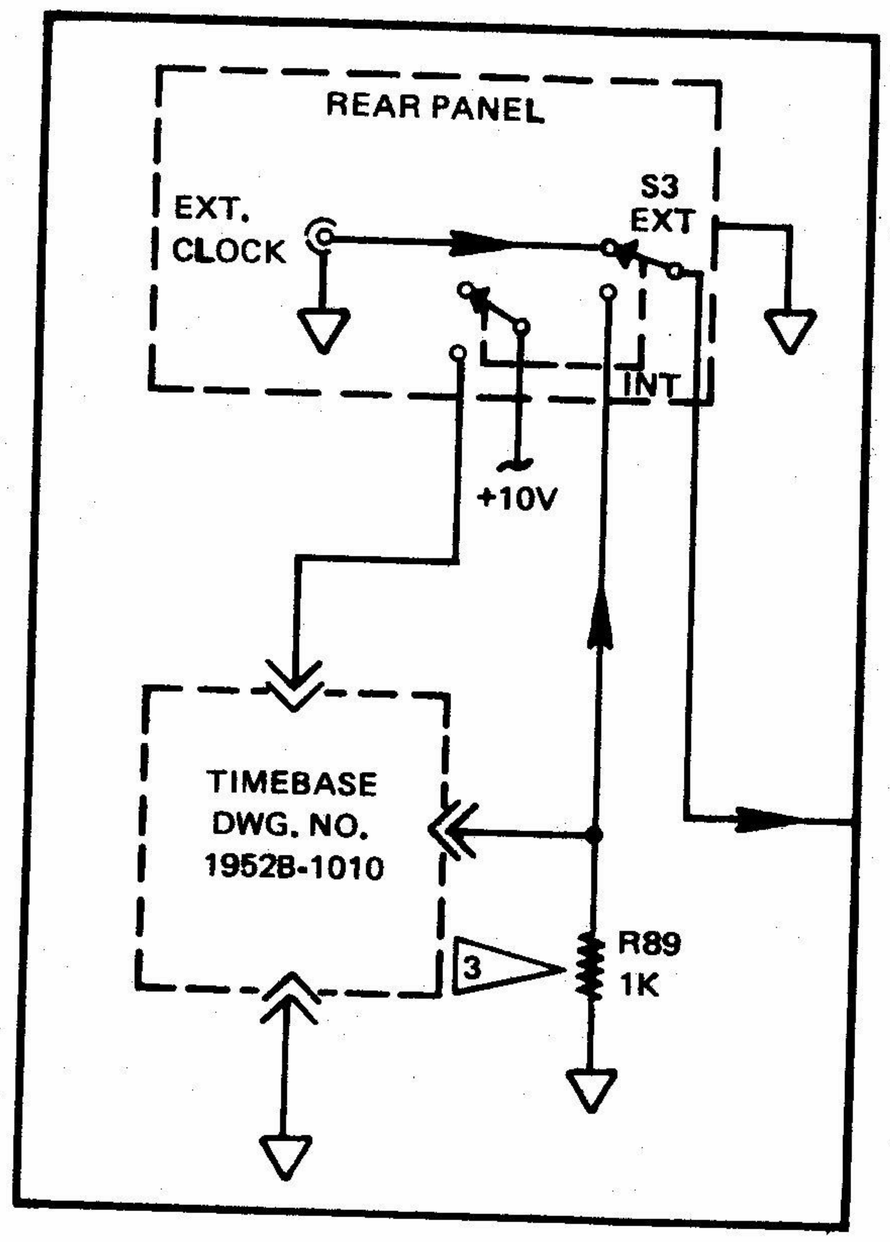
Page 5-9 Change the following items on the parts list:
3; ; 152033; BNC Jacks; 2
9; ; 110668; Nut #2-56; 2
10; S3; 370122; Slide Switch; 1
14; ; 372441; Rear Panel 1952B-2013; 1

Page S-1 Redraw the input circuit to Q2 as shown below.

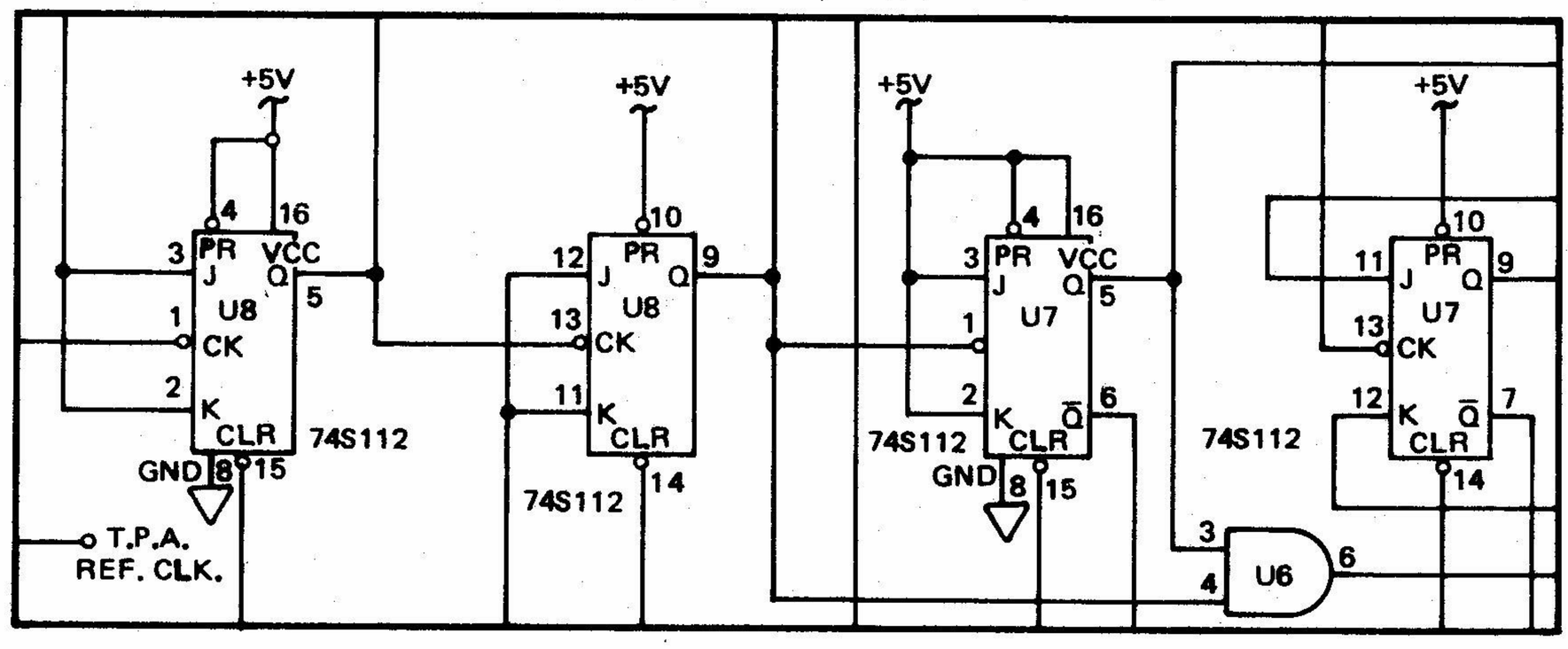


Page S-3 Change the value of R81 and R84 in the upper right quadrant to 3K3
Drop a .01 μF capacitor C47 to ground from the left side of R83 in the upper right quadrant.

Page S-4 Add the second pole to S3 as shown below:



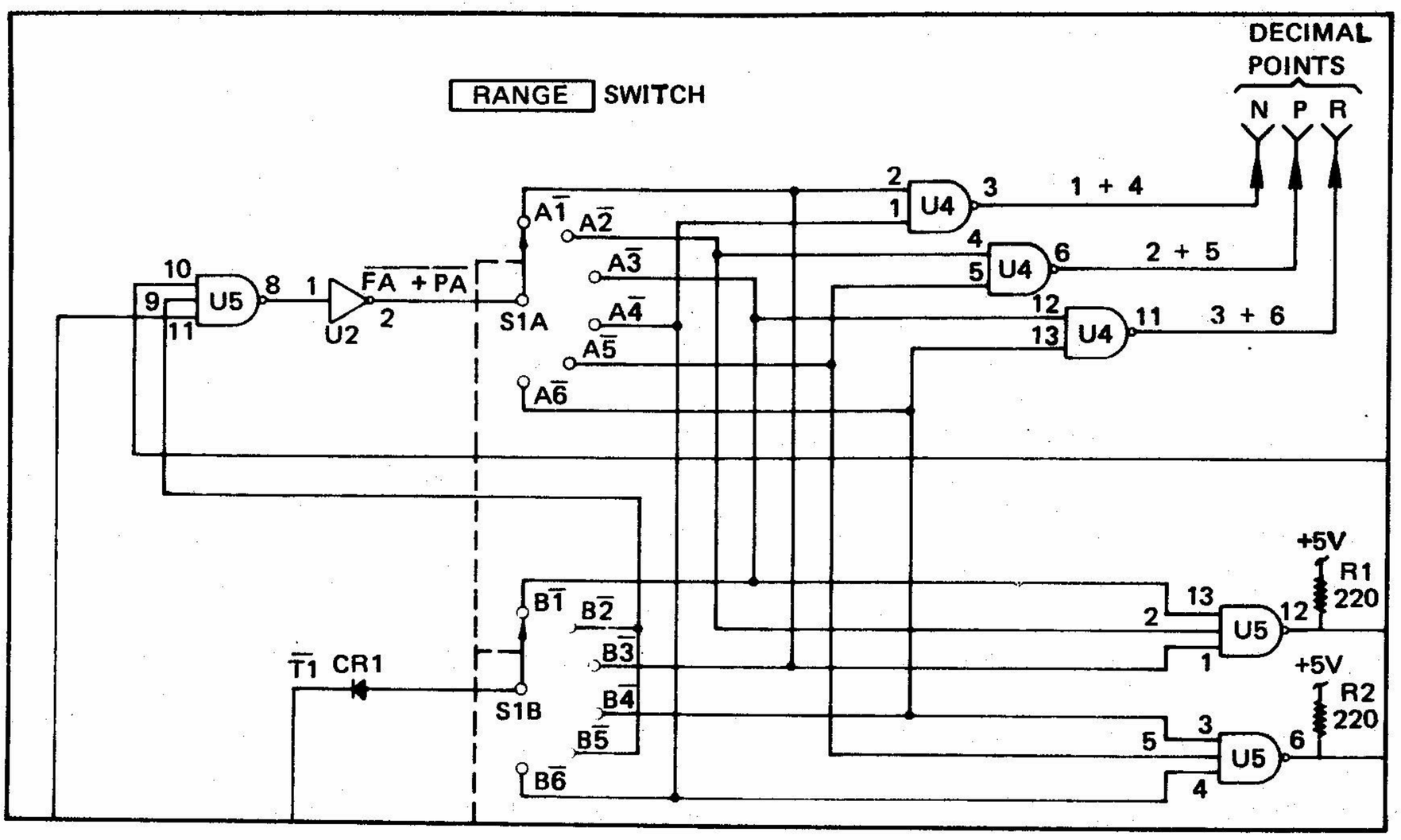
Add the connections to U7 and U8 as shown below:



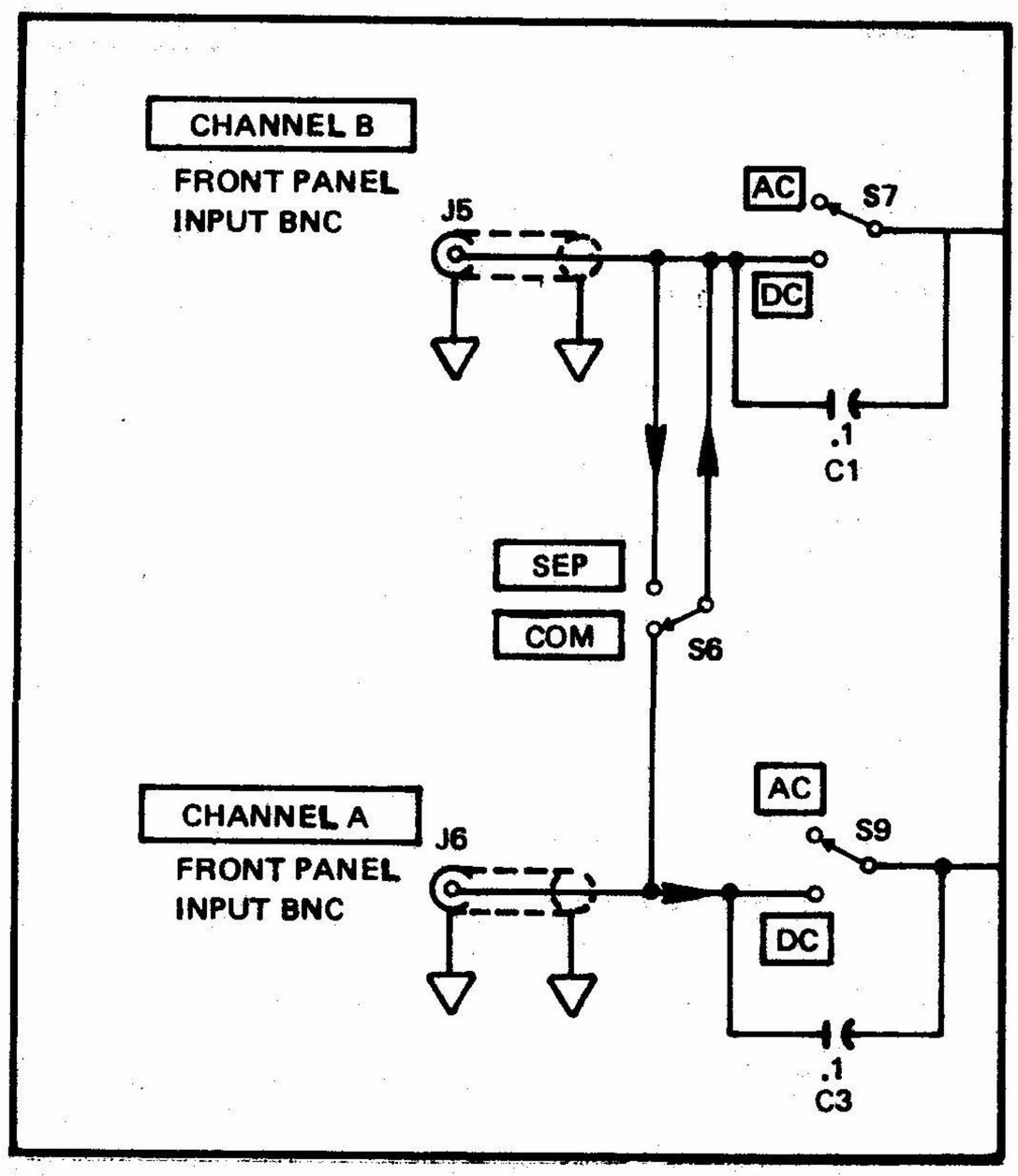
Page S-5 Change the value of R103 in the upper right quadrant from 220K to 180K.
Change the value of R104 in the upper right quadrant from 820 to 2K7.
Add a 43 pF capacitor C46 from the line connecting U30-3/U29-1 to ground.

Page S-6 Delete the three 390Ω resistors R4, R7 and R10 that are connected between ground and U4-13, U3-13 and U2-13 respectively.
Change the value of the cycle rate adjustment pot R14 from 10K to 25K.

Page S-7 Correct the range switch inputs as shown below.



Page S-8 Change the SEP-COM switch connections to those shown below:



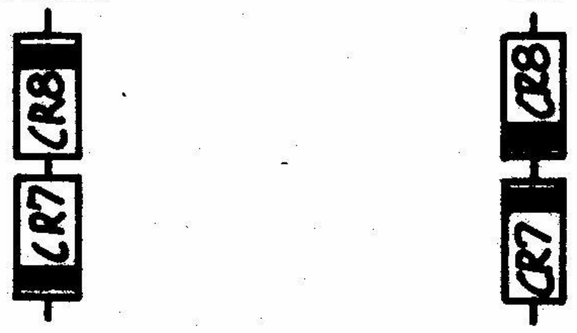
CHANGE #1–10972

On page 5-5, Display Board PCB Assembly, change part number of S4 from: 369421 to 380113.

CHANGE #2–10973

On page 5-4, Main Board PC Assembly, change position of cathode band on diodes CR7 and CR8 as shown below:

From: To:



CHANGE #3

On page 5-3, Main Board PC Assembly, change part number and nomenclature of item 39 from: 369140, Capacitor .001 uF to: 106732, Capacitor .0012 uF.

On page S-1, Main PCB Power Supply Schematic, change value of capacitors C6 and C7 from: .001 to: .0012,

On page S-3, Main PCB Schematic, change value of capacitors C29 and C31 from: .001 to: .0012.

On page S-4, Main PCB Schematic, change value of capacitors C32, C34 and C35 from: 001 to: .0012.

On page S-5, Main PCB Schematic, change value of capacitor C44 from: .001 to: .0012.

2B

CHANGE #4–11318

On page 5-3, make the following changes to the parts list:
On item 54, delete R103 and change quantity from 2 to 1.
Add the following new entry:
R103; 160937; 220k ±5%, 1/4W

CHANGE #5–11339

On page 5-3, make the following changes to the parts list:
On item 55, change quantity from 4 to 3.
On flag note 23, delete R83 from entry.
Add the following new entry:
R83; 325704; 130 ±5%, 1/4W.

On page 5-5, delete the existing R1 entry (49484, 82Ω) and replace it with the following:
R1; 325704; Resistor, 1/4W, 5%cc, 130;1.

On page 5-6, delete the existing R7–R10 entry (49484, 82Ω) and replace it with the following:
R7–R10; 325704; Resistor, 1/4W, 5%cc, 130; 1.

On page S-3, change the value of R83 from 68Ω to 130Ω.

On page S-6, change the value of R1 from 82Ωto 130Ω. Change R14 from 10 kΩ to 25 kΩ.

On page S-8, change the value of resistors R7 through R10 from 82Ω to 130Ω.

CHANGE #6 – 11971

Rev.-M, Main Board PC Assembly (1952B-4001)

On page 5-3, make the following changes:

DELETE: 72/Q4/370155/Heatsink/1

ERRATA #1

On page 4-8, paragraph 4-35, make the following changes:

ADD: a.1 Change the CHANNEL B slope to – (minus)

Change Step C, FROM: Connect a 1V rms, 100 Hz signal . . .
TO: Connect a 1V squarewave, 10 Hz signal . . .

Change Step D, FROM: . . .of 0.1000 kHz TO: . . .of 0.0100 kHz

On page 4-9, make the following changes:

On Table 4-6, change the Numeric Readout Column

FROM:	TO:
10.0000	50.0000
10.000	50.000
10.00	50.00
.0100	.0500
.010	.050
.01	.05

FROM: *The accuracy of the readout is dependent on the frequency accuracy of the 100 Hz signal source.

TO: *The accuracy of the readout is dependent on the frequency accuracy of the 10 Hz signal source, the symetry of the waveform and the Time Interval Accuracy Specification. (See section one of this manual).